# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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# Contact us

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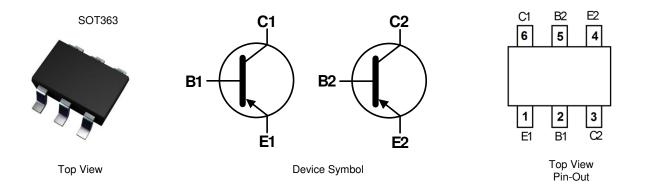
65V DUAL PNP SURFACE MOUNT SMALL SIGNAL TRANSISTOR IN SOT363

#### Features

- BV<sub>CEO</sub> > -65V
- I<sub>C</sub> = -100mA High Collector Current
- Complementary NPN Types Available (BC846AS)
- Ideally Suited for Automatic Insertion
- For Switching and AF Amplifier Applications
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Finish. Solderable per MIL-STD-202, Method 208 © 3
- Weight: 0.006 grams (Approximate)



#### Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
BC856AS-7	AEC-Q101	KPS	7	8	3,000

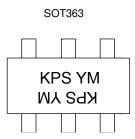
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

#### **Marking Information**



KPS = Product Type Marking Code YM = Date Code Marking Y = Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code Key

Notes:

Year	2017		2018	2019		2020	2021		2022	2023		2024
Code	E		F	G		Н			J	K		L
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



#### Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Collector-Base Voltage		V <sub>CBO</sub>	-80	V	
Collector-Emitter Voltage		V <sub>CEO</sub>	-65	V	
Emitter-Base Voltage		V <sub>EBO</sub>	-5.0	V	
Collector Current		Ι <sub>C</sub>	-100	mA	
Peak Collector Current		I <sub>CM</sub>	-200	mA	
Peak Emitter Current		I <sub>EM</sub>	-200	mA	
Power Dissipation	(Note 5)	PD	200	mW	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{ heta JA}$	625	°C/W	
Operating and Storage Temperature Range		TJ, T <sub>STG</sub>	-65 to +150	°C	

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	200	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	R <sub>0JA</sub>	625	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-55 to +150	°C

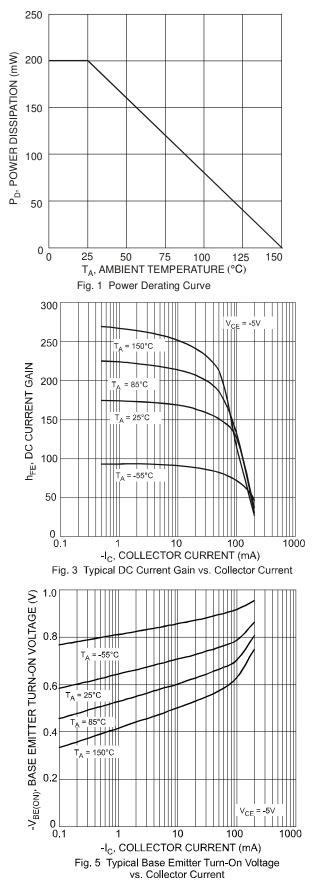
#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

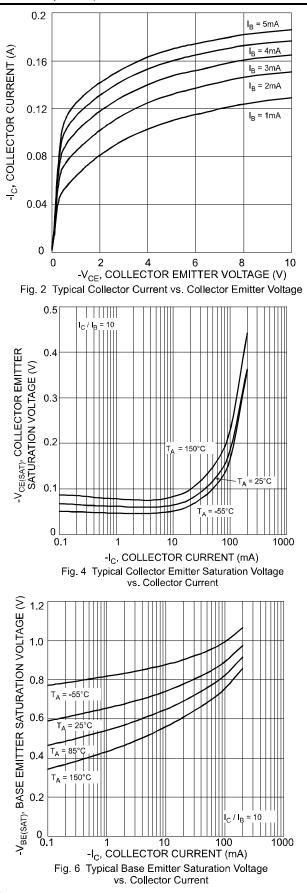
Characteristic (Note 6)	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-80	_	_	V	I <sub>C</sub> = 10μA
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	-65		_	V	$I_{C} = 10 \text{mA}$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5	—	—	V	$I_E = 1 \mu A$
DC Current Gain	h <sub>FE</sub>	125	180	250	_	$V_{CE} = -5.0V, I_{C} = -2.0mA$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		-75 -250	-300 -650	mV	$I_{C} = -10mA$ , $I_{B} = -0.5mA$ $I_{C} = -100mA$ , $I_{B} = -5.0mA$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>		-700 -850		mV	$I_{C} = -10mA$ , $I_{B} = -0.5mA$ $I_{C} = -100mA$ , $I_{B} = -5.0mA$
Base-Emitter Voltage	V <sub>BE(ON)</sub>	-600	-650 —	-750 -820	mV	$V_{CE} = -5.0V, I_{C} = -2.0mA$ $V_{CE} = -5.0V, I_{C} = -10mA$
Collector-Cutoff Current	I <sub>CES</sub> I <sub>CBO</sub> I <sub>CBO</sub>			-15 -15 -4.0	nA nA μA	$V_{CE} = -80V$ $V_{CB} = -30V$ $V_{CB} = -30V$ , $T_A = +150^{\circ}C$
Gain Bandwidth Product	fT	100	—	—	MHz	V <sub>CE</sub> = -5.0V, I <sub>C</sub> = -10mA, f = 100MHz
Collector-Base Capacitance	C <sub>CB</sub>	_	3	_	рF	V <sub>CB</sub> = -10V, f = 1.0MHz

 For the device mounted on minimum recommended pad layout FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
Short duration pulse test used to minimize self-heating effect. Notes:

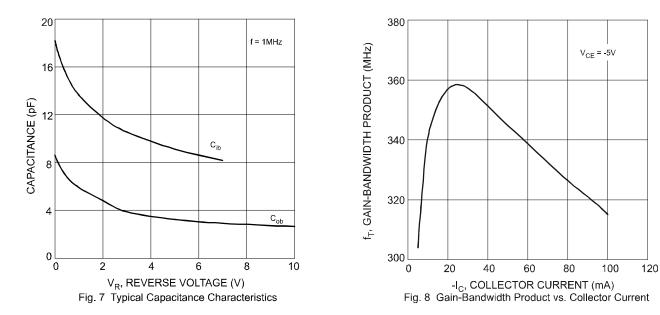


#### Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)





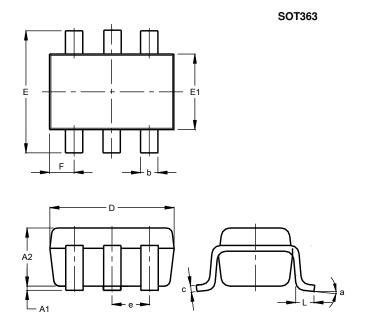






## Package Outline Dimensions

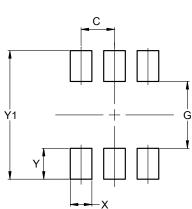
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	1.00				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
E	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0	).650 E	SC				
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All	All Dimensions in mm						

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT363

Dimensions	Value (in mm)			
С	0.650			
G	1.300			
Х	0.420			
Y	0.600			
Y1	2.500			

BC856AS Document number: DS30834 Rev. 9 - 2



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