imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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General Purpose Transistor

PNP Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-416/SC-75 which is designed for low power surface mount applications.

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- This is a Pb–Free Device

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V _{CEO}	-45	V
Collector-Base Voltage	V _{CBO}	-50	V
Emitter-Base Voltage	V _{EBO}	-5.0	V
Collector Current – Continuous	۱ _C	-100	mAdc
Collector Current – Peak	Ι _C	-200	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^{\circ}C$ Derated above 25°C	P _D	200 1.6	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	R _{θJA}	600	°C/W
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^{\circ}C$ Derated above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	400	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

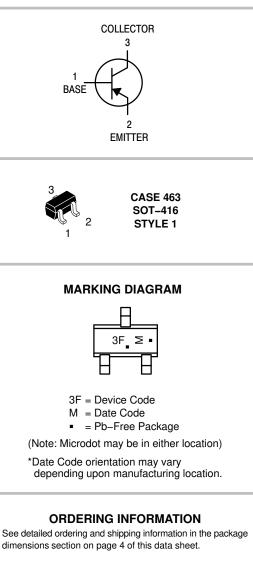
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

2. FR-4 @ 1.0 × 1.0 in pad.



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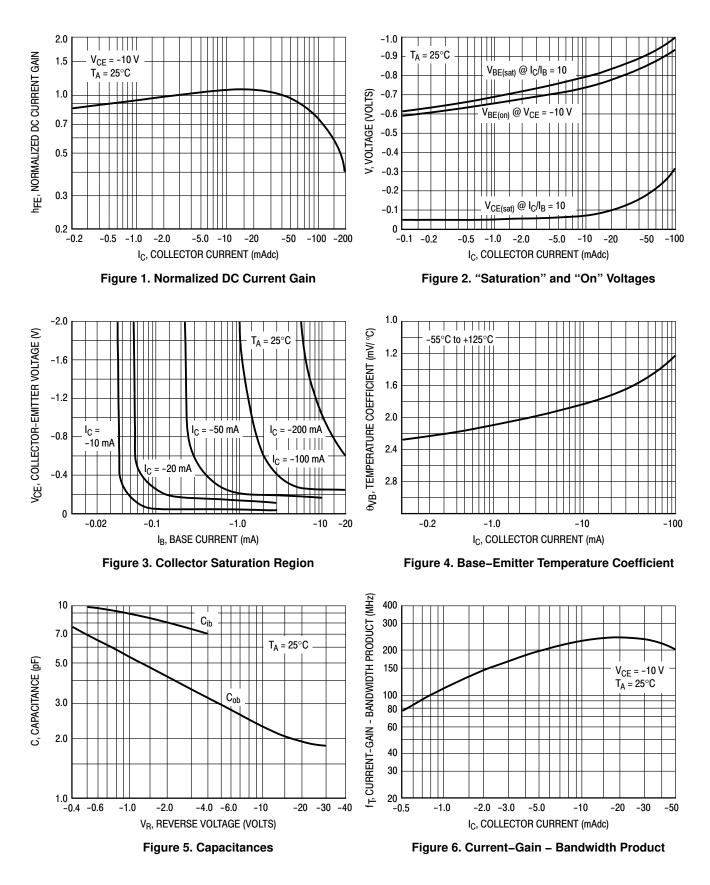


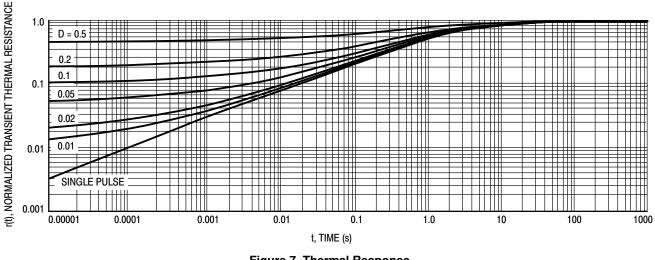
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS				•	
Collector – Emitter Breakdown Voltage $(I_{C} = -10 \text{ mA})$	V _{(BR)CEO}	-45	_	-	V
Collector – Emitter Breakdown Voltage $(I_C = -10 \ \mu A, \ V_{EB} = 0)$	V _{(BR)CES}	-50	_	-	V
Collector – Base Breakdown Voltage $(I_C = -10 \ \mu A)$	V _{(BR)CBO}	-50	_	_	V
Emitter – Base Breakdown Voltage $(I_E = -1.0 \ \mu A)$	V _{(BR)EBO}	-5.0	_	_	V
Collector Cutoff Current (V _{CB} = -30 V) (V _{CB} = -30 V, T _A = 150° C)	I _{CBO}	-		-15 -4.0	nA μA
ON CHARACTERISTICS	·				•
DC Current Gain ($I_C = -10 \ \mu A, V_{CE} = -5.0 \ V$) ($I_C = -2.0 \ mA, V_{CE} = -5.0 \ V$)	h _{FE}	_ 220	150 290	_ 475	_
Collector – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)	V _{CE(sat)}			-0.3 -0.65	V
Base – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)	V _{BE(sat)}		-0.7 -0.9		V
Base – Emitter On Voltage ($I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$) ($I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ V}$)	V _{BE(on)}	-0.6 -		-0.75 -0.82	V
SMALL-SIGNAL CHARACTERISTICS	·				
Current-Gain – Bandwidth Product ($I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, f = 100 MHz)	fT	100	-	-	MHz
Output Capacitance ($V_{CB} = -10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$)	C _{ob}	-	_	4.5	pF
Noise Figure (I _C = -0.2 mA, V _{CE} = -5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	_	-	10	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS







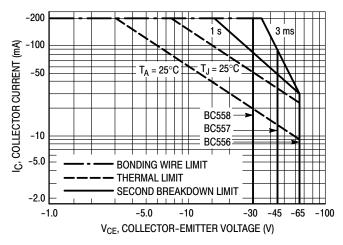


Figure 8. Active Region Safe Operating Area

The safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 8 is based upon $T_{J(pk)} = 150^{\circ}C$; T_C or T_A is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}C$. $T_{J(pk)}$ may be calculated from the data in Figure 7. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

ORDERING INFORMATION

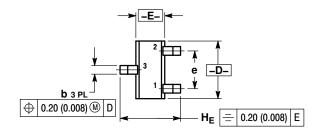
Device	Package	Shipping [†]		
BC857BTT1G	SOT-416	3,000 / Tape & Reel		
NSVBC857BTT1G*	(PB-Free)	3,000 / Tape & Reel		

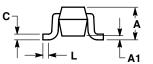
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable.

PACKAGE DIMENSIONS

SOT-416 (SC-75) CASE 463 ISSUE F





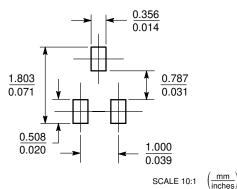
NOTES:

 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		ILLIMETERS INCHES			;
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
С	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.059	0.063	0.067
E	0.70	0.80	0.90	0.027	0.031	0.035
е	1.00 BSC			0.04 BSC		
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.061	0.063	0.065

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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