imall

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Preferred Devices

General Purpose Transistors

PNP Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SC-70/SOT-323 which is designed for low power surface mount applications.

Features

• Pb–Free Packages are Available

Rating		Symbol	Value	Unit		
Collector-Emitter Voltage	BC856 BC857 BC858	V _{CEO}	-65 -45 -30	V		
Collector-Base Voltage	BC856 BC857 BC858	V _{CBO}	-80 -50 -30	V		
Emitter-Base Voltage		V _{EBO}	-5.0	V		
Collector Current – Continuous		Ι _C	-100	mAdc		

MAXIMUM RATINGS ($T_A = 25^{\circ}C$ unless otherwise noted)

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) $T_A = 25^{\circ}C$	PD	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	883	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

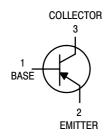
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. $FR-5 = 1.0 \times 0.75 \times 0.062$ in.



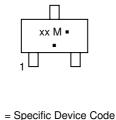
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



xx = Specific Device Code
M = Date Code*
Pb-Free Package
(Note: Microdot may be in either location)
*Date Code orientation may vary depending

upon manufacturing location.

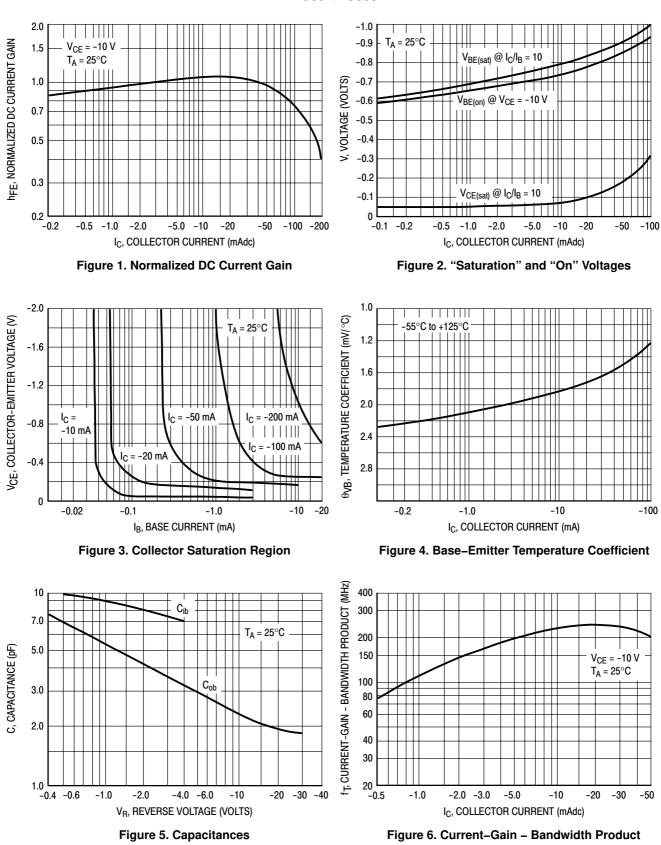
ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

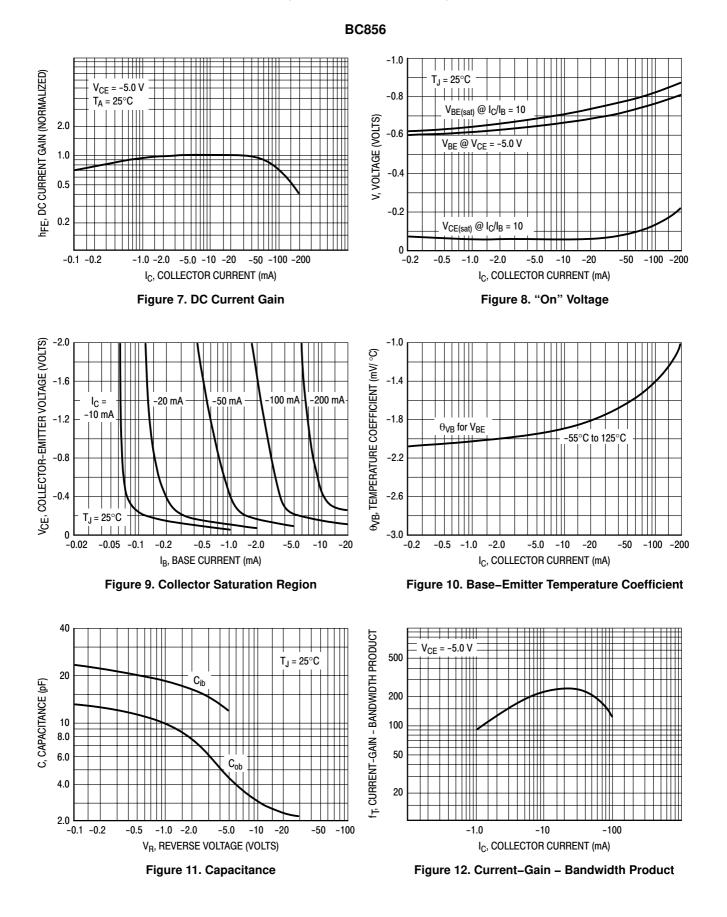
Preferred devices are recommended choices for future use and best overall value.

Characteristic	Symbol	Min	Тур	Мах	Unit	
OFF CHARACTERISTICS		L		L		
(I _C = -10 mA) BC	2856 Series 2857 Series 2858 Series	V _{(BR)CEO}	-65 -45 -30	- - -	- - -	V
$(I_{C} = -10 \ \mu A, V_{EB} = 0)$ BC	856 Series 857B Only 858 Series	V _{(BR)CES}	-80 -50 -30	- - -	- - -	V
$(I_{\rm C} = -10 \ \mu {\rm A})$ BC	856 Series 857 Series 858 Series	V _{(BR)CBO}	-80 -50 -30	- - -	- - -	V
(I _E = -1.0 μA) BC	2856 Series 2857 Series 2858 Series	V _{(BR)EBO}	-5.0 -5.0 -5.0	- - -	- - -	V
Collector Cutoff Current (V _{CB} = -30 V) (V _{CB} = -30 V, T _A = 150° C	Ісво	-		-15 -4.0	nA μA	
ON CHARACTERISTICS		•				
		h _{FE}		90 150 270	- - -	-
$ (I_C = -2.0 \text{ mA}, \text{V}_{CE} = -5.0 \text{ V}) \\ BC856A, \text{BC8} \\ BC856B, \text{BC8} \\ BC857C \\ BC857C \\ BC857C \\ BC857C \\ BC857C \\ BC857C \\ BC857C \\ BC857C \\ BC857C \\ $			125 220 420	180 290 520	250 475 800	
Collector – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)	V _{CE(sat)}	-		-0.3 -0.65	V	
Base – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)		V _{BE(sat)}		-0.7 -0.9		V
Base – Emitter On Voltage ($I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$) ($I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ V}$)	$V_{BE(on)}$	-0.6 -	-	-0.75 -0.82	V	
SMALL-SIGNAL CHARACTERISTICS						
Current – Gain – Bandwidth Product ($I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, f = 100 MHz)	f _T	100	-	-	MHz	
Output Capacitance (V _{CB} = -10 V, f = 1.0 MHz)		C _{ob}	_	-	4.5	pF
Noise Figure (I _C = -0.2 mA, V _{CE} = -5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	-	-	10	dB	

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)



BC857/BC858



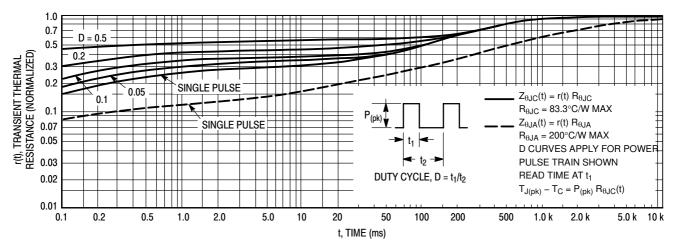


Figure 13. Thermal Response

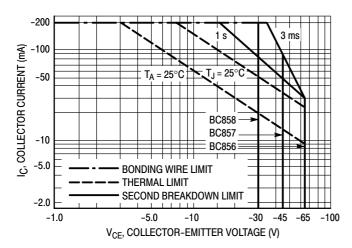


Figure 14. Active Region Safe Operating Area

The safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation. Collector load lines for specific circuits must fall below the limits indicated by the applicable curve.

The data of Figure 14 is based upon $T_{J(pk)} = 150^{\circ}$ C; T_{C} or T_{A} is variable depending upon conditions. Pulse curves are valid for duty cycles to 10% provided $T_{J(pk)} \le 150^{\circ}$ C. $T_{J(pk)}$ may be calculated from the data in Figure 13. At high case or ambient temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by the secondary breakdown.

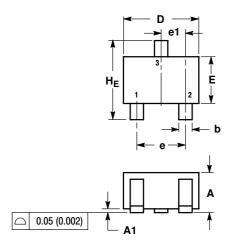
Device	Marking	Package	Shipping [†]	
BC856BWT1		SC-70/SOT-323		
BC856BWT1G	3В	SC-70/SOT-323 (Pb-Free)	3,000 / Tape & Reel	
BC857BWT1		SC-70/SOT-323		
BC857BWT1G	3F	SC-70/SOT-323 (Pb-Free)	3,000 / Tape & Reel	
BC857CWT1		SC-70/SOT-323		
BC857CWT1G	3G	SC-70/SOT-323 (Pb-Free)	3,000 / Tape & Reel	
BC858AWT1		SC-70/SOT-323		
BC858AWT1G	3J	SC-70/SOT-323 (Pb-Free)	3,000 / Tape & Reel	
BC858BWT1		SC-70/SOT-323		
BC858BWT1G	зк	SC-70/SOT-323 (Pb-Free)	3,000 / Tape & Reel	

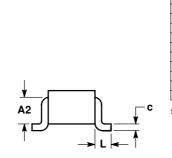
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ORDERING INFORMATION

PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE M





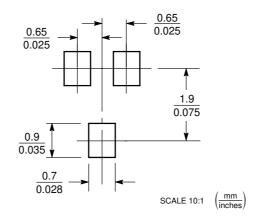
NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI V14 5M 1992

Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.7 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
Е	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.425 REF			0.017 REF		
HE	2.00	2.10	2.40	0.079	0.083	0.095

STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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