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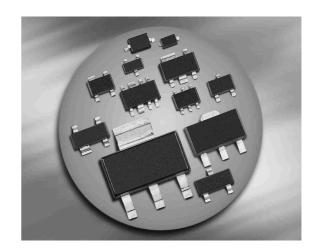


#### **PNP Silicon AF Transistors**

- For AF driver and output stages
- High collector current
- Low collector-emitter saturation voltage
- Complementary types: BCP54 ... BCP56 (NPN)
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101







Туре	Marking	Marking Pin Configuration					Package	
BCP51	*	1=B	2=C	3=E	4=C	-	-	SOT223
BCP51-16	*	1=B	2=C	3=E	4=C	-	-	SOT223
BCP52-16	*	1=B	2=C	3=E	4=C	-	-	SOT223
BCP53-10	*	1=B	2=C	3=E	4=C	-	-	SOT223
BCP53-16	*	1=B	2=C	3=E	4=C	-	_	SOT223

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<sup>\*</sup> Marking is the same as type-name

K/W



**Maximum Ratings** 

Junction - soldering point<sup>1)</sup>

Parameter	Symbol	Value	Unit	
Collector-emitter voltage	$V_{CEO}$		V	
BCP51		45		
BCP52		60		
BCP53		80		
Collector-base voltage	$V_{\mathrm{CBO}}$			
BCP51		45		
BCP52		60		
BCP53		100		
Emitter-base voltage	$V_{EBO}$	5		
Collector current	I <sub>C</sub>	1	Α	
Peak collector current, $t_p \le 10 \text{ ms}$	I <sub>CM</sub>	1.5		
Base current	I <sub>B</sub>	100	mA	
Peak base current	I <sub>BM</sub>	200		
Total power dissipation-	P <sub>tot</sub>	2	W	
<i>T</i> <sub>S</sub> ≤ 120°C				
Junction temperature	$ au_{ m j}$	150	°C	
Storage temperature	$T_{ m stg}$	-65 150		
Thermal Resistance				
Parameter	Symbol	Value	Unit	

 $R_{\mathsf{thJS}}$ 

≤ 15

 $<sup>^{1}</sup>$ For calculation of  $R_{thJA}$  please refer to Application Note AN077 (Thermal Resistance Calculation)



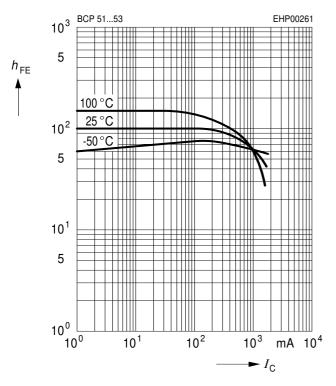
**Electrical Characteristics** at  $T_A = 25$ °C, unless otherwise specified **Symbol Values** Unit **Parameter** min. typ. max. **DC Characteristics**  $V_{(BR)CEO}$ ٧ Collector-emitter breakdown voltage  $I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0 \text{ , BCP51}$ 45  $I_{\rm C}$  = 10 mA,  $I_{\rm B}$  = 0 , BCP52 60 80  $I_{\rm C}$  = 10 mA,  $I_{\rm B}$  = 0 , BCP53 Collector-base breakdown voltage  $V_{(BR)CBO}$  $I_{\rm C} = 100 \, \mu \text{A}, I_{\rm F} = 0 \, , \, \text{BCP51}$ 45  $I_{\rm C} = 100 \, \mu \text{A}, I_{\rm F} = 0 \, , \, \text{BCP52}$ 60  $I_{\rm C} = 100 \, \mu \text{A}, I_{\rm F} = 0 \, , \, \text{BCP53}$ 100 Emitter-base breakdown voltage  $V_{(BR)EBO}$ 5  $I_{\rm E}$  = 10  $\mu$ A,  $I_{\rm C}$  = 0 Collector-base cutoff current μΑ *I*CBO  $V_{CB} = 30 \text{ V}, I_{F} = 0$ 0.1 20  $V_{\text{CB}} = 30 \text{ V}, I_{\text{E}} = 0 , T_{\text{A}} = 150 \text{ }^{\circ}\text{C}$ DC current gain<sup>1)</sup>  $h_{\mathsf{FE}}$  $I_{\rm C}$  = 5 mA,  $V_{\rm CE}$  = 2 V 25  $I_{\rm C}$  = 150 mA,  $V_{\rm CF}$  = 2 V, BCP51 40 250  $I_{\rm C}$  = 150 mA,  $V_{\rm CF}$  = 2 V, BCP53-10 63 100 160  $I_{\rm C}$  = 150 mA,  $V_{\rm CF}$  = 2 V, BCP51-16...BCP53-16 100 160 250  $I_{\rm C}$  = 500 mA,  $V_{\rm CF}$  = 2 V 25 Collector-emitter saturation voltage<sup>1)</sup>  $V_{\mathsf{CEsat}}$ V 0.5  $I_{\rm C}$  = 500 mA,  $I_{\rm B}$  = 50 mA Base-emitter voltage<sup>1)</sup> 1  $V_{\rm BE(ON)}$  $I_{\rm C}$  = 500 mA,  $V_{\rm CE}$  = 2 V **AC Characteristics** 125 MHz Transition frequency  $f_{\mathsf{T}}$  $I_{\rm C}$  = 50 mA,  $V_{\rm CE}$  = 10 V, f = 100 MHz

<sup>&</sup>lt;sup>1</sup>Pulse test: t < 300µs; D < 2%



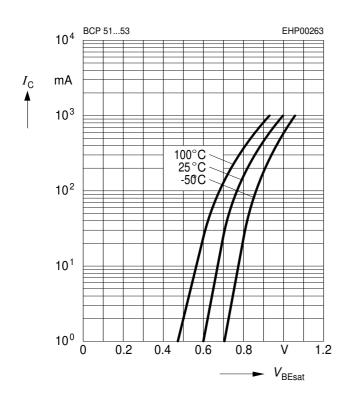
## **DC** current gain $h_{FE} = f(I_C)$

$$V_{CE} = 2 \text{ V}$$



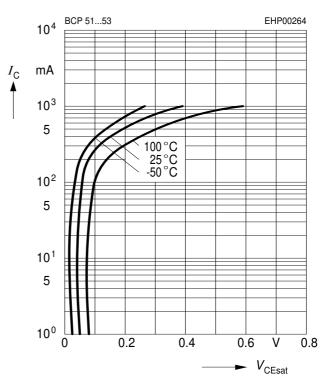
#### **Base-emitter saturation voltage**

$$I_{\rm C} = f(V_{\rm BEsat}), h_{\rm FE} = 10$$



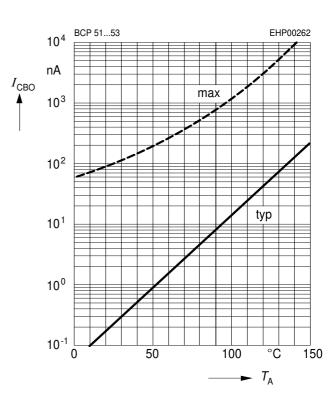
#### Collector-emitter saturation voltage

$$I_{\text{C}} = f(V_{\text{CEsat}}), h_{\text{FE}} = 10$$



## Collector cutoff current $I_{CBO} = f(T_A)$

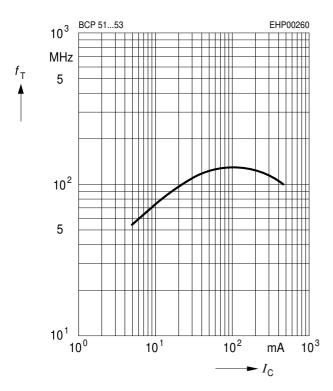
$$V_{\rm CBO}$$
 = 30 V



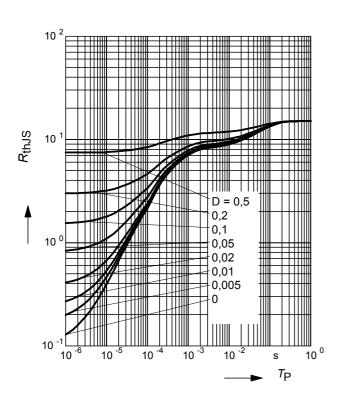


## Transition frequency $f_T = f(I_C)$

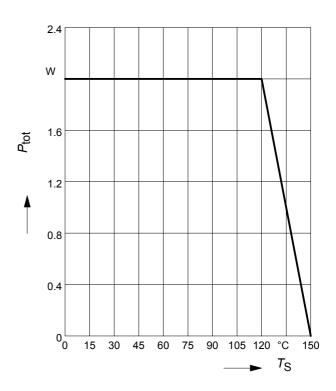
 $V_{CE}$  = 10 V



# Permissible Pulse Load $R_{thJS} = f(t_p)$

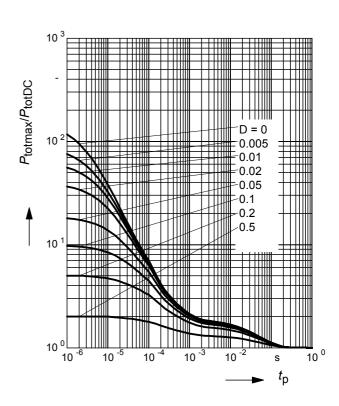


## Total power dissipation $P_{tot} = f(T_S)$



## **Permissible Pulse Load**

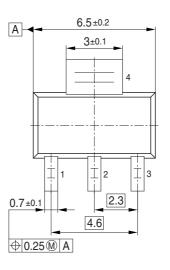
 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{p})$ 

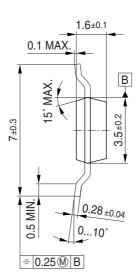




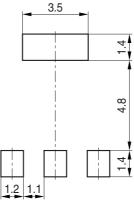
# Package Outline



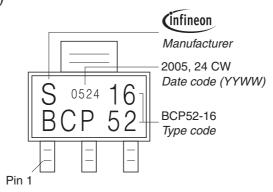




Foot Print

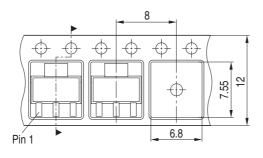


### Marking Layout (Example)



## Packing

Reel ø180 mm = 1.000 Pieces/Reel Reel ø330 mm = 4.000 Pieces/Reel



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