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## PNP Silizium Digital Transistor

- Switching circuit, inverter, interface circuit, driver circuit
- Built in bias resistor ( $R_{1}=4.7 \mathrm{k} \Omega$ )


| Type | Marking | Pin Configuration |  |  | Package |
| :--- | :--- | :--- | :--- | :--- | :--- |
| BCR569 | XLs | $1=\mathrm{B}$ | $2=\mathrm{E}$ | $3=\mathrm{C}$ | SOT23 |

## Maximum Ratings

| Parameter | Symbol | Value | Unit |
| :--- | :--- | :---: | :--- |
| Collector-emitter voltage | $V_{\text {CEO }}$ | 50 | V |
| Collector-base voltage | $V_{\text {CBO }}$ | 50 |  |
| Emitter-base voltage | $V_{\text {EBO }}$ | 5 |  |
| Input on Voltage | $V_{\text {ilon }}$ | 30 |  |
| DC collector current | $I_{\mathrm{C}}$ | 500 | mA |
| Total power dissipation, $T_{\mathrm{S}}=79^{\circ} \mathrm{C}$ | $P_{\text {tot }}$ | 330 | mW |
| Junction temperature | $T_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | $T_{\text {stg }}$ | $-65 \ldots 150$ |  |

Thermal Resistance

| Junction - soldering point ${ }^{1}$ ) | $R_{\text {thJs }}$ | $\leq 215$ | K/W |
| :--- | :--- | :--- | :--- |

[^0]Electrical Characteristics at $T_{\mathrm{A}}=25^{\circ} \mathrm{C}$, unless otherwise specified

| Parameter | Symbol | Values |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | min. | typ. | max. |  |
| DC Characteristics |  |  |  |  |  |
| Collector-emitter breakdown voltage $I_{C}=100 \mu \mathrm{~A}, I_{B}=0$ | $V_{\text {(BR)CEO }}$ | 50 | - | - | V |
| Collector-base breakdown voltage $I_{C}=10 \mu \mathrm{~A}, I_{B}=0$ | $V_{\text {(BR) }}$ CBO | 50 | - | - |  |
| Emitter-base breakdown voltage $I_{E}=10 \mu \mathrm{~A}, I_{C}=0$ | $V_{\text {(BR)EBO }}$ | 5 | - | - | V |
| Collector cutoff current $V_{\mathrm{CB}}=40 \mathrm{~V}, I_{\mathrm{E}}=0$ | ${ }^{\text {CBO }}$ | - | - | 100 | nA |
| DC current gain 1) $I_{\mathrm{C}}=50 \mathrm{~mA}, V_{\mathrm{CE}}=5 \mathrm{~V}$ | $h_{\text {FE }}$ | 120 | - | 630 | - |
| Collector-emitter saturation voltage1) $I_{C}=50 \mathrm{~mA}, I_{\mathrm{B}}=2.5 \mathrm{~mA}$ | $V_{\text {CEsat }}$ | - | - | 0.3 | V |
| Input off voltage $I_{\mathrm{C}}=100 \mu \mathrm{~A}, V_{\mathrm{CE}}=5 \mathrm{~V}$ | $V_{\text {i(off) }}$ | 0.4 | - | 0.8 | V |
| Input on Voltage $I_{\mathrm{C}}=10 \mathrm{~mA}, V_{\mathrm{CE}}=0.3 \mathrm{~V}$ | $V_{\text {i(on) }}$ | 0.5 | - | 1.5 |  |
| Input resistor | $R_{1}$ | 3.2 | 4.7 | 6.2 | $\mathrm{k} \Omega$ |
| AC Characteristics |  |  |  |  |  |
| Transition frequency $I_{\mathrm{C}}=50 \mathrm{~mA}, V_{\mathrm{CE}}=5 \mathrm{~V}, f=100 \mathrm{MHz}$ | $f_{\top}$ | - | 150 | - | MHz |

1) Pulse test: t $<300 \mu \mathrm{~s} ; \mathrm{D}<2 \%$

BCR569

DC Current Gain $h_{\text {FE }}=f\left(l_{\mathrm{C}}\right)$
$V_{\mathrm{CE}}=5 \mathrm{~V}$ (common emitter configuration)


Input on Voltage $V_{\mathrm{i}(\mathrm{on})}=f\left(I_{\mathrm{C}}\right)$
$V_{C E}=0.3 \mathrm{~V}$ (common emitter configuration)


Collector-Emitter Saturation Voltage
$V_{\text {CEsat }}=f\left(l_{\mathrm{C}}\right), h_{\text {FE }}=20$


Input off voltage $V_{\text {i(off) }}=f\left(I_{\mathrm{C}}\right)$
$V_{C E}=5 \mathrm{~V}$ (common emitter configuration)


Total power dissipation $P_{\text {tot }}=f\left(T_{S}\right)$


Permissible Pulse Load $R_{\mathrm{thJS}}=f\left({ }_{\mathrm{p}}\right)$


Permissible Pulse Load
$P_{\text {totmax }} / P_{\text {totDC }}=f\left(t_{\mathrm{p}}\right)$



[^0]:    ${ }^{1}$ For calculation of $R_{\text {thJA }}$ please refer to Application Note Thermal Resistance

