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Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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BCR569

PNP Silizium Digital Transistor

- Switching circuit, inverter, interface circuit, driver circuit
- Built in bias resistor ($R_1 = 4.7 \mathrm{k}\Omega$)





Туре	Marking	Pin Configuration			Package
BCR569	XLs	1 = B	2 = E	3 = C	SOT23

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CEO}	50	V
Collector-base voltage	V _{CBO}	50	
Emitter-base voltage	V _{EBO}	5	
Input on Voltage	V _{i(on)}	30	
DC collector current	I _C	500	mA
Total power dissipation, $T_{\rm S}$ = 79 °C	P _{tot}	330	mW
Junction temperature	Tj	150	°C
Storage temperature	T _{stg}	-65 150	

Thermal Resistance

Junction - soldering point ¹⁾	R _{thJS}	≤ 215	K/W

¹For calculation of R_{thJA} please refer to Application Note Thermal Resistance





Parameter	Symbol	Values			Unit
		min.	typ.	max.	1
DC Characteristics					•
Collector-emitter breakdown voltage	V _{(BR)CEO}	50	-	-	V
$I_{\rm C} = 100 \ \mu {\rm A}, \ I_{\rm B} = 0$					
Collector-base breakdown voltage	V _{(BR)CBO}		-	-	
$I_{\rm C} = 10 \ \mu {\rm A}, \ I_{\rm B} = 0$					
Emitter-base breakdown voltage	V _{(BR)EBO}		-	-	V
$I_{\rm E} = 10 \ \mu {\rm A}, \ I_{\rm C} = 0$					
Collector cutoff current	I _{CBO}	-	-	100	nA
$V_{\rm CB} = 40 {\rm V}, I_{\rm E} = 0$					
DC current gain 1)	h _{FE}	120	-	630	-
$I_{\rm C} = 50 \text{ mA}, V_{\rm CE} = 5 \text{ V}$					
Collector-emitter saturation voltage1)	V _{CEsat}	-	-	0.3	V
$I_{\rm C} = 50 \text{ mA}, I_{\rm B} = 2.5 \text{ mA}$					
Input off voltage	V _{i(off)}	0.4	-	0.8	V
$I_{\rm C} = 100 \ \mu {\rm A}, \ V_{\rm CE} = 5 \ {\rm V}$					
Input on Voltage	V _{i(on)}	0.5	-	1.5	
$I_{\rm C} = 10 \text{ mA}, V_{\rm CE} = 0.3 \text{ V}$					
Input resistor	R ₁	3.2	4.7	6.2	kΩ
AC Characteristics					
Transition frequency	f _T	-	150	-	MHz
$I_{\rm C} = 50 \text{ mA}, V_{\rm CE} = 5 \text{ V}, f = 100 \text{ MHz}$					

Electrical Characteristics at $T_A=25^{\circ}$ C, unless otherwise specified



DC Current Gain $h_{FE} = f(l_C)$ $V_{CE} = 5V$ (common emitter configuration)



Input on Voltage $V_{i(on)} = f(I_C)$ $V_{CE} = 0.3V$ (common emitter configuration)



Collector-Emitter Saturation Voltage

 $V_{\text{CEsat}} = f(I_{\text{C}}), \ h_{\text{FE}} = 20$



Input off voltage $V_{i(off)} = f(I_C)$ $V_{CE} = 5V$ (common emitter configuration)





Total power dissipation $P_{\text{tot}} = f(T_{\text{S}})$



Permissible Pulse Load $R_{\text{thJS}} = f(t_{p})$

Permissible Pulse Load

 $P_{\text{totmax}} / P_{\text{totDC}} = f(t_p)$





Jul-23-2001