imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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BCR410W

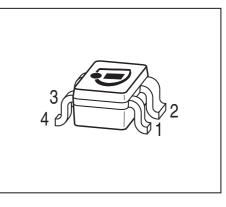
Active Bias Controller

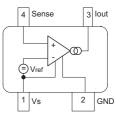
Characteristics

- Supplies stable bias current from 1.8V operating voltage on
- Low voltage drop: 110mV for 10mA collector currrent

Application notes

- Stabilizing bias current of NPN transistors and FET's from 100µA to 20mA
- Ideal supplement for Sieget and other transistors





- Pb-free (RoHS compliant) package¹⁾
- Qualified according AEC Q101



Туре	Marking	Pin Configuration				Package
BCR410W	W8s	1= Vs	2=GND	3=lout	4=Sense	SOT343

Maximum Ratings

Parameter	Symbol	Value	Unit	
Supply voltage	V _S	18	V	
Output current	/ _{out}	0.5	mA	
Total power dissipation, $T_{\rm S}$ = 110 °C	P _{tot}	100	mW	
Junction temperature	T _j	150	°C	
Storage temperature	T _{stg}	-65 150		

Thermal Resistance

Junction - soldering point ²⁾	R _{thJS}	≤ 470	K/W

¹Pb-containing package may be available upon special request

 $^2 \rm For}$ calculation of ${\it R}_{\rm thJA}$ please refer to Application Note Thermal Resistance



Parameter	Symbol	Values			Unit
		min.	typ.	max.	1
DC Characteristics		•		•	•
Additional current consumption	<i>I</i> ₀	-	200	400	μA
$V_{\rm S}$ = 3 V					
DC Characteristics with stabilized NPN-	Transistors	•		•	F
Lowest sufficient battery voltage	V _{Smin}	-	1.8	-	V
Voltage drop	V _{drop}	-	110	-	mV
<i>I</i> _C = 10 mA					
Change of I _C versus h _{FE}	$\Delta I_{\rm C}/I_{\rm C}$	-	tbd	-	Δh_{FE}
h _{FE} = 50					h _{FE}
Change of $I_{\rm C}$ versus $V_{\rm S}$	$\Delta I_{\rm C}/I_{\rm C}$	-	2	-	%/V
$V_{\rm S}$ = 3 V					
Change of $I_{\rm C}$ versus $T_{\rm A}$	$\Delta I_{\rm C}/I_{\rm C}$	-	0.15	-	%/K

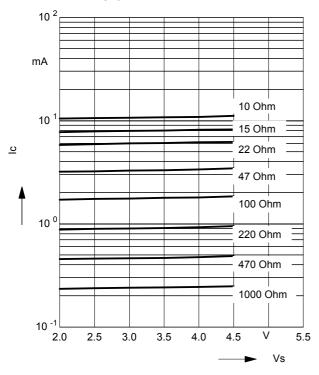
Electrical Characteristics at $T_A = 25^{\circ}C$, unless otherwise specified



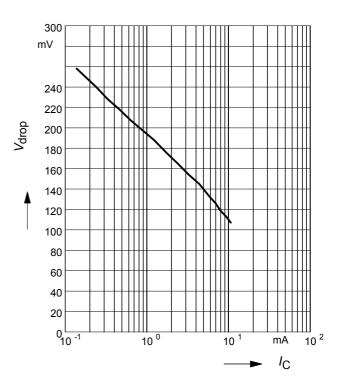
Collector Current $I_{\rm C} = f(V_{\rm S})$

of stabilized NPN Transistor

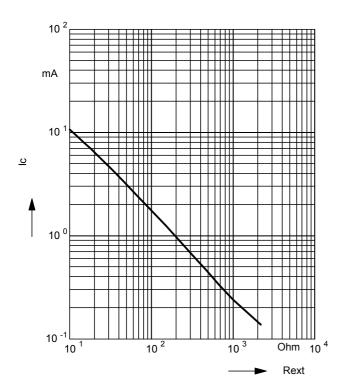
Parameter $R_{ext.}$ (Ω)



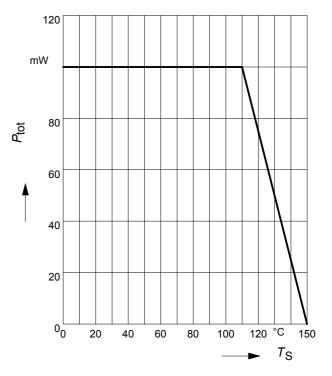
Voltage drop $V_{drop} = f(I_C)$



Collector current $I_{\rm C} = f(R_{\rm ext.})$ of stabilized NPN Transistor

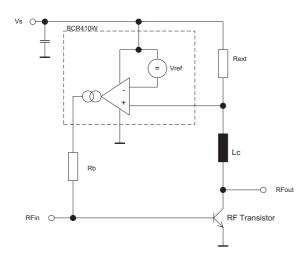


Total power dissipation $P_{tot} = f(T_S)$

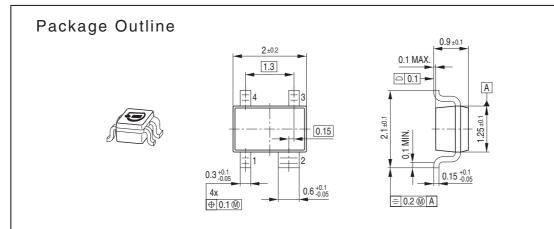




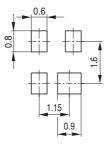
Application Circuit:



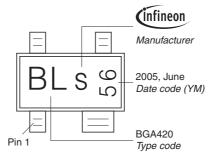




Foot Print

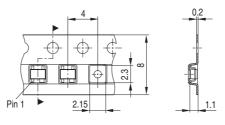


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel





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