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# ne<mark>x</mark>peria

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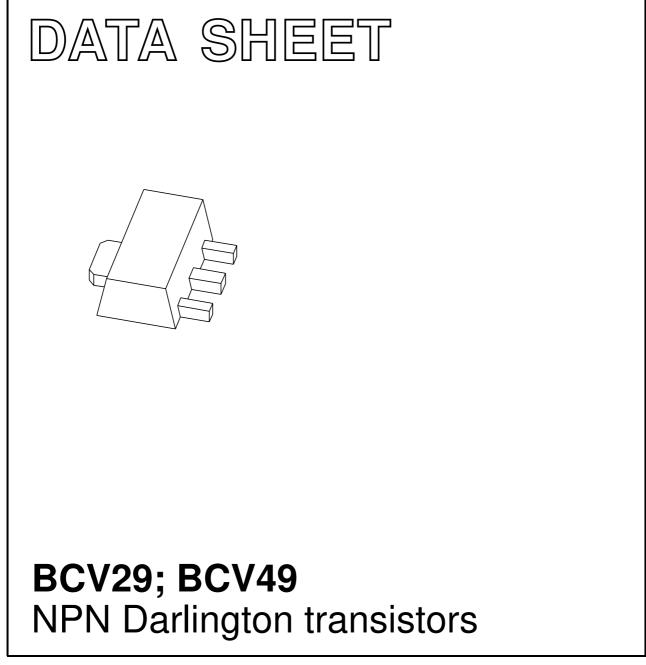
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Kind regards,

Team Nexperia

### DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1999 Apr 08 2004 Dec 06



BCV29; BCV49

### **NPN Darlington transistors**

### FEATURES

- High current (max. 500 mA)
- Low voltage (max. 60 V)
- High DC current gain (min. 20000).

### **APPLICATIONS**

• Preamplifier input applications.

#### DESCRIPTION

NPN small-signal Darlington transistor in a surface mount SOT89 plastic package. PNP complements: BCV28 and BCV48.

#### MARKING

TYPE NUMBER	MARKING CODE
BCV29	EF
BCV49	EG

### **ORDERING INFORMATION**

		PACKAGE			
	NAME	DESCRIPTION	VERSION		
BCV29	SC-62	plastic surface mounted package; collector pad for good heat	SOT89		
BCV49		transfer; 3 leads			

#### PINNING

PIN	DESCRIPTION	
1	emitter	
2	collector	
3	base	

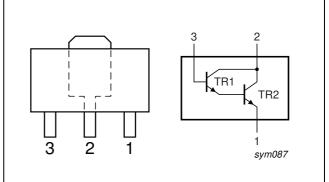


Fig.1 Simplified outline (SOT89) and symbol.

### BCV29; BCV49

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BCV29		-	40	V
	BCV49		-	80	V
V <sub>CES</sub>	collector-emitter voltage	$V_{BE} = 0 V$			
	BCV29		-	30	V
	BCV49		-	60	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	10	V
I <sub>C</sub>	collector current (DC)		-	500	mA
I <sub>CM</sub>	peak collector current		_	1	А
I <sub>BM</sub>	peak base current		-	200	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$ ; note 1	-	1.3	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

#### Note

1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see *"Thermal considerations for SOT89 in the General Part of associated Handbook"*.

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	96	K/W
R <sub>th(j-s)</sub>	thermal resistance from junction to soldering point		16	K/W

Note

<sup>1.</sup> Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 1 cm<sup>2</sup>. For other mounting conditions, see *"Thermal considerations for SOT89 in the General Part of associated Handbook"*.

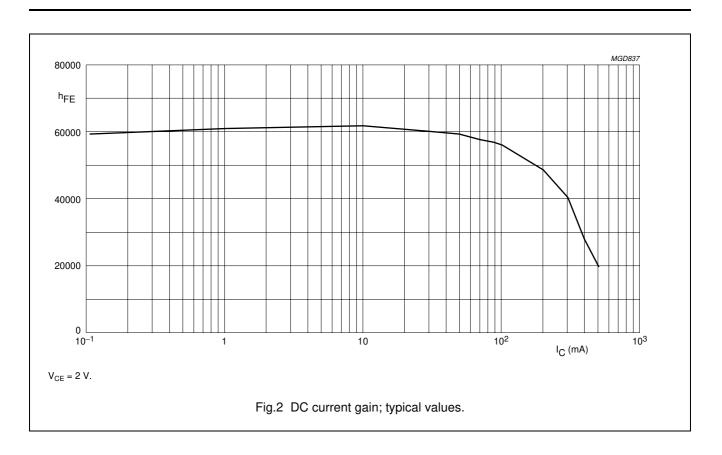
### BCV29; BCV49

### CHARACTERISTICS

 $T_{amb}$  = 25 °C unless otherwise specified.

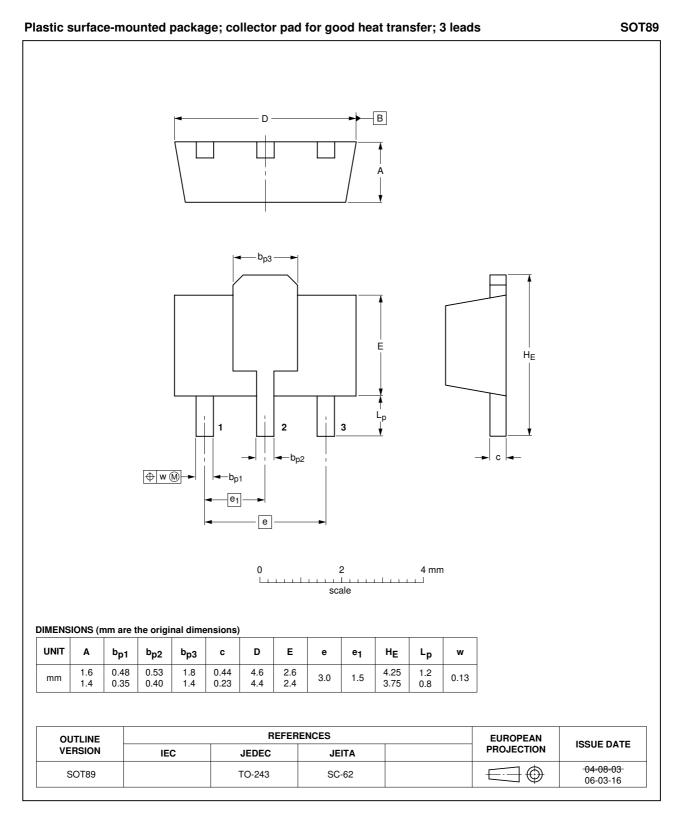
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current					
	BCV29	$I_E = 0 \text{ A}; V_{CB} = 30 \text{ V}$	_	-	100	nA
	BCV49	$I_E = 0 \text{ A}; V_{CB} = 60 \text{ V}$	_	-	100	nA
I <sub>EBO</sub>	emitter-base cut-off current	I <sub>C</sub> = 0 A; V <sub>EB</sub> = 10 V	_	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE} = 5 V$ ; see Fig.2				
	BCV29	$I_{\rm C} = 1  \mathrm{mA}$	4000	-	-	
		I <sub>C</sub> = 10 mA	10000	-	-	
		I <sub>C</sub> = 100 mA	20000	-	-	
		I <sub>C</sub> = 500 mA	4000	-	-	
	DC current gain	V <sub>CE</sub> = 5 V; see Fig.2				
	BCV49	$I_{\rm C} = 1  \mathrm{mA}$	2000	-	-	
		I <sub>C</sub> = 10 mA	4000	-	-	
		I <sub>C</sub> = 100 mA	10000	-	-	
		I <sub>C</sub> = 500 mA	2000	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_{C} = 100 \text{ mA}; I_{B} = 0.1 \text{ mA}$	_	-	1	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_{C} = 100 \text{ mA}; I_{B} = 0.1 \text{ mA}$	_	_	1.5	V
V <sub>BEon</sub>	base-emitter on-state voltage	$I_{C} = 10 \text{ mA}; V_{CE} = 5 \text{ V}$	_	_	1.4	V
f <sub>T</sub>	transition frequency	$I_{C} = 30 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	-	220	-	MHz

### BCV29; BCV49



### BCV29; BCV49

#### PACKAGE OUTLINE



BCV29; BCV49

#### DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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### NXP Semiconductors

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com For sales offices addresses send e-mail to: salesaddresses@nxp.com

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