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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







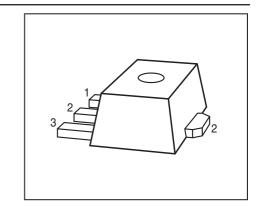


NPN Silicon AF Transistors

- For AF driver and output stages
- High collector current
- Low collctor-emitter saturation voltage
- Complementary types: BCX51...BCX53 (PNP)
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101







Туре	Marking	Pin Configuration		Package	
BCX54-16	BD	1=B	2=C	3=E	SOT89
BCX55	BE	1=B	2=C	3=E	SOT89
BCX55-16	BM	1=B	2=C	3=E	SOT89
BCX56	ВН	1=B	2=C	3=E	SOT89
BCX56-10	BK	1=B	2=C	3=E	SOT89
BCX56-16	BL	1=B	2=C	3=E	SOT89

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K/W



Maximum Ratings

Junction - soldering point¹⁾

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$		-
BCX54		45	
BCX55		60	
BCX56		80	
Collector-base voltage	V _{CBO}		V
BCX54		45	
BCX55		60	
BCX56		100	
Emitter-base voltage	V _{EBO}	5	
Collector current	I _C	1	Α
Peak collector current, $t_p \le 10 \text{ ms}$	I _{CM}	1.5	
Base current	l _B	100	mA
Peak base current	/ _{BM}	200	
Total power dissipation-	P _{tot}	2	W
<i>T</i> _S ≤ 120°C			
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-65 150	
Thermal Resistance			
Parameter	Symbol	Value	Unit

R_{thJS}

≤ 15

 $^{^{1}}$ For calculation of R_{thJA} please refer to Application Note AN077 (Thermal Resistance Calculation)



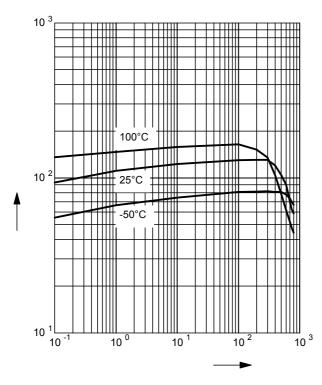
Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified **Symbol Values** Unit **Parameter** min. typ. max. **DC Characteristics** ٧ Collector-emitter breakdown voltage $V_{(BR)CEO}$ $I_{\rm C}$ = 10 mA, $I_{\rm B}$ = 0 , BCX54 45 $I_{\rm C}$ = 10 mA, $I_{\rm B}$ = 0 , BCX55 60 80 $I_{\rm C}$ = 10 mA, $I_{\rm B}$ = 0 , BCX56 Collector-base breakdown voltage $V_{(BR)CBO}$ $I_{\rm C} = 100 \, \mu \text{A}, I_{\rm F} = 0 \, , \, \text{BCX54}$ 45 $I_{\rm C} = 100 \, \mu \text{A}, I_{\rm F} = 0 \, , \, \text{BCX55}$ 60 $I_{\rm C} = 100 \, \mu \text{A}, I_{\rm F} = 0 \, , \, \text{BCX56}$ 100 Emitter-base breakdown voltage $V_{(BR)EBO}$ 5 $I_{\rm E}$ = 10 μ A, $I_{\rm C}$ = 0 Collector-base cutoff current μΑ *I*CBO $V_{CB} = 30 \text{ V}, I_{F} = 0$ 0.1 20 $V_{\text{CB}} = 30 \text{ V}, I_{\text{E}} = 0 , T_{\text{A}} = 150 \text{ }^{\circ}\text{C}$ DC current gain¹⁾ h_{FE} $I_{\rm C} = 5 \text{ mA}, V_{\rm CF} = 2 \text{ V}$ 25 $I_{\rm C}$ = 150 mA, $V_{\rm CF}$ = 2 V, BCX55/BCX56 40 250 $I_{\rm C}$ = 150 mA, $V_{\rm CF}$ = 2 V, BCX55-10/BCX56-10 63 100 160 $I_{\rm C}$ = 150 mA, $V_{\rm CF}$ = 2 V, BCX54-16...BCX56-16 100 160 250 $I_{\rm C}$ = 500 mA, $V_{\rm CF}$ = 2 V 25 Collector-emitter saturation voltage¹⁾ V_{CEsat} V 0.5 $I_{\rm C}$ = 500 mA, $I_{\rm B}$ = 50 mA 1 Base-emitter voltage- $V_{\rm BE(ON)}$ $I_{\rm C}$ = 500 mA, $V_{\rm CE}$ = 2 V **AC Characteristics** 100 MHz Transition frequency f_{T} $I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 10 V, f = 20 MHz

¹Pulse test: t < 300µs; D < 2%



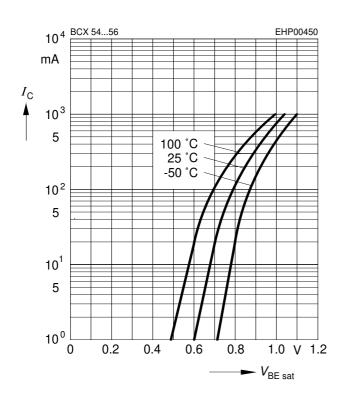
DC current gain $h_{FE} = f(I_C)$

$$V_{CE} = 2 \text{ V}$$



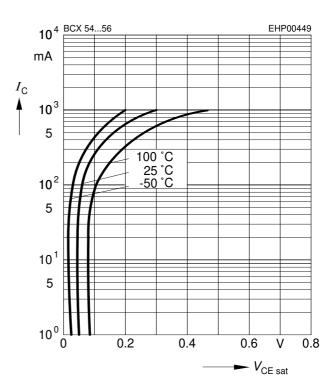
Base-emitter saturation voltage

$$I_{\rm C} = f(V_{\rm BEsat}), h_{\rm FE} = 10$$



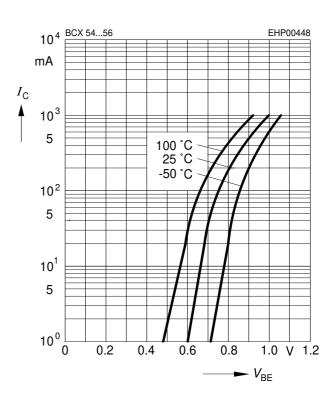
Collector-emitter saturation voltage

$$I_{\text{C}} = f(V_{\text{CEsat}}), h_{\text{FE}} = 10$$



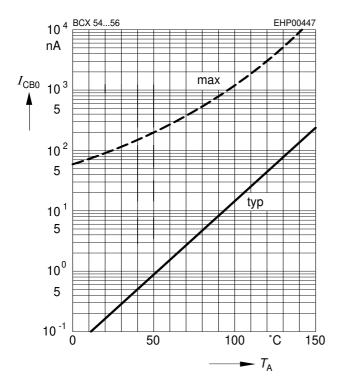
Collector current $I_{C} = f(V_{BE})$

$$V_{CE} = 2V$$

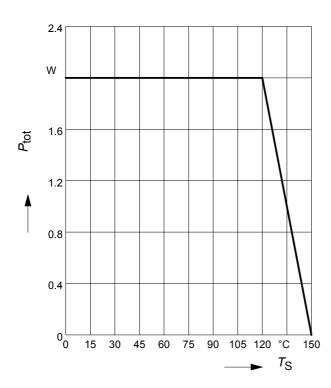




Collector cutoff current $I_{CBO} = f(T_A)$ $V_{CBO} = 30 \text{ V}$

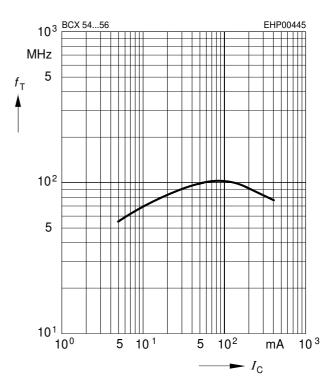


Total power dissipation $P_{tot} = f(T_S)$

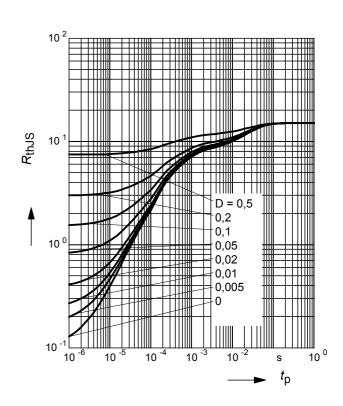


Transition frequency $f_T = f(I_C)$

 V_{CE} = parameter in V, f = 2 GHz



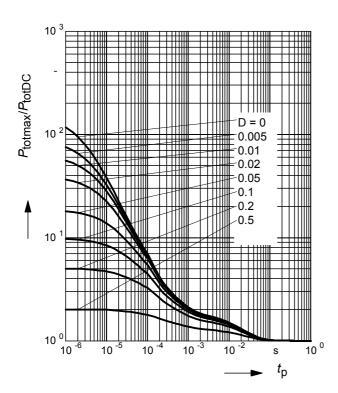
Permissible Pulse Load $R_{thJS} = f(t_p)$





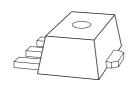
Permissible Pulse Load

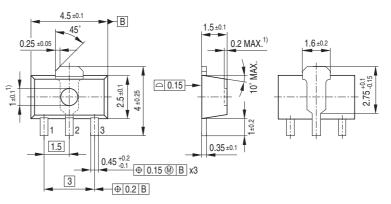
 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$





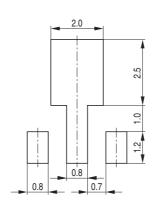
Package Outline



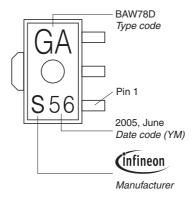


1) Ejector pin markings possible

Foot Print

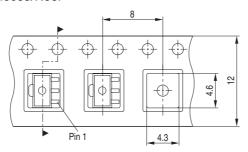


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 1.000 Pieces/Reel Reel ø330 mm = 4.000 Pieces/Reel







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