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# Plastic Medium-Power Silicon NPN Transistor

This device is designed for use in 5.0 to 10 Watt audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

#### **Features**

- High DC Current Gain
- BD179 is complementary with BD180
- These Devices are Pb-Free and are RoHS Compliant\*

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	80	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	80	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector Current	I <sub>C</sub>	3.0	Adc
Base Current	Ι <sub>Β</sub>	1.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	30 240	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### THERMAL CHARACTERISTICS

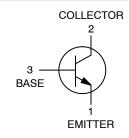
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	4.16	°C/W



#### ON Semiconductor®

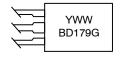
http://onsemi.com

# 3.0 AMPERES POWER TRANSISTORS NPN SILICON 80 VOLTS, 30 WATTS





#### **MARKING DIAGRAM**



Y = Year

WW = Work Week

BD179 = Device Code

G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
BD179G	TO-225 (Pb-Free)	500 Units/Box

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Collector–Emitter Sustaining Voltage (Note 1) (I <sub>C</sub> = 0.1 Adc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	80	-	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 80 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	0.1	mAdc
Emitter Cutoff Current (V <sub>BE</sub> = 5.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	1.0	mAdc
DC Current Gain (I <sub>C</sub> = 0.15 A, V <sub>CE</sub> = 2.0 V) (I <sub>C</sub> = 1.0 A, V <sub>CE</sub> = 2.0 V)	h <sub>FE</sub>	63 15	160 -	-
Collector-Emitter Saturation Voltage (Note 1) (I <sub>C</sub> = 1.0 Adc, I <sub>B</sub> = 0.1 Adc)	V <sub>CE(sat)</sub>	-	0.8	Vdc
Base–Emitter On Voltage (Note 1) ( $I_C = 1.0 \text{ Adc}$ , $V_{CE} = 2.0 \text{ Vdc}$ )	V <sub>BE(on)</sub>	-	1.3	Vdc
Current-Gain - Bandwidth Product (I <sub>C</sub> = 250 mAdc, V <sub>CE</sub> = 10 Vdc, f = 1.0 MHz)	f <sub>T</sub>	3.0	-	MHz

<sup>1.</sup> Pulse Test: Pulse Width ≤ 300 As, Duty Cycle ≤ 2.0%.

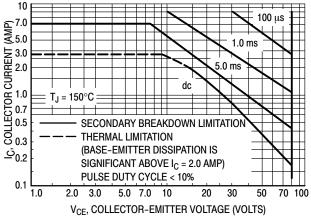


Figure 1. Active Region Safe Operating Area

The Safe Operating Area Curves indicate  $I_C - V_{CE}$  limits below which the device will not enter secondary breakdown. Collector load lines for specific circuits must fall within the applicable Safe Area to avoid causing a catastrophic failure. To insure operation below the maximum  $T_J$ , power–temperature derating must be observed for both steady state and pulse power conditions.

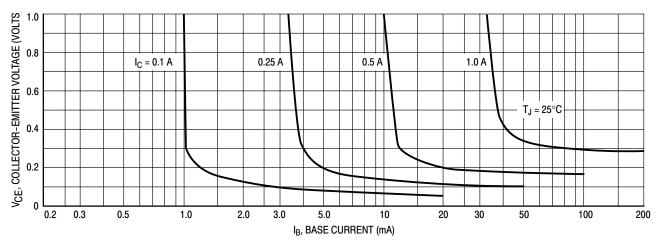
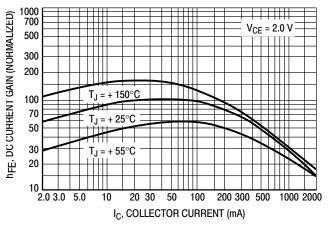


Figure 2. Collector Saturation Region



1.5
1.2
T<sub>J</sub> = 25°C

1.2
V<sub>BE</sub>(sat) @ I<sub>C</sub>/I<sub>B</sub> = 10

V<sub>BE</sub> @ V<sub>CE</sub> = 2.0 V

0.3
V<sub>CE</sub>(sat) @ I<sub>C</sub>/I<sub>B</sub> = 10

2.0 3.0 5.0 10 20 30 50 100 200 300 500 1000 2000 I<sub>C</sub>, COLLECTOR CURRENT (mA)

Figure 3. Current Gain

Figure 4. "On" Voltages

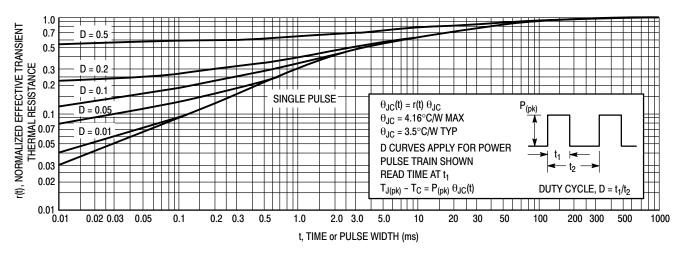
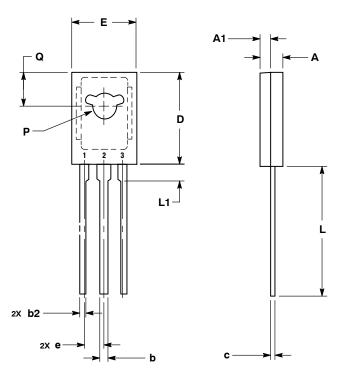


Figure 5. Thermal Response

#### PACKAGE DIMENSIONS

TO-225 CASE 77-09 **ISSUE AB** 



#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. NUMBER AND SHAPE OF LUGS OPTIONAL.

ſ		MILLIMETERS		
-	DIM	MIN	MAX	
	Α	2.40	3.00	
	A1	1.00	1.50	
	b	0.60	0.90	
L	b2	0.51	0.88	
	C	0.39	0.63	
	D	10.60	11.10	
	Е	7.40	7.80	
	е	2.04	2.54	
	L	14.50	16.63	
	L1	1.27	2.54	
	Р	2.90	3.30	
	Q	3.80	4.20	

STYLE 1:

PIN 1. EMITTER COLLECTOR

BASE

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