imall

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2.7V to 5.5V 130µs (Typ.)

13.5V(Typ.)

50µA(Typ.)

5µA (Typ.)

-25°C to +85°C

W(Typ.) D(Typ.) H (Max.)

1.60mm x 1.60mm x 0.60mm



Controller ICs for High Side NMOSFET

BD2270HFV-LB

General Description

This is the product guarantees long time support in Industrial market.

BD2270HFV is an IC with a built-in external N-channel MOSFET driver circuit. This IC has a built-in charge pump circuit for gate drive and output discharge circuit, enabling configuration of a high side load switch for N-channel MOSFET drive without using any external parts.

In addition, the control input terminal has a built-in comparator with hysteresis function, facilitating control of the power up sequence. The space saving type of HVSOF5 package is used.

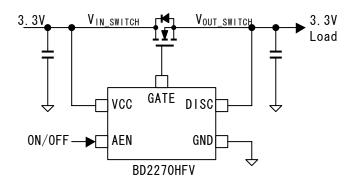
Features

- Long time support a product for Industrial applications.
- Built-in charge pump
- Built-in discharge circuit for output charge
- Soft start circuit
- Built-in comparator with hysteresis function at control input terminal
- Possible to drive N-channel power MOSFET

Applications

Industrial Equipment, PCs, PC peripheral devices, digital consumer electronics, etc.

Typical Application Circuit



Key Specifications

HVSOF5

Package

Input voltage range:

Operating current:

Standby current:

GATE rise time (C_{GATE}=500pF) :

GATE output voltage(V_{CC}=5V):

Operating temperature range:

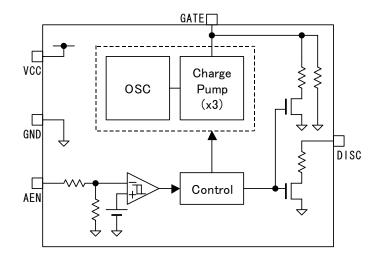
HVSOF5

Lineup

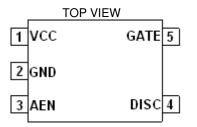
GATE o	output voltage(\	/ _{CC=5} V)	Package		Orderable Part Number
Min.	Тур.	Max.			
10V	13.5V	15V	HVSOF5	Reel of 3000	BD2270HFV – LBTR

OProduct structure : Silicon monolithic integrated circuit OThis product has no designed protection against radioactive rays

Block Diagram



Pin Configuration



Pin Description

PIN No.	PIN name	I / O	Function
1	VCC	-	Power input terminal
2	GND	-	Ground terminal
3	AEN	I	Control input terminal Turn ON the external MOSFET switch with high level input. High level input > 2.0V, Low level input < 0.8V
4	DISC	0	Switch output discharge terminal
5	GATE	0	GATE drive output terminal Used to connect the gate of the external N-channel MOSFET.

Absolute Maximum Ratings

Symbol Ratings		Unit
Vcc	-0.3 to 6.0	V
VAEN	-0.3 to 6.0	V
VDISC	-0.3 to 6.0	V
VGATE	-0.3 to 15.0	V
Tstg	-55 to 150	°C
Pd	669 ^{*1}	mW
	VCC VAEN VDISC VGATE TSTG	Vcc -0.3 to 6.0 VAEN -0.3 to 6.0 VDISC -0.3 to 6.0 VGATE -0.3 to 15.0 TSTG -55 to 150

*1 When mounted on a 70 mm×1.6 mm glass epoxy PCB, derate by 5.352 mW/°C above Ta=25°C

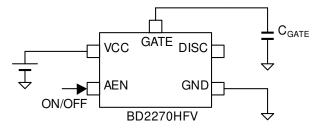
Recommended Operating Conditions

Parameter	Symbol	Ratings	Unit
Operating voltage range	Vcc	2.7 to 5.5	V
Operating temperature range	T _{OPR}	-25 to 85	°C

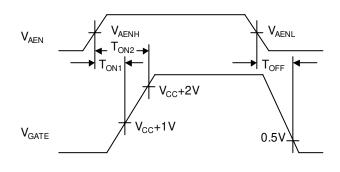
Electrical Characteristics (Vcc = 3.0V, Ta=25°C unless otherwise specified)

Deremeter	Cumbal	Limits			1.1	O a sa aliti a sa
Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Operating current	I _{CC}	-	50	75	μA	$V_{AEN} = 2.5V$
Standby current	I _{STB}	-	5	10	μA	$V_{AEN} = 0V$
	V _{AENH}	1.55	2	2.45	V	High level input
AEN input voltage	V _{AENL}	1.35	1.9	2.35	V	Low level input
AEN input current	I _{AEN}	-	3	5	μA	V _{AEN} = 3V
			1			
		10	13.5	15	V	V _{CC} =5V
GATE output voltage	V _{GATE}	6.6	9.5	9.9	V	V _{CC} =3.3V
		6	8.5	9	V	V _{CC} =3V
			1		1	
GATE rise time	T _{ON}	-	130	750	μs	C_{GATE} =500pF V_{CC} =3 V V_{GATE} > 4V
GATE fall time	T _{OFF}	-	18	60	μs	$\begin{array}{l} C_{\text{GATE}} = 500 \text{pF } V_{\text{CC}} = 3 \text{V} \\ V_{\text{GATE}} < 0.5 \text{V} \end{array}$
DISC discharge resistance	R _{DISC}	-	200	300	Ω	V _{AEN} =0V

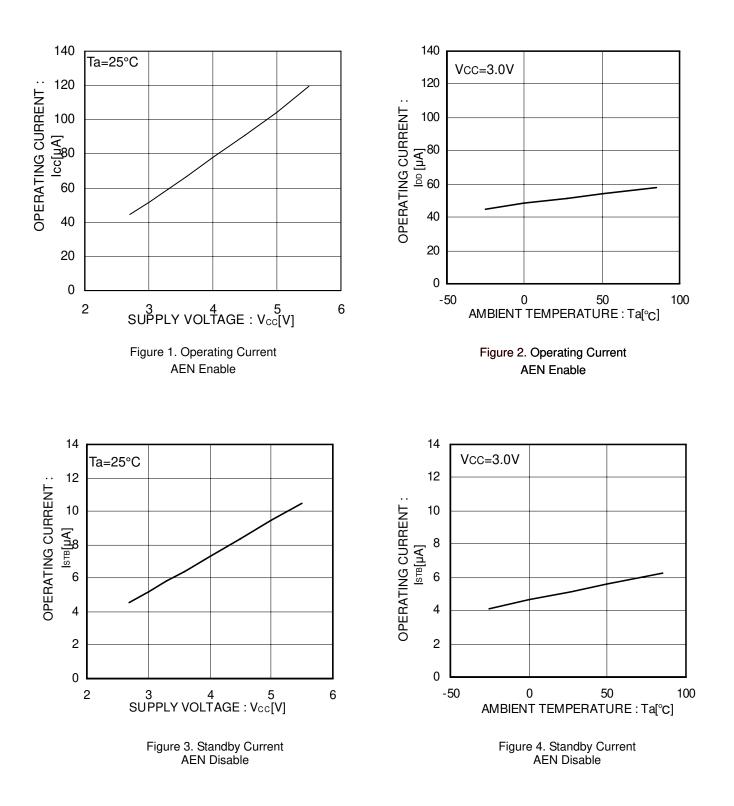
Measurement Circuit

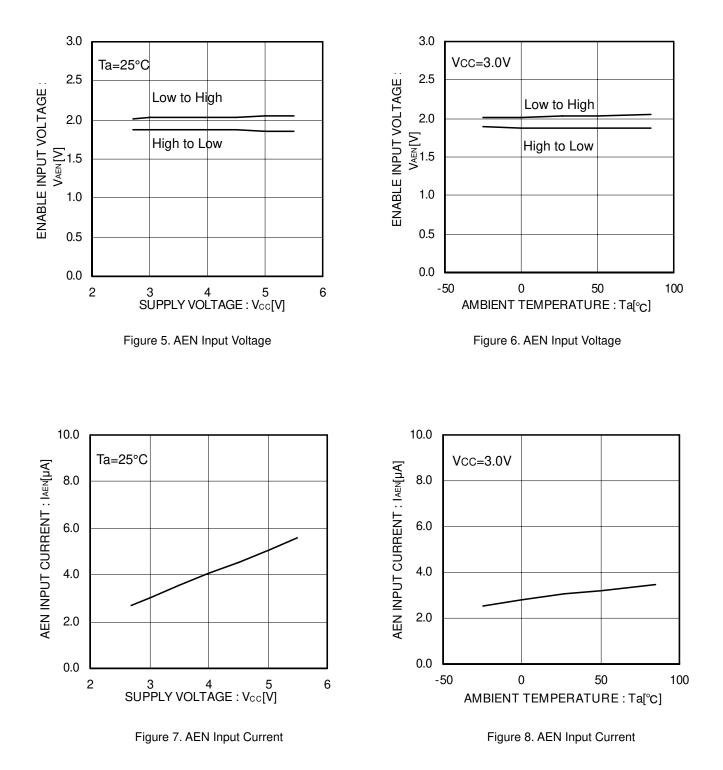


Timing Diagram



Typical Performance Curves





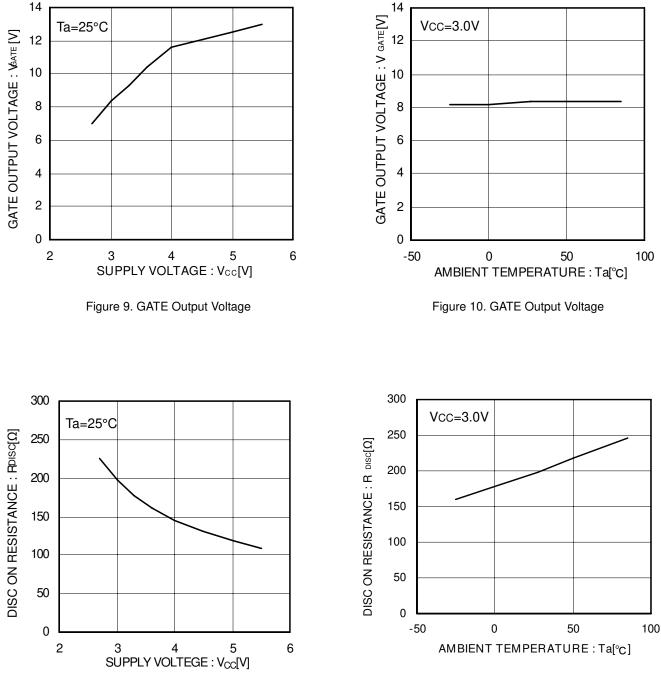
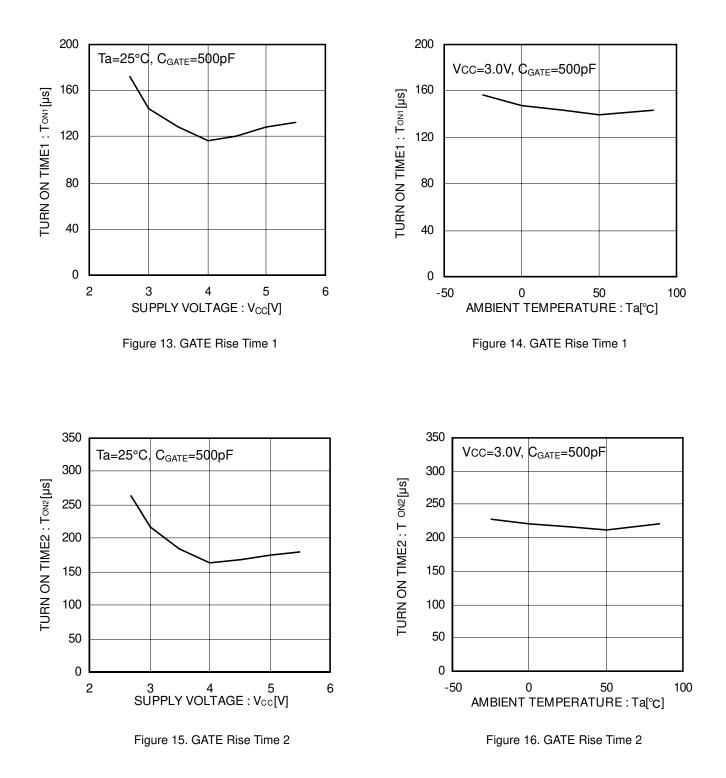


Figure 11. DISC ON Resistance

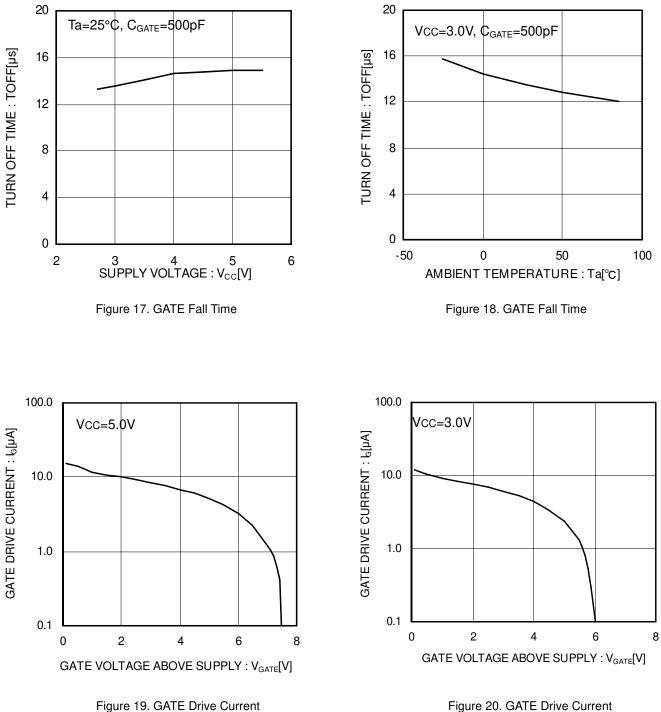
Figure 12. DISC ON Resistance

6/18

Typical Performance Curves - continued

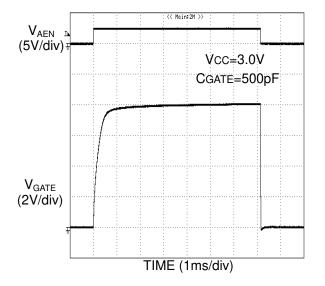


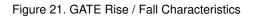
Typical Performance Curves - continued





Typical Wave Forms





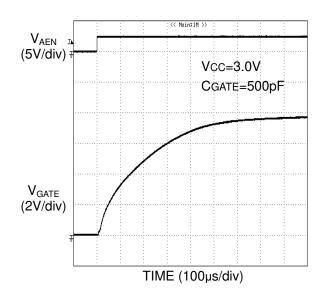


Figure 22. GATE Rise Characteristics

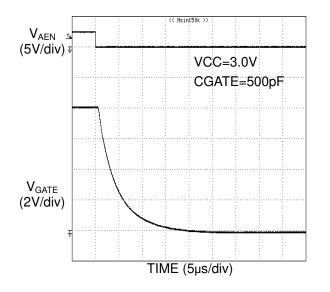


Figure 23. GATE Fall Characteristics

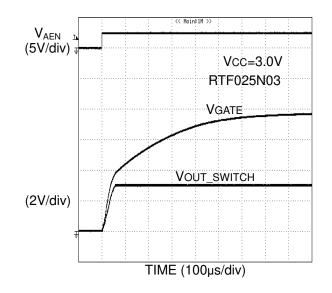


Figure 24. GATE Switch Rise Characteristics

Typical Wave Forms - continued

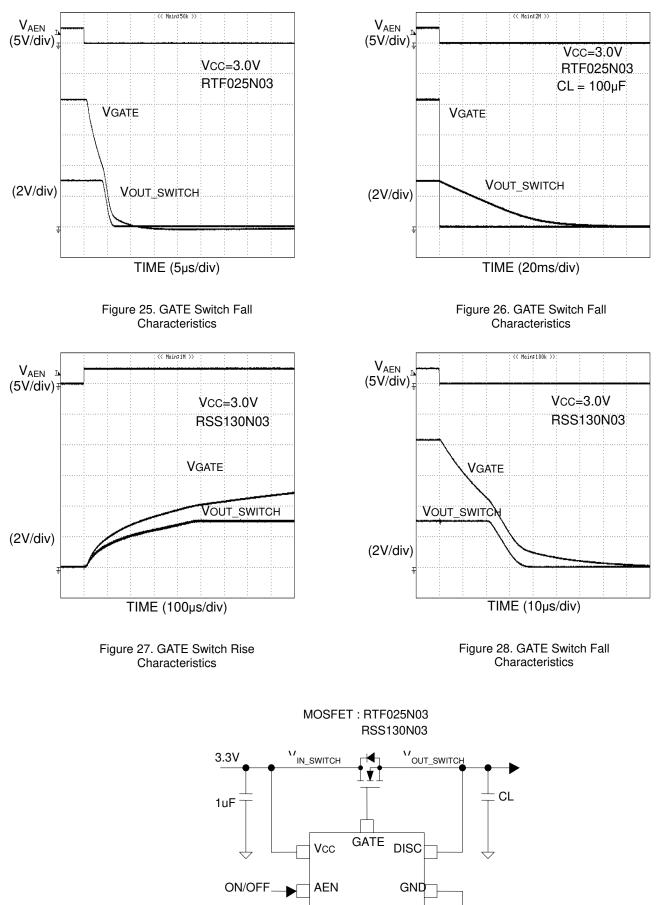


Figure 29. Switch Rise / Fall Characteristics Measurement Circuit Diagram

BD2270HFV

Application Circuit

1. Configuration of 3.3V load switch

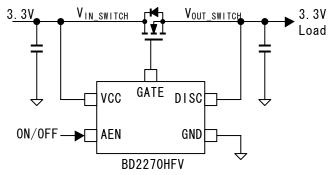
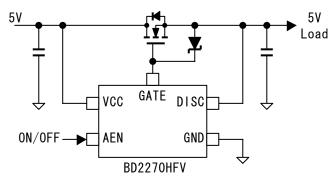
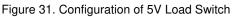


Figure 30. Configuration of 3.3V Load Switch 2. Configuration of 5V load switch





A 5V load switch can be configured like the 3.3V load switch. However, if the external N-channel MOSFET has low VGSS, clamp it with Zener diode and the like.

3. Configuration of low-voltage load switch

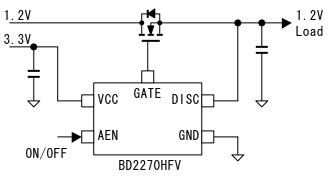
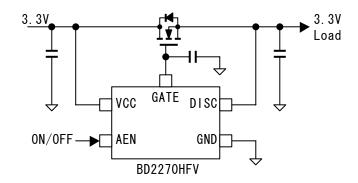


Figure 32. Configuration of Low-voltage Load Switch

Providing BD2270HFV drive power supply enables configuration of a low-voltage load switch.

4. Soft start configuration





Connecting an external capacitor to the GATE terminal of the BD2270HFV makes it possible to lengthen the rise time of the N-channel MOSFET, thus achieving reduction of the inrush current to the large-capacity capacitor mounted on the load side.

Application Information

The system connection diagram shown here gives no guarantee to the operation of the application circuit.

When the recommended external circuit components are changed, be sure to consider adequate margins by taking into account external parts and/or IC's dispersion including not only static characteristics, but also transient characteristics.

Functional Description

The BD2270HVF is a driver IC that uses an N-channel MOSFET as a high side load switch. This IC incorporates the following functions.

1. GATE drive

The gate drive voltage of an N-channel MOSFET is generated by a built-in charge pump in the BD2270HFV. The built-in charge pump in the BD2270HFV generates a voltage three times as high as the power supply voltage at the GATE terminal. In addition, since this IC has a built-in capacitor for the charge pump, it needs no external parts.

The charge pump operates when the AEN is set to High. When the AEN is set to Low, the GATE terminal voltage is fixed to the GND level.

2. Output discharge circuit

The output discharge circuit is enabled when the AEN is set to Low. When the discharge circuit is activated, the 200Ω (Typ.) MOSFET switch located between the DISC terminal and the GND terminal turns ON. Connecting the DISC terminal and the source side (load side) of the N-channel MOSFET makes it possible to immediately discharge capacitive load.

3. Soft start function

When the AEN terminal input voltage reaches the High level, the built-in charge pump in the BD2270HFV charges the gate of the N-channel MOSFET. The turn-on time of the N-channel MOSFET is determined by the GATE capacity. In addition, connecting a capacitor to the GATE terminal makes it possible to lengthen the rise of turn-on time of the N-channel MOSFET, thus achieving reduction of the inrush current to a large capacitive load.

4. Analog control input terminal

The AEN input of the BD2270HFV is connected to the built-in hysteresis comparator. Consequently, even analog signals can control the BD2270HFV, thus facilitating the control of the switch ON-OFF sequence.

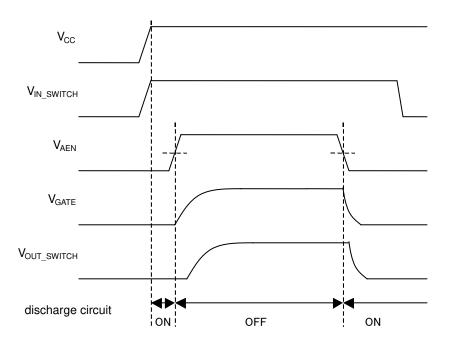


Figure 34. Operation Timing

* To turn ON the power supply (V_{CC} , VIN_{SWITCH}), set the AEN to Low.

Power Dissipation (HVSOF5) 800 700 POWER DISSIPATION : Pd (mW) 600 500 400 300 200 100 0 0 25 50 75 100 125 150 AMBIENT TEMPERATURE : Ta (°C)

Mounted on a 70 mm×70 mm×1.6 mm glass epoxy PCB Figure 35. Power dissipation curve (Pd-Ta Curve)

I/O Equivalent Circuit

Pin name	Pin No.	Equivalent circuit
AEN	3	
DISC	4	
GATE	5	

Operational Notes

(1) Absolute Maximum Ratings

Operating the IC over the absolute maximum ratings may damage the IC. The damage can either be a short circuit between pins or an open circuit between pins. Therefore, it is important to consider circuit protection measures, such as adding a fuse, in case the IC is operated over the absolute maximum ratings

(2) Recommended operating conditions

These conditions represent a range within which the expected characteristics of the IC can be approximately obtained. The electrical characteristics are guaranteed under the conditions of each parameter.

(3) Reverse connection of power supply

Connecting the power supply in reverse polarity can damage the IC. Take precautions against reverse polarity when connecting the power supply, such as mounting an external diode between the power supply and the IC's power supply terminals.

(4) Power supply line

Design the PCB layout pattern to provide low impedance ground and supply lines. Separate the ground and supply lines of the digital and analog blocks to prevent noise in the ground and supply lines of the digital block from affecting the analog block. Furthermore, connect a capacitor to ground at all power supply pins. Consider the effect of temperature and aging on the capacitance value when using electrolytic capacitors.

(5) Ground Voltage

The voltage of the ground pin must be the lowest voltage of all pins of the IC at all operating conditions. Ensure that no pins are at a voltage below the ground pin at any time, even during transient condition.

(6) Short between pins and mounting errors

Be careful when mounting the IC on printed circuit boards. The IC may be damaged if it is mounted in a wrong orientation or if pins are shorted together. Short circuit may be caused by conductive particles caught between the pins.

(7) Operation under strong electromagnetic field

Operating the IC in the presence of a strong electromagnetic field may cause the IC to malfunction.

(8) Testing on application boards

When testing the IC on an application board, connecting a capacitor directly to a low-impedance output pin may subject the IC to stress. Always discharge capacitors completely after each process or step. The IC's power supply should always be turned off completely before connecting or removing it from the test setup during the inspection process. To prevent damage from static discharge, ground the IC during assembly and use similar precautions during transport and storage.

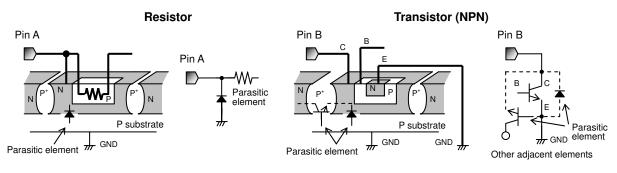
(9) Regarding input pins of the IC

This monolithic IC contains P+ isolation and P substrate layers between adjacent elements in order to keep them isolated. P-N junctions are formed at the intersection of the P layers with the N layers of other elements, creating a parasitic diode or transistor. For example (refer to figure below):

When GND > Pin A and GND > Pin B, the P-N junction operates as a parasitic diode.

When GND > Pin B, the P-N junction operates as a parasitic transistor.

Parasitic diodes inevitably occur in the structure of the IC. The operation of parasitic diodes can result in mutual interference among circuits, operational faults, or physical damage. Therefore, conditions that cause these diodes to operate, such as applying a voltage lower than the GND voltage to an input pin (and thus to the P substrate) should be avoided.





(10) GND wiring pattern

When using both small-signal and large-current GND traces, the two ground traces should be routed separately but connected to a single ground at the reference point of the application board to avoid fluctuations in the small-signal ground caused by large currents. Also ensure that the GND traces of external components do not cause variations on the GND voltage. The power supply and ground lines must be as short and thick as possible to reduce line impedance.

(11) External Capacitor

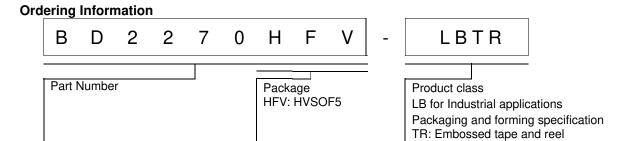
When using a ceramic capacitor, determine the dielectric constant considering the change of capacitance with temperature and the decrease in nominal capacitance due to DC bias and others.

(12) Thermal consideration

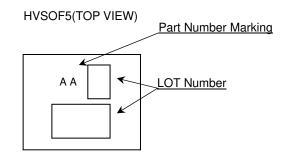
Use a thermal design that allows for a sufficient margin by taking into account the permissible power dissipation (Pd) in actual operating conditions. Consider Pc that does not exceed Pd in actual operating conditions ($Pc \ge Pd$).

Package Power dissipation	: Pd (W)=(Tjmax $-$ Ta)/ θ ja
Power dissipation	: Pc (W)=(Vcc-Vo)×Io+Vcc×Ib

Tjmax : Maximum junction temperature=150°C, Ta : Peripheral temperature[°C], θ ja : Thermal resistance of package-ambience[°C/W], Pd : Package Power dissipation [W], Pc : Power dissipation [W], Vcc : Input Voltage, Vo : Output Voltage, Io : Load, Ib : Bias Current



Marking Diagram



Part Number	Part Number Marking	
BD2270HFV	AA	

Physical Dimension Tape and Reel Information HVSOF5 Package Name $1. 6 \pm 0. 05$ (0.8) 1. 0 ± 0.05 (0.3) 2 MAX (20)4 5 5 4 0. 0. BURR) 05 $1. 6\pm 0. 05$ 28 (include. 91)41) 2 ± 0 . (0. (0) ÷ _ (MAX1 1 2 3 3 2 1 0.13 ± 0.05 6 MAX S 0. 03 $0\ 2\ ^{+0.}_{-0.}$ □ 0. 1 S (UNIT:mm) PKG: HVSOF 5 0.5 0. $22\pm0.05 \oplus 0.08$ Drawing No. EX108-5002 0. <Tape and Reel information> Embossed carrier tape Таре 3000pcs Quantity TR Direction The direction is the 1pin of product is at the upper right when you hold of feed reel on the left hand and you pull out the tape on the right hand Ipin $\overline{\bigcirc}$ $\overline{}$ C Direction of feed Reel *Order quantity needs to be multiple of the minimum quantity.

Revision History

Date Revision Changes		Changes		
13.Mar.2013	001	New Release		
21.Feb.2014	002	Delete sentence "and log life cycle" in General Description and Futures (page 1). Change "Industrial Applications" to "Industrial Equipment" in Applications (page 1). Applied new style ("title", "Ordering Information" and "Physical Dimension Tape and Reel Information").		

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CLASSI		CLASS II b	CLASSII	
CLASSⅣ	CLASSⅢ	CLASSⅢ	CLASSIII	

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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