imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





50Ω to 200Ω Balanced

Asia:

+86 512-62749282

Ultra Low Profile 0805 Balun Description

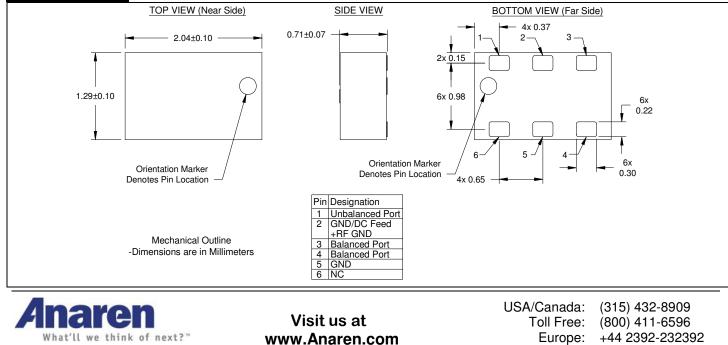
The BD3438J50200AHF is a low profile sub-miniature balanced to unbalanced transformer designed for differential inputs and output locations on modern chipsets in an easy to use surface mount package The BD3438J50200AHF is ideal for high volume manufacturing and is higher performance than traditional ceramic and lumped element baluns. The BD3438J50200AHF has an unbalanced port impedance of 50 Ω and a 200 Ω balanced port impedance. This transformation enables single ended signals to be applied to differential ports on modern semiconductors. The output ports have equal amplitude (-3dB) with 180 degree phase differential. The BD3438J50200AHF is available on tape and reel for pick and place high volume manufacturing.

Detailed Electrical Specifications: Specifications subject to change without notice.

		ROOM (25°C)			
Features:	Parameter	Min.	Тур.	Max	Unit
• 3400 – 3800 MHz	Frequency	3400		3800	MHz
 0.7mm Height Profile 50 Ohm to 2 x 100 Ohm 	Unbalanced Port Impedance		50		Ω
Low Insertion Loss	Balanced Port Impedance		200		Ω
 Input to Output DC Isolation 	Return Loss	11	15		dB
Surface Mountable	Insertion Loss*		0.7	1.0	dB
Tape & Reel	Amplitude Balance		0.1	0.6	dB
Non-conductive SurfaceRoHS Compliant	Phase Balance		4	10	Degrees
	CMRR		29		dB
	Power Handling @85C			2	Watts
	Power Handling @105C			1.2	Watts
	Operating Temperature	-55		+105	°C

^t Insertion Loss stated at room temperature (Insertion Loss is approximately 0.1 dB higher at +85 °C)

Outline Drawing

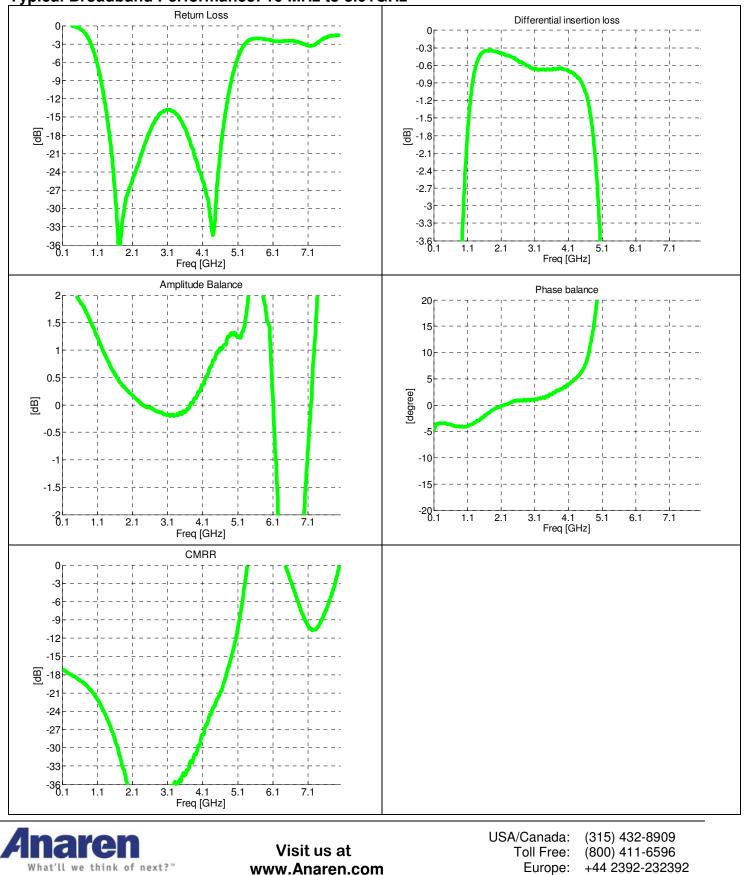




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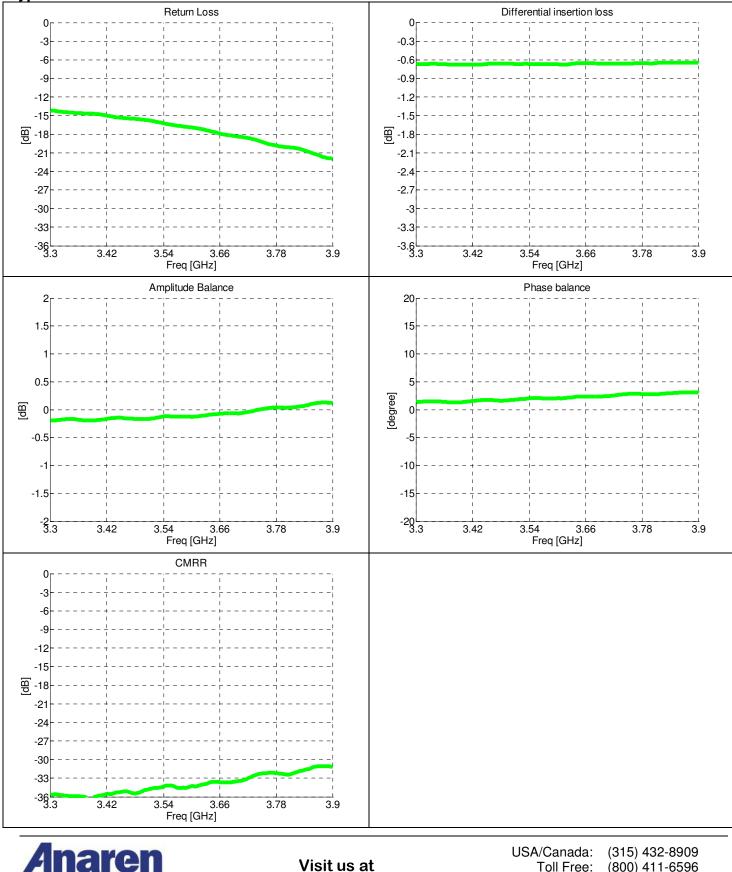




What'll we think of next?"

Model BD3438J50200AHF

Typical Performance: 3300 MHz to 3900 MHz



www.Anaren.com

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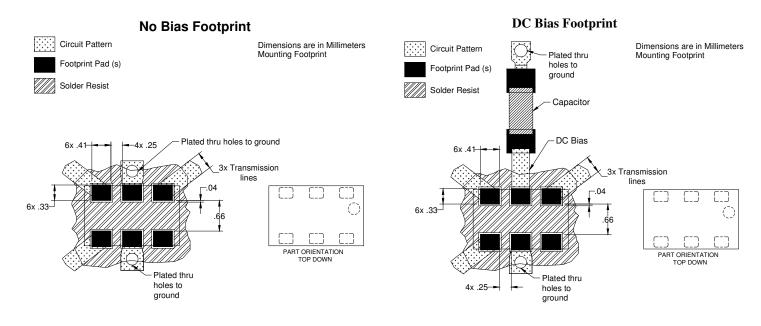


Mounting Configuration:

In order for Xinger surface mount components to work optimally, the proper impedance transmission lines must be used to connect to the RF ports. If this condition is not satisfied, insertion loss, Isolation and VSWR may not meet published specifications.

All of the Xinger components are constructed from organic PTFE based composites which possess excellent electrical and mechanical stability. Xinger components are compliant to a variety of ROHS and Green standards and ready for Pb-free soldering processes. Pads are Gold plated with a Nickel barrier.

An example of the PCB footprint used in the testing of these parts is shown below. In specific designs, the transmission line widths need to be adjusted to the unique dielectric coefficients and thicknesses as well as varying pick and place equipment tolerances.







Packaging and Ordering Information

Parts are available in reel and are packaged per EIA 481-D. Parts are oriented in tape and reel as shown below. Minimum order quantities are 4000 per reel.

