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Sound Processors for Home Theater System

7.1ch Sound Processor

BD3452KS

No.10081EAT01

Description

BD3452KS is a sound processor where the functions including Input Selector, 8ch Volume and Gain Amp required for applications such as AV receivers, home theater systems and mini-component systems are integrated into a single chip. Adopting the BiCMOS process achieves low distortion, low noise and a wide dynamic range.

Features

- 1) Dynamic range: 132dB (VOL=MUTE, IHF-A)
- 2) Independent 8 channels for Master Volume (0 to -99 dB, MUTE 1dB/Step)
- 3) Supporting 2nd room entertainment
- 4) Low current consumption design achieved by adopting the BiCMOS process
- 5) Built-in Output Gain Amp useful for adjusting output signal voltages (0 to 15dB, 1dB/Step)
- 6) BD3841FS (9-input selector), BD3843FS (6-input selector) and BUS are common to be controlled simultaneously.
- 7) Built-in 2ch output port
- 8) 2-wire serial control (For both 3.3V and 5V)

Applications

AV receivers, home theater systems and mini-component systems

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Ratings	Unit
Power Supply Voltage	VCC	7.5 ^{*1} -7.5	V
	VEE		
Input Signal Voltage	VIN	VCC+0.3 to VEE-0.3	V
Power Dissipation	Pd	1300 *2	mW
Operating Temperature Range	Topr	-20 to +75	S
Storage Temperature Range	Tastg	-55 to +125	°C

*1 Even in the specified range of Power Supply Voltage, applying voltage only to the VCC side may cause an excessive current to give a permanent damage to the IC.

When starting up power supplies, VEE and VCC should be powered on simultaneously or VEE first; then followed by VCC. *2 Over Ta=25°C, reduce at the rate of 13mW/°C. When installed on the standard board (size: 70×70×1.6mm).

Operating conditions

It must function normally at Ta=25°C.

Parameter	Symbol		Unit			
Falameter	Symbol	Min.	Тур.	Max.	Unit	
Operating Supply Voltage	VCC	6.5	7	7.3	V	
Operating Supply Voltage	VEE	-7.3	-7	-6.5	v	

•Electrical characteristics

Ta=25°C, VCC=7V, VEE=-7V, f=1kHz, Vin=1Vrms, RL=10kΩ, Rg=600Ω, Input Gain=0dB, Master volume=0dB. Output gain=0dB, unless otherwise noted.

	volume=0dB, Out Paramete		Symbol		Limits		Unit	Conditions
			-,	Min.	Тур.	Max.		
	Circuit Current	VCC VEE	IQ	- -40	20 -20	40 mA		No signal
	Output Voltage G	ain	Gv	-2	0	2	dB	Measure : Pin87,88
	Total Harmonic Distortion Ratio		THD	-	0.0006	0.03	%	Measure : Pin87,88 BW=400 to 30kHz
L	Maximum Output	Voltage	Vomax	3.6	4.2	-	Vrms	Measure : Pin87,88 THD=1%
Total Output	Output Noise Volt	tage	Vno	-	1.4	12	μVrms	Measure : Pin87,88, Rg=0 Ω , BW=IHF-A
Tota	Residual Noise V	oltage	Vnor	-	1	8	μVrms	Measure : Pin87,88, Rg=0Ω, BW=IHF-A, Volume=MUTE
	Cross-talk between Channe	ls	СТС	-	-95	-80	dB	$\begin{array}{l} \mbox{Measure : Pin88(OUTFL),87 (OUTFR)} \\ \mbox{Rg=0}\Omega, \mbox{BW=IHF-A} \\ \mbox{Reference : Pin87(OUTFR)}, \\ \mbox{88(OUTFL)=1Vrms} \end{array}$
	Cross-talk between Selector	ſS	CTS	-	-95	-80	dB	Measure : Pin87,88 Rg=0Ω, BW=IHF-A
	Input Impedance		Rin	32	47	62	kΩ	
	V Output Voltage	Gain	GVV	-2	0	2	dB	Measure : Pin 81,82,83,84,85,86,87,88
tput	V Total Harmonic Distortion Ratio		THDV	-	0.0006	0.03	%	Measure : Pin 81,82,83,84,85,86,87,88 BW=400 to 30kHz
Volume Output	V Residual Noise	Voltage	VnorV	-	1	8	μVrms	Measure : Pin 81,82,83,84,85,86,87,88 BW=IHF-A, Rg=0Ω,Volume=MUTE
Volr	Volume Setting E	rror	VOLE1	-0.5	0	0.5	dB	Measure : Pin 81,82,83,84,85,86,87,88 Volume=0dB, Vin=3Vrms
	Maximum Attenua	ation	VOLmin	-	-115	-105	dB	Measure : Pin 81,82,83,84,85,86,87,88 Vin=3Vrms, BW=IHF-A
Input Gain	Input Gain Contro	ol Range	GIG	10	12	14	dB	Measure : Pin 81,82,83,84,85,86,87,88 Input Gain=12dB, Vin=0.3Vrms
Gain	Output Gain Cont	trol Range	GOG	13	15	17	dB	Measure : Pin 81,82,83,84,85,86,87,88 Output Gain=15dB, Vin=0.3Vrms
Output Gain	Output Gain Setti	ng Error	GOE	-0.5	0	0.5	dB	Measure : Pin 81,82,83,84,85,86,87,88 Output Gain=0dB, Vin=0.3Vrms
<u></u>	R Output Impeda	nce	RoutR	-	20	100	Ω	Measure : Pin 44,45,46,47
REC Out	R Voltage Gain		GVR	-2	0	2	dB	Measure : Pin 44,45,46,47 (*)RL=10kΩ
£	R Total Harmonic Distortion Ratio		THDR	-	0.005	0.09	%	Measure : Pin 44,45,46,47 BW=400 to 30kHz, (*)RL=10kΩ
Ľ	Port H Output		PH	4.0	4.9	5.4	v	Measure : Pin 62,63 RL=10kΩ
Port	Port Output Curre	ent	PI	-	-	1.0	mA	Measure : Pin 62,63

(*) If two RECOUTs are ON, total load resistances of these two (RL) should be 10 kΩ.

This product is not of "anti radiation design".

Timing chart

- 1) Signal Timing Conditions
 - Data is read on the rising edge of the clock.
 - \cdot Latch is read on the falling edge of the clock.
 - $\boldsymbol{\cdot}$ Latch signal must terminate with the LOW state.

*To avoid malfunctions, clock and data signals must terminate with the LOW state.

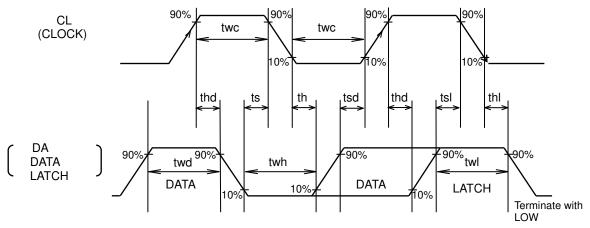


Fig.1

Parameter	Symbol		Unit		
	Min.		Тур.	Max.	Unit
Minimum Clock Width	twc	1.0	-	-	μs
Minimum Data Width	twd	1.0	-	-	μs
Minimum Latch Width	twl	1.0	-	-	μs
LOW Hold Width	twh	1.0	-	-	μs
Data Set-up Time (DATA→CLK)	tsd	0.5	-	-	μs
Data Hold Time (CLK→DATA)	thd	0.5	-	-	μs
Latch Set-up Time (CLK \rightarrow LATCH)	tsl	0.5	-	-	μs
Latch Hold Time (DATA→LATCH)	thl	0.5	-	-	μs
Latch Low Set-up Time	ts	0.5	-	-	μs
Latch Low Hold Time	th	0.5	-	-	μs

2) Voltage Conditions for Control Signals

Parameter		Limits		Unit	Conditions
Parameter	Min. Typ. Max.		Unit	Conditions	
"H" Input Voltage	2.2	-	5.5	V	Vcc = 6.5~7.3V
"L" Input Voltage	0	_	1.0	V	VEE=-6.5~-7.3V

3) Basic Configuration of Control Data Formats

-	Input direction																
	MSB																LSB
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
Data	Data													Sele	ct Add	ress	

• Con		ta Forr directi													Sele	ct Add	ress
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(1)	l	nput se	lector	1	Ir	nput se	lector 2	2	Input	t ATT	Input	gain	*	*	0	0	0
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(2)		LR Inpi Selecto			BLR Inp Selecto		Multi Sele		REC A	REC B	Port A	Port B	*	0	0	0	1
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(3)	C	Dutput ç	gain 7c	h	Output gain SWch			ch	*	*	*	*	*	1	0	0	1
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(4)	ſ	Master	volum	e		FLch		Master volume			FRch			0	1	0	
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(5)	1	Master	volum	e		Cch Master volume		e		SWch		0	1	1			
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(6)	Master volume					SLch			Master volume			SRch			1	1	0
Data	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
(7)	Master volume					SBLch			Master	volume	e	SBRch			1	1	1

By changing the setting of Select Address, seven different control formats are selectable.

For Select Address, the values except those shown above must not be specified.

Each time of power-on, all of the address data must be initialized.

* indicates 0 or 1.

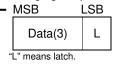
(Example)

(LAUNP	10)														
←	Inp	ut d	irection												
MSB	LS	βB	MSB	LSE	3	MSB	LSB	MSB	LSB	MSB I	_SB	MSB L	SB	MSB L	SB
Data(1)	L	Data(2)	L	Data(3) L	Data(4)	L	Data(5)	L	Data(6)	L	Data(7)	L
"!" ~~~~~		h													

"L" means latch.

After power-on, for the second and subsequent times, only the desired data can be selected for setting.

(Example) When changing Output Gain SWch, Input direction



Application circuit

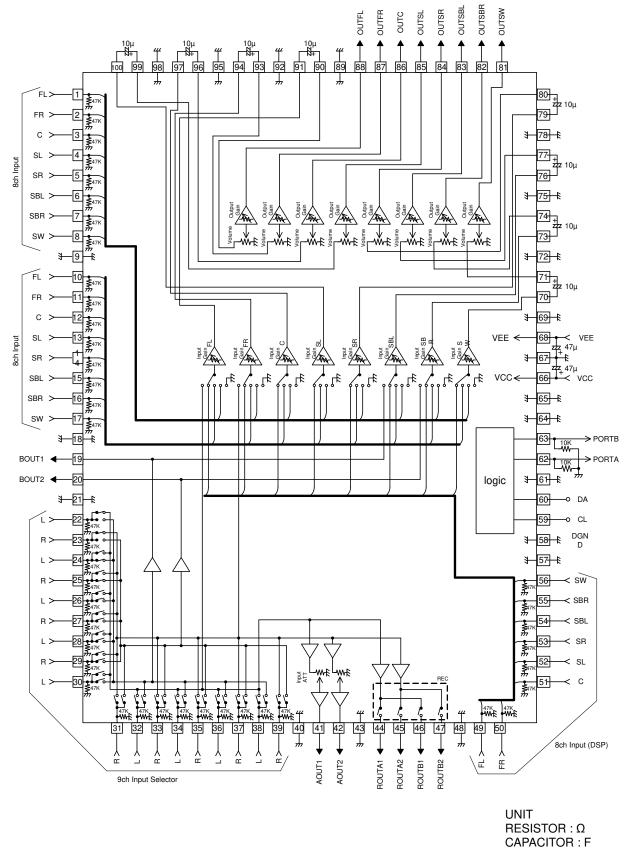


Fig.2

Reference data

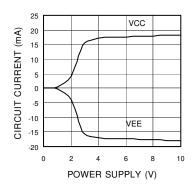
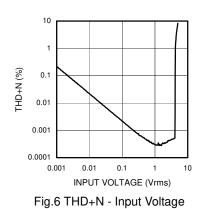


Fig.3 Circuit Current - Power Supply



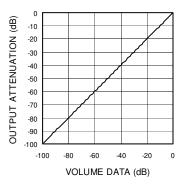


Fig.9 Volume Attenuation -Volume Settin

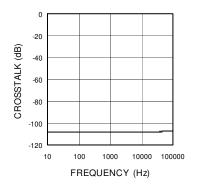


Fig.12 Cross-talk between Selectors -Frequency

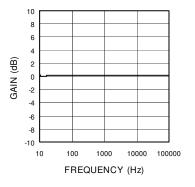


Fig.4 Voltage Gain - Frequency

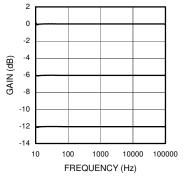


Fig.7 Input Attenuation - Frequency

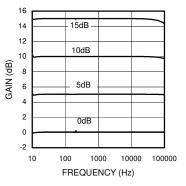


Fig.10 Output Gain - Frequency

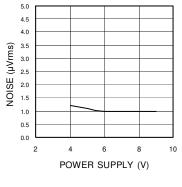


Fig.13 Output Noise Voltage -Power Supply Voltage

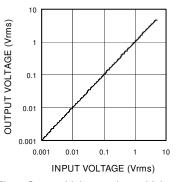


Fig.5 Output Voltage - Input Voltage

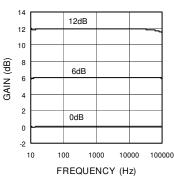


Fig.8 Input Gain - Frequency

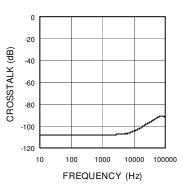
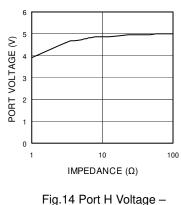


Fig.11 Cross-talk between Channels -Frequency



-ig.14 Port H Voltage -Load Resistance

Notes for use

- (1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- (2) Although we are confident in recommending the sample application circuits, carefully check their characteristics further when using them. When modifying externally attached component constants before use, determine them so that they have sufficient margins by taking into account variations in externally attached components and the Rohm LSI, not only for static characteristics but also including transient characteristics.
- (3) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

(4) VEE potential

Make the VEE pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the VEE pin, including transient phenomena.

(5) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

(6) Shorts between pins and misinstallation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

- (7) Operation in strong magnetic fields Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.
- (8) About Operating Voltage Range and Operating Temperature Range

The circuit functional operations are guaranteed within the Operating Voltage Range and Operating Temperature Range. The standard values of electrical characteristics, however, are guaranteed under the specific conditions. Accordingly, careful consideration of the IC characteristic variations is required to design a set of circuit.

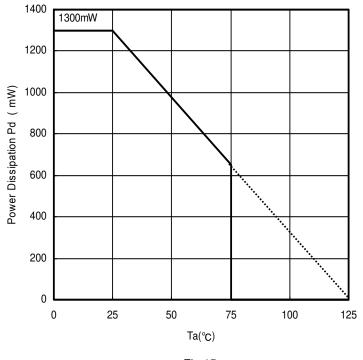
- (9) About power ON/OFF
 - (a) At power ON/OFF, a shock sound will be generated and, therefore, use MUTE on the set.
 - (b) When turning on power supplies, VEE and VCC should be powered on simultaneously or VEE first; then followed by VCC. If the VCC side is started up first, an excessive current may pass VCC through VEE.
- (10) About serial control

For the CL and DA terminals, the patterned and other wirings should be routed not to cause interference with the analog-signal-related lines.

(11) About function switching

When switching Input Selector or Input Gain, use MUTE on Master Volume.

Thermal derating characteristic



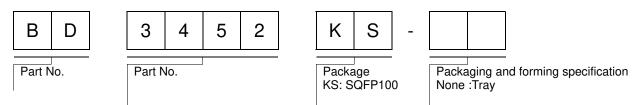


BD3452KS

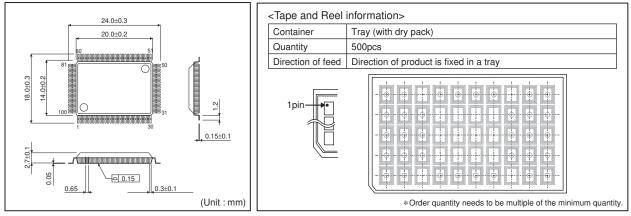
ROHM standard board packaging time value Board size: 70 x 70 x 1.6mm Raw material : FR4 glass epoxy board (copper area 3% or below)

BD3452KS

Ordering part number



SQFP100



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 - [C] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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