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Structure : Silicon Monolithic Integrated Circuit

Product : Audio sound controller

Type : BD3882FV

Feature : 1. Dual built-in recording and playing preamplifiers for cassette tapes
Less external components allows a compact size of the set.

2. The shock sound at power-ON/OFF is absorbed by both power sources.
The reference voltage of signal systems is designed to be a ground level so that
low offset voltage and low noise are achieved.

Absolute Maximum Ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit
Power Supply Voltage	VDD	5	V
	VEE	−5	
Power Dissipation	Pd	900※	mW
Operating Temperature	Topr	−20〜+75	℃
Storage Temperature	Tastg	−55〜+125	℃

※Over Ta＝25°C, derating at the rate of 9.0mW/°C.
When installed on the standard board (Size: 70 × 70 × 1.6mm).

Operating Voltage Range

Symbol	Limit	Unit
VDD	3.5〜4.75	V
VEE	−4.75〜−3.5	V

(It must function normally at Ta = 25℃)

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Electrical characteristics

(Ta = 25°C, VDD = 4.5V, VEE = −4.5V, f = 1kHz, Vin = 50mVrms, Rg = 600Ω, RL = 10kΩ, Input Selector = Ach, Front Volume=0dB, Rear Volume=0dB, Bass=0dB, Middle=0dB, Treble=0dB, unless otherwise noted.)

	Parameter	Symbol	Limit Min.	Limit Typ.	Limit Max.	Unit	Conditions
GENERAL	VDD Circuit Current	IQVDD	–	4.5	10.0	mA	Vin = 0Vrms
	VEE Circuit Current	IQVEE	−10.0	−4.5	–		
	Ach Maximum Input Voltage	Vaim					THD(Vout) =1%, BPF = 400～30kHz
	Bch Maximum Input Voltage	Vbim	0.6	0.8	–	Vrms	Front Volume = −6dB
	Cch Maximum Input Voltage	Vcim					Rear Volume = −18dB
	Maximum Input Voltage TP	Vimtp	0.6	0.8	–	Vrms	THD(Vout)=1%, BPF = 400～30kHz GAIN = 10dB, Front Volume = −6dB Rear Volume = −18dB
	Maximum Output Voltage	Vom	2.2	2.5	–	Vrms	THD = 1%, BPF = 400～30kHz
	Voltage Gain	Gv	26	28	30	dB	Gv = 20log(Vout/Vin)
	Channel Balance	CB	−1.5	0	1.5	dB	CB = Gv1−Gv2
	Total Harmonic Distortion Ratio	THD	–	0.01	0.1	%	BPF = 400–30KHz
	Output Noise Voltage ＊	Vno	–	22	60	μVrms	BPF = IHF−A, Rg = 0Ω
	Residual Noise Voltage ＊	Vmno	–	3	8	μVrms	Volume=−∞, BPF = IHF−A, Rg=0Ω
	Cross−talk between Channels ＊	CT	70	85	–	dB	CT = 20log(Vin/Vout) BPF = IHF−A, Rg = 0Ω
	Output Impedance	Rout	—	—	50	Ω	
INPUT	Voltage Gain A	Gva					
	Voltage Gain B	Gvb	8	10	12	dB	
	Voltage Gain C	Gvc					
	Cross−talk between Selectors A1ch→B1ch ＊	CTab1					
	Cross−talk between Selectors A2ch→B2ch ＊	CTab2					
	Cross−talk between Selectors B1ch→C1ch ＊	CTbc1	—	−110	−70	dB	Rg = 0Ω, BPF = IHF−A Vin=500mVrms
	Cross−talk between Selectors B2ch→C2ch ＊	CTbc2					
	Cross−talk between Selectors C1ch→A1ch ＊	CTca1					
	Cross−talk between Selectors C2ch→A2ch ＊	CTca2					
	Selector Output Impedance	Rsout	—	—	50	Ω	
KARAOKE	Karaoke Voltage Gain 1ch mono	Gk1	8	10	12	dB	1ch mono
	Karaoke Voltage Gain 2ch mono	Gk2	8	10	12	dB	2ch mono
	Karaoke Voltage Gain 1ch + 2ch	Gk12	8	10	12	dB	1ch+2ch
RECODING EQ	Voltage Gain RC	Gvrc	38	40	42	dB	f=10kHz
	Maximum Output Voltage RC	Vomrc	2.2	2.5	–	Vrms	THD=1%, BPF=400～30kHz
	Input conversion Noise Voltage RC	Vnorc	–	1.5	3.0	μVrms	Rg = 0Ω, BPF = IHF−A
	Slew Rate RC	Vtr	2	4	–	V/μS	
PLAY BACK EQ	Voltage Gain TP	Gvtp	44	46	48	dB	f=10kHz
	Maximum Output Voltage TP	Vomtp	2.2	2.5	–	Vrms	THD=1%, BPF=400～30kHz
	Input conversion Noise Voltage TP	Vnotp	–	1.1	2.0	μVrms	Rg=2.2kΩ, BPF=IHF−A, Gv=20dB
FRONT VOLUME	Volume Maximum Input Voltage	Vimv	1.5	1.9	—	Vrms	THD(Vout)=1%, BPF=400～30kHz Front Volume ATT=−6dB Rear Volume ATT=−18dB
	Volume Input Impedance	Rvin	14	20	26	KΩ	
	Volume Control Range	Vr	−79	−76	−73	dB	BPF = IHF−A
	Volume Control Step 1	Sv1	–	2	–	dB	0dB to −36dB
	Volume Control Step 2	Sv2	–	4	–	dB	−36dB to −76dB
	Volume Setting Error F	Evf	−2	0	2	dB	0dB to −76dB
	Volume Maximum Attenuation ＊	ATTm	—	−116	−90	dB	Volume = −∞, BPF = IHF−A ATT=20log(Vout/Vin)−18dB
REAR VOLUME	Volume Control Range	RVr	−20	−18	−16	dB	BPF=400～30kHz
	Volume Setting Error R	Evr	−2	0	2	dB	All steps

REV. B

	Parameter	Symbol	Limit			Unit	Conditions
			Min.	Typ.	Max.		
BASS	Bass Boost Control Range	Gbbr	+11	+14	+17	dB	f = 55Hz, Vin = 10mVrms BASS = +14dB
	Bass Cut Control Range	Gbcr	−17	−14	−11	dB	f = 55Hz, Vin = 10mVrms BASS = −14dB
	Bass Control Step	Sbc	−	2	−	dB	f = 55Hz Vin = 10mVrms
	Bass Setting Error (−12dB～12dB)	Ebs1	−2	0	2	dB	
	Bass Setting Error ±14dB	Ebs2	−3	0	3	dB	
TREBLE	Treble Boost Control Range	Gtbr	+11	+14	+17	dB	f = 10kHz, Vin = 10mVrms TREBLE = +14dB
	Treble Cut Control Range	Gtcr	−17	−14	−11	dB	f = 10kHz, Vin = 10mVrms TREBLE = −14dB
	Treble Control Step	Stc	−	2	−	dB	f = 10kHz Vin = 10mVrms
	Treble Setting Error (−12dB～12dB)	Ets1	−2	0	2	dB	
	Treble Setting Error (±14dB)	Ets2	−3	0	3	dB	
MIDDLE	Middle Boost Control Range	Gmbr	+11	+14	+17	dB	f = 1kHz, Vin = 10mVrms MIDDLE = +14dB
	Middle Cut Control Range	Gmcr	−17	−14	−11	dB	f = 1kHz, Vin = 10mVrms MIDDLE = −14dB
	Middle Control Step	Smc	−	2	−	dB	f = 1kHz Vin = 10mVrms
	Middle Setting Error (−12dB～12dB)	Ems1	−2	0	2	dB	
	Middle Setting Error (±14dB)	Ems2	−3	0	3	dB	

※The operational amplifier for PB should be used with the gain of 10dB or above.

※The input voltage over 1.9Vrms(Typ.) causes a distortion on the output wave at around the setting of Front Volume=0dB.
   Therefore, using at 1.9Vrms or less is recommended.

※For measurement, VP−9690A (Average value wave detection, Effective value display) IHF−A filter by Matsushita
   Communication Industrial is used.

※Phase relation between Input/Output signal terminals is Equiphase.

※Not designed for radiation resistance.

Outline dimension・Marking dimension　　　　Block Diagram
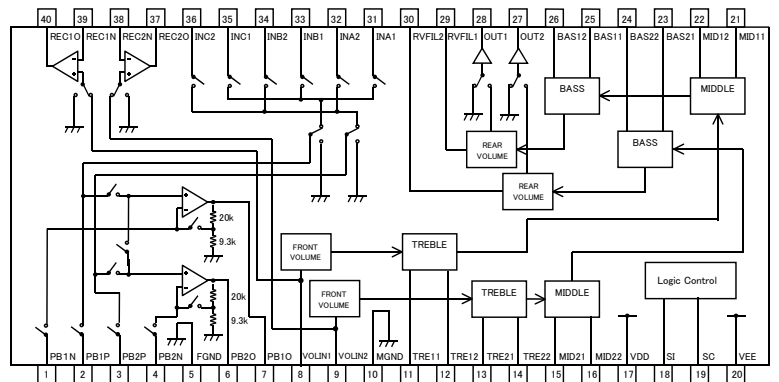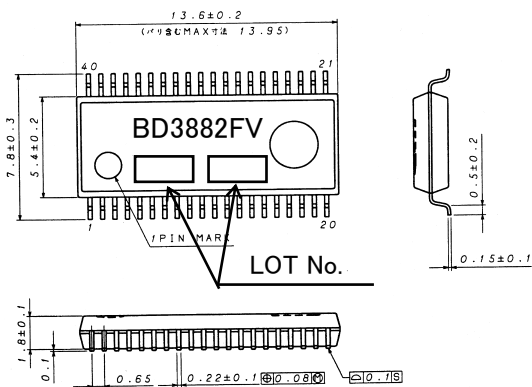
BD3882FV

LOT No.

SSOP−B40 (Unit：mm)

Unit: Resistance = Ω

REV. B

Pin number・Pin name

Pin number	Pin name	Pin number	Pin name	Pin number	Pin name	Pin number	Pin name	Pin number	Pin name
1	PB1N	9	VOLIN2	17	VDD	25	BAS11	33	INB1
2	PB1P	10	MGND	18	SI	26	BAS12	34	INB2
3	PB2P	11	TRE11	19	SC	27	OUT2	35	INC1
4	PB2N	12	TRE12	20	VEE	28	OUT1	36	INC2
5	FGND	13	TRE21	21	MID11	29	RVFIL1	37	REC2O
6	PB2O	14	TRE22	22	MID12	30	RVFIL2	38	REC2N
7	PB1O	15	MID21	23	BAS21	31	INA1	39	REC1N
8	VOLIN1	16	MID22	24	BAS22	32	INA2	40	REC1O

Cautions on use

1. About operating voltage range and operating temperature range

Within the Operating Voltage Range and the Operating Temperature Range, while basic circuit functional operations are supposed to be guaranteed, the standard values of the electrical characteristics are guaranteed only when used under the specific conditions defined within these ranges. Thus, the users must verify those conditions before setting constants, elements, voltages, and temperatures. Note that the conditions of power dissipation are also affected with temperatures.

2. About power on reset

A built-in circuit for performing initialization inside the IC at power-ON is provided. In the case of the set design, however, to be on the safe side, it is recommended that data shall be sent to all the addresses as initial data at power-ON and, until this sending operation is completed, the mute shall be applied.

Function	Initial Condition
Input Selector	MUTE
REC Output	REC MUTE
Karaoke	Stereo
Front Volume	$-\infty$dB
Rear Volume	-18dB
Treble Gain	0dB
Middle Gain	0dB
Bass Gain	0dB

3. About 2-wire serial control

As the terminals of SI and SC are designed for inputting high-frequency digital signals, the wiring and layout patterns should be routed not to cause interference with the analog-signal-related lines.

4. About power ON/OFF

Shock sound absorbing measures at power ON/OFF are implemented on 5pin, 6pin, 16pin, 18pin, 25pin, and 28pin for BD3881FV, as well as on 6pin, 7pin, 27pin, 28pin, 37pin, and 40pin for BD3882FV. When booting up power supplies, the VEE side should be booted a little bit earlier than the other side. If the VDD side is booted up first, an excessive current may pass VDD through VEE. When booting off the power supply, the VDD side should be booted off a little bit earlier than the other side. Note that, at this time, voltage change passing through the GND level may produce an abnormally large current.

5. About function switching

On switching between the Front Volume, Rear Volume, Bass, Middle, Treble, Karaoke, and REC Mute functions, the action has been taken to absorb such switching shock sound. For the other function switching operations, a combined use of MUTE is recommendable.

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