imall

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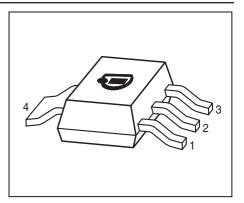


BDP948_BDP950_BDP954

PNP Silicon AF Power Transistors

- For AF driver and output stages
- High collector current
- High current gain
- Low collector-emitter saturation voltage
- Complementary types: BDP947, BDP949 BDP953 (NPN)
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101





Туре	Marking	Pin Configuration				Package		
BDP948	BDP948	1=B	2=C	3=E	4=C	-	-	SOT223
BDP950	BDP950	1=B	2=C	3=E	4=C	-	-	SOT223
BDP954	BCP954	1=B	2=C	3=E	4=C	-	-	SOT223



Maximum Ratings

Parameter	Symbol	Value	Unit	
Collector-emitter voltage	V _{CEO}		V	
BDP948		45		
BDP950		60		
BDP954		100		
Collector-base voltage	V _{CBO}			
BDP948		45		
BDP950		60		
BDP954		120		
Emitter-base voltage	V _{EBO}	5		
Collector current	I _C	3	A	
Peak collector current, $t_p \le 10 \text{ ms}$	I _{CM}	5		
Base current	I _B	200	mA	
Peak base current	/ _{BM}	500		
Total power dissipation-	P _{tot}	5	W	
<i>T</i> _S ≤ 100 °C				
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-65 150		

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R _{thJS}	≤ 10	K/W

¹For calculation of R_{thJA} please refer to Application Note AN077 (Thermal Resistance Calculation)



Parameter	Symbol		Unit		
		min.	typ.	max.	1
DC Characteristics					
Collector-emitter breakdown voltage	V _{(BR)CEO}				V
<i>I</i> _C = 10 mA, <i>I</i> _B = 0 , BDP948		45	-	-	
<i>I</i> _C = 10 mA, <i>I</i> _B = 0 , BDP950		60	-	-	
I _C = 10 mA, I _B = 0 , BDP954		100	-	-	
Collector-base breakdown voltage	V _{(BR)CBO}]
/ _C = 100 μA, / _E = 0 , BDP948		45	-	-	
/ _C = 100 μA, / _E = 0 , BDP950		60	-	-	
/ _C = 100 μA, / _E = 0 , BDP954		120	-	-	
Emitter-base breakdown voltage	V _{(BR)EBO}	5	-	-]
<i>I</i> _E = 10 μA, <i>I</i> _C = 0					
Collector-base cutoff current	I _{CBO}				μA
$V_{\rm CB} = 45 \text{V}, I_{\rm E} = 0$		-	-	0.1	
$V_{\rm CB}$ = 45 V, $I_{\rm E}$ = 0 , $T_{\rm A}$ = 150 °C		-	-	20	
Emitter-base cutoff current	I _{EBO}	-	-	100	nA
$V_{\rm EB} = 4 \rm V, I_{\rm C} = 0$					
DC current gain ¹⁾	h _{FE}				-
<i>I</i> _C = 10 mA, <i>V</i> _{CE} = 5 V		25	-	-	
<i>I</i> _C = 500 mA, <i>V</i> _{CE} = 1 V		85	-	475	
<i>I</i> _C = 1 A, <i>V</i> _{CE} = 2 V BDP948,BDP950		50	-	-	
BDP954		15	-	-	
$I_{\rm C}$ = 1 A, $V_{\rm CE}$ = 2 V					
Collector-emitter saturation voltage ¹⁾	V _{CEsat}	-	-	0.5	V
$I_{\rm C}$ = 2 A, $I_{\rm B}$ = 0.2 A					
Base emitter saturation voltage ¹⁾	V _{BEsat}	-	-	1.3	
<i>I</i> _C = 2 A, <i>I</i> _B = 0.2 A					
AC Characteristics					
Transition frequency	f _T	-	100	-	MHz
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 10 V, f = 100 MHz					
Collector-base capacitance	C _{cb}	-	40	-	pF
V _{CB} = 10 V, <i>f</i> = 100 MHz					

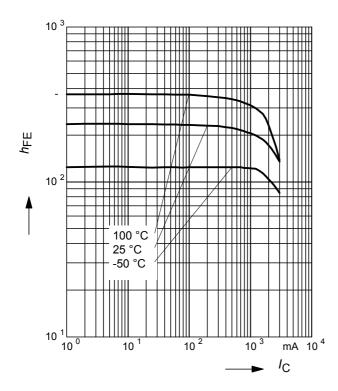
Electrical Characteristics at $T_A = 25^{\circ}$ C, unless otherwise specified

¹Pulse test: t < 300 μ s; D < 2%

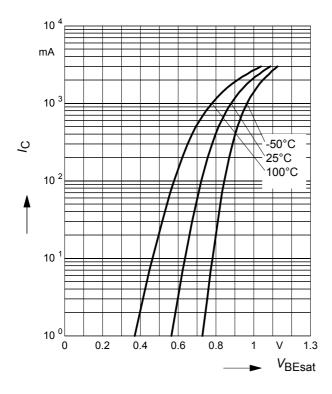


DC current gain $h_{\text{FE}} = f(I_{\text{C}})$

*V*_{CE} = 2 V

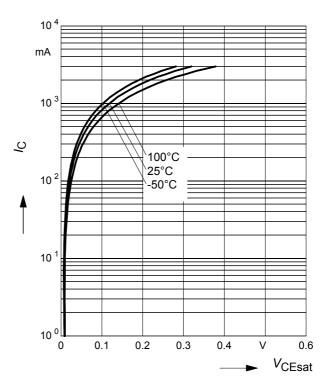


Base-emitter saturation voltage $I_{\rm C}$ = ($V_{\rm BEsat}$), $h_{\rm FE}$ = 10



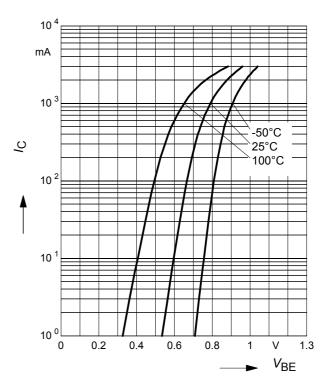
Collector-emitter saturation voltage

 $I_{\rm C} = f(V_{\rm CEsat}), h_{\rm FE} = 10$



Collector current $I_{C} = f(V_{BE})$

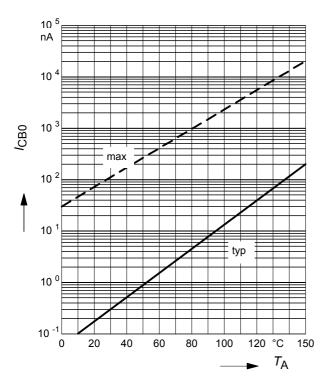
*V*_{CE} = 2 V





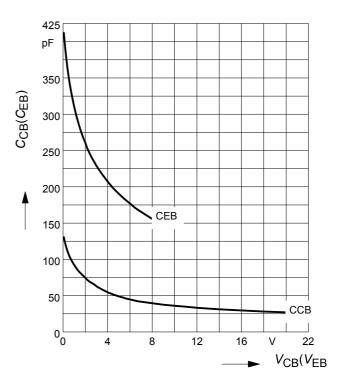
Collector cutoff current $I_{CBO} = f(T_A)$

V_{CB} = 45 V

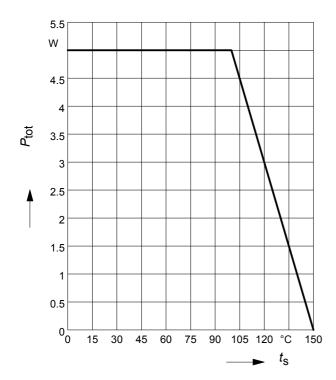


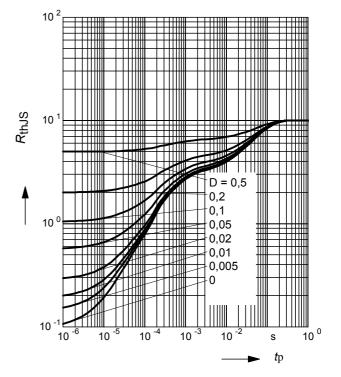
Total power dissipation $P_{tot} = f(T_S)$





Permissible Pulse Load $R_{\text{thJS}} = f(t_p)$

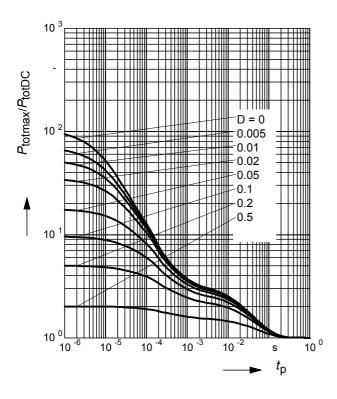




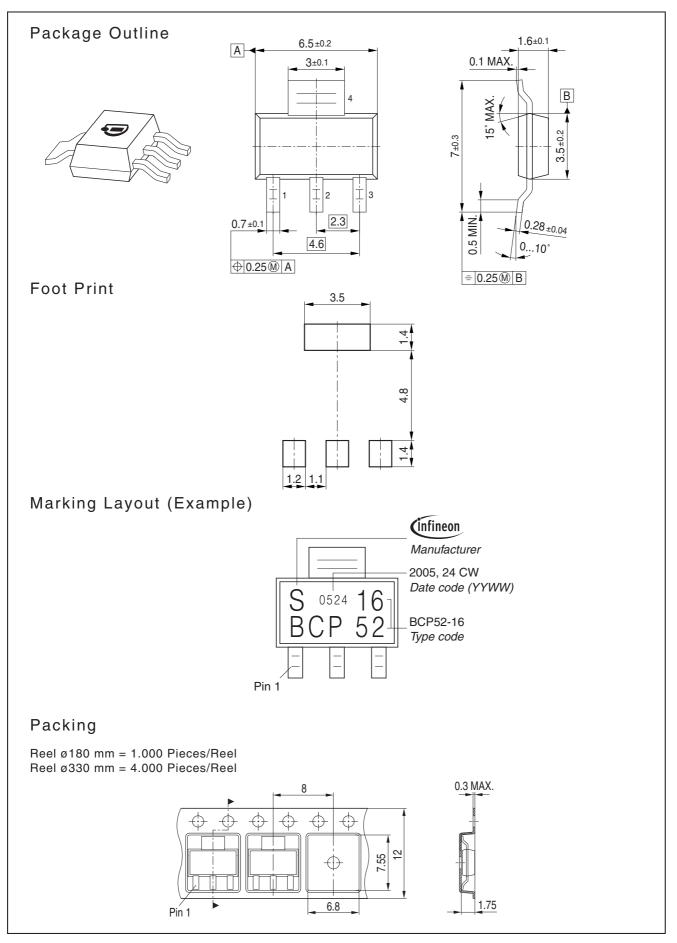


Permissible Pulse Load

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$











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