imall

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Darlington Complementary Silicon Power Transistors

These devices are designed for general purpose and low speed switching applications.

Features

- High DC Current Gain $h_{FE} = 2500$ (typ.) at $I_C = 4.0$
- Collector–Emitter Sustaining Voltage at 100 mAdc
 V_{CEO(sus)} = 80 Vdc (min) BDX33B, BDX334B
 = 100 Vdc (min) BDX33C, BDX334C
- Low Collector–Emitter Saturation Voltage
 V_{CE(sat)} = 2.5 Vdc (max) at I_C = 3.0 Adc – BDX33B, 33C/34B, 34C
- Monolithic Construction with Build–In Base–Emitter Shunt Resistors
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage BDX33B, BDX34B BDX33C, BDX34C	V _{CEO}	80 100	Vdc
Collector–Base Voltage BDX33B, BDX34B BDX33C, BDX34C	V _{CB}	80 100	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current Continuous Peak	Ι _C	10 15	Adc
Base Current	Ι _Β	0.25	Adc
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above 25°C	PD	70 0.56	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

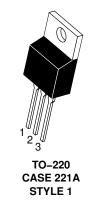
Characteristics	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.78	°C/W



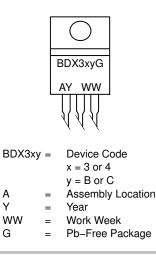
ON Semiconductor®

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DARLINGTON 10 AMPERE COMPLEMENTARY SILICON POWER TRANSISTORS 80–100 VOLTS, 65 WATTS



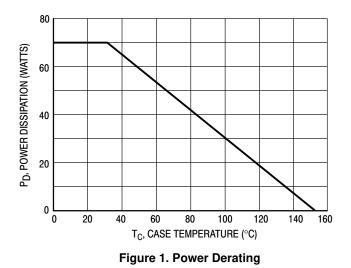
MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



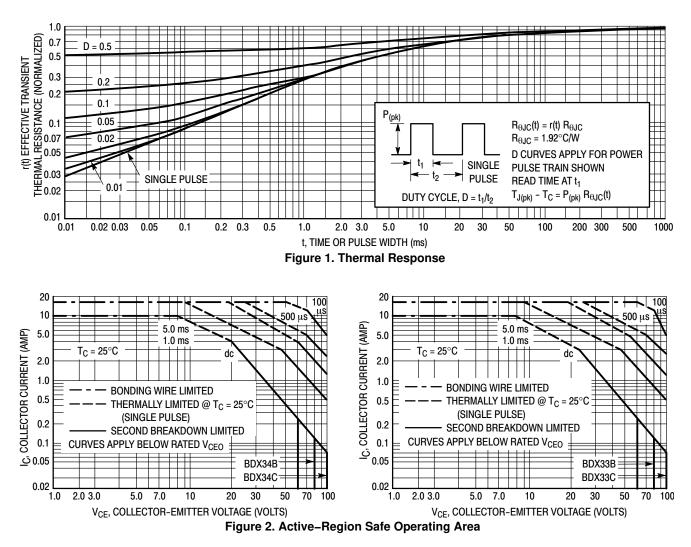
ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage (Note 1) $(I_C = 100 \text{ mAdc}, I_B = 0)$	BDX33B/BDX34B BDX33C/BDX34C	V _{CEO(sus)}	80 100		Vdc
Collector–Emitter Sustaining Voltage (Note 1) $(I_C = 100 \text{ mAdc}, I_B = 0, R_{BE} = 100)$	BDX33B/BDX34B BDX33C/BDX33C	V _{CER(sus)}	80 100		Vdc
Collector–Emitter Sustaining Voltage (Note 1) ($I_C = 100 \text{ mAdc}, I_B = 0, V_{BE} = 1.5 \text{ Vdc}$)	BDX33B/BDX34B BDX33C/BDX34C	V _{CEX(sus)}	80 100		Vdc
Collector Cutoff Current $(V_{CE} = 1/2 \text{ rated } V_{CEO}, I_B = 0)$	T _C = 25°C T _C = 100°C	I _{CEO}		0.5 10	mAdc
Collector Cutoff Current $(V_{CB} = rated V_{CBO}, I_E = 0)$	T _C = 25°C T _C = 100°C	I _{CBO}		1.0 5.0	mAdc
Emitter Cutoff Current ($V_{BE} = 5.0 \text{ Vdc}, I_C = 0$)		I _{EBO}	-	10	mAdc
ON CHARACTERISTICS			•	•	•

DC Current Gain (Note 1) (I _C = 3.0 Adc, V_{CE} = 3.0 Vdc)	BDX33B, 33C/34B, 34C	h _{FE}	750	-	-
Collector–Emitter Saturation Voltage $(I_C = 3.0 \text{ Adc}, I_B = 6.0 \text{ mAdc})$	BDX33B, 33C/34B, 34C	V _{CE(sat)}	-	2.5	Vdc
Base–Emitter On Voltage (I _C = 3.0 Adc, V _{CE} = 3.0 Vdc)	BDX33B, 33C/34B, 34C	$V_{BE(on)}$	-	2.5	Vdc
Diode Forward Voltage (I _C = 8.0 Adc)		V _F	-	4.0	Vdc

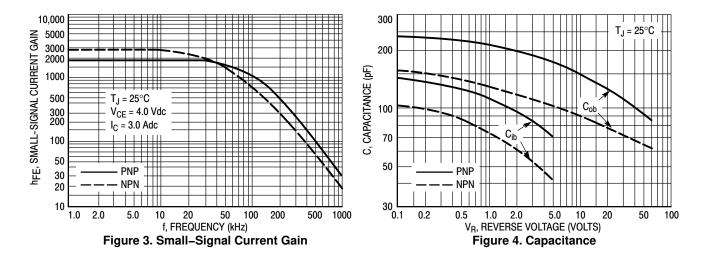
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%. 2. Pulse Test non repetitive: Pulse Width = 0.25 seconds.

BDX33B, BDX33C (NPN) BDX34B, BDX34C (PNP)



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 3 is based on $T_{J(pk)}$

= 150°C; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} = 150$ °C. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.



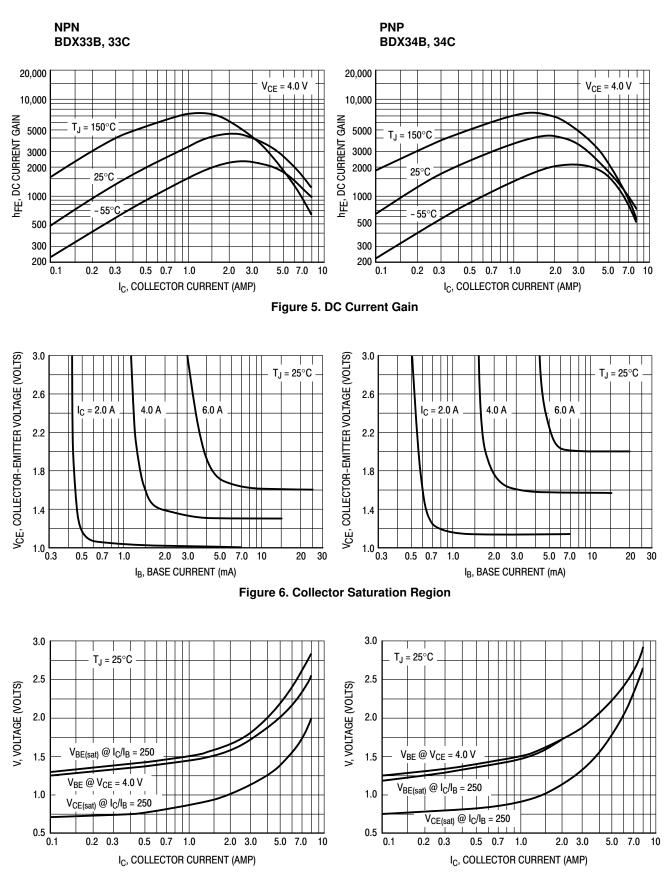


Figure 7. "On" Voltages

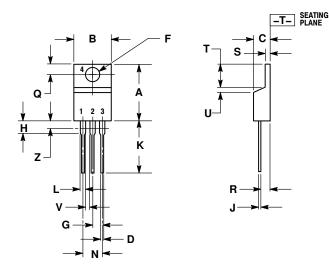
ORDERING INFORMATION

Device	Package	Shipping [†]
BDX33BG	TO-220 (Pb-Free)	50 Units / Rail
BDX33CG	TO-220 (Pb-Free)	50 Units / Rail
BDX34BG	TO-220 (Pb-Free)	50 Units / Rail
BDX34CG	TO-220 (Pb-Free)	50 Units / Rail

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AH**



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.415	9.66	10.53
С	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
н	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
κ	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
Ν	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
۷	0.045		1.15	
Ζ		0.080		2.04

STYLE 1: BASE PIN 1. 2. COLLECTOR FMITTER 3 COLLECTOR

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