

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





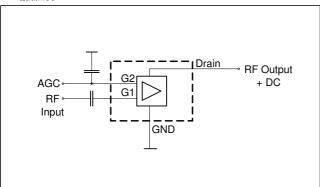


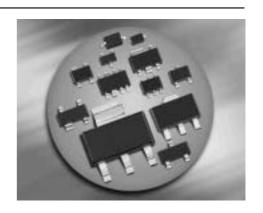


Silicon N-Channel MOSFET Tetrode

- For low noise, high gain controlled input stages up to 1 GHz
- Operating voltage 5V
- Integrated biasing network
- Pb-free (RoHS compliant) package 1)
- Qualified according AEC Q101







ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Package	Pin Configuration						Marking
BF1005	SOT143	1=S	2=D	3=G2	4=G1	-	-	MZs
BF1005R	SOT143R	1=D	2=S	3=G1	4=G2	-	-	MZs

Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	8	V
Continuous drain current	I _D	25	mA
Gate 1/ gate 2-source current	±/ _{G1/2SM}	10	
Gate 1 (external biasing)	+V _{G1SE}	3	V
Total power dissipation	P_{tot}	200	mW
<i>T</i> _S ≤ 76 °C			
Storage temperature	$T_{ m stg}$	-55 150	°C
Channel temperature	T_{ch}	150	

¹Pb-containing package may be available upon special request

Note:

It is not recommended to apply external DC-voltage on Gate 1 in active mode.

1



Thermal Resistance

Parameter	Symbol	Value	Unit
Channel - soldering point1)	R _{thchs}	≤ 370	K/W

Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit	
			typ.	max.	1	
DC Characteristics				•		
Drain-source breakdown voltage	$V_{(BR)DS}$	12	-	-	V	
$I_{D} = 650 \ \mu\text{A}, \ V_{\text{G1S}} = 0 \ , \ V_{\text{G2S}} = 0$						
Gate1-source breakdown voltage	+V _{(BR)G1SS}	8	-	12		
$+I_{G1S} = 10 \text{ mA}, \ V_{G2S} = 0 \ , \ V_{DS} = 0$, ,					
Gate2 source breakdown voltage	±V _{(BR)G2SS}	8	-	13		
$\pm I_{G2S} = 10 \text{ mA}, \ V_{G1S} = 0 \ , \ V_{DS} = 0$						
Gate1-source leakage current	+/ _{G1SS}	-	100	-	μΑ	
$V_{\text{G1S}} = 0$, $V_{\text{G2S}} = 6 \text{ V}$						
Gate 2 source leakage current	±/ _{G2SS}	-	-	50	nA	
$\pm V_{\rm G2S} = 8 \text{ V}, \ V_{\rm G1S} = 0 \ , \ V_{\rm DS} = 0$						
Drain current	l _{DSS}	-	-	1.5	mA	
$V_{\text{DS}} = 5 \text{ V}, \ V_{\text{G1S}} = 0 \ , \ V_{\text{G2S}} = 4 \text{ V}$						
Operating current (selfbiased)	l _{DSO}	8	10	16		
$V_{DS} = 5 \text{ V}, \ V_{G2S} = 4 \text{ V}$						
Gate2-source pinch-off voltage	V _{G2S(p)}	-	1	-	V	
$V_{\rm DS} = 5 \text{ V}, I_{\rm D} = 100 \ \mu\text{A}$						

 $^{^{1}}$ For calculation of R_{thJA} please refer to Application Note Thermal Resistance

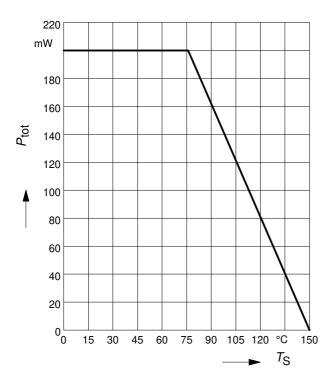


Electrical Characteristics at $T_A = 25$ °C, unless otherwise specified

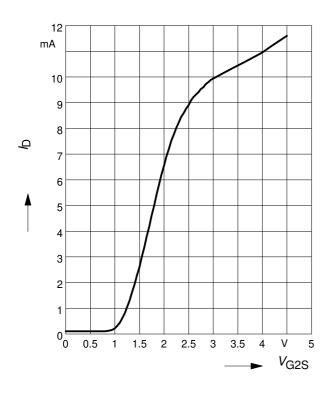
Parameter	Symbol		Values	Values		
		min.	typ.	max.		
AC Characteristics (verified by random sampling)						
Forward transconductance	g_{fs}	20	24	-	mS	
$V_{DS} = 5 \text{ V}, \ V_{G2S} = 4.5 \text{ V}$						
Gate1 input capacitance	C_{g1ss}	-	2.1	2.5	pF	
$V_{DS} = 5 \text{ V}, \ V_{G2S} = 4 \text{ V}, \ f = 10 \text{ MHz}$						
Output capacitance	C_{dss}	-	1.3	-		
$V_{DS} = 5 \text{ V}, \ V_{G2S} = 4 \text{ V}, \ f = 10 \text{ MHz}$						
Power gain (self biased)	G_{p}	17	19	-	dB	
$V_{DS} = 5 \text{ V}, \ V_{G2S} = 4 \text{ V}, \ f = 800 \text{ MHz}$	·					
Noise figure	F	-	1.6	2.5	dB	
$V_{DS} = 5 \text{ V}, \ V_{G2S} = 4 \text{ V}, \ f = 800 \text{ MHz}$						
Gain control range	ΔG_{p}	40	50	-		
$V_{DS} = 5 \text{ V}, \ V_{G2S} = 4 \text{V}0 \text{V}, \ f = 800 \text{ GHz}$						



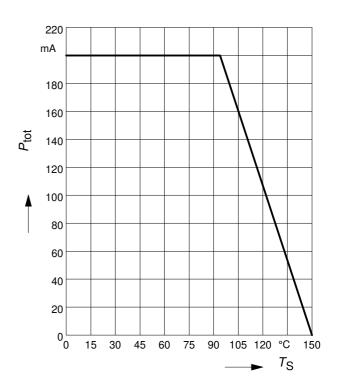
Total power dissipation $P_{tot} = f(T_S)$ BF1005, BF1005R



Drain current $I_D = f(V_{G2S})$



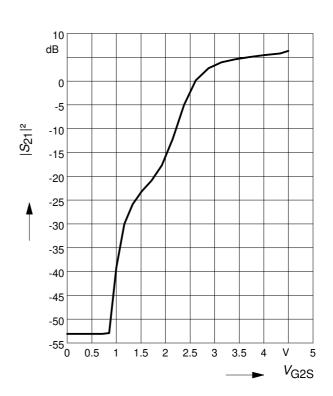
Total power dissipation $P_{tot} = f(T_S)$ BF1005W



Insertion power gain

$$|S_{21}|^2 = f(V_{G2S})$$

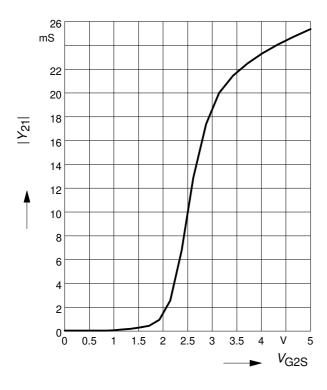
4



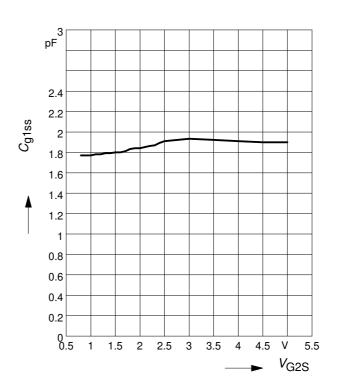


Forward transfer admittance

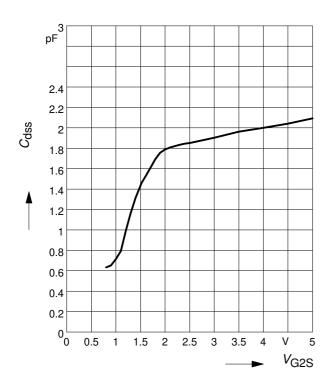
 $|Y_{21}| = f(V_{G2S})$



Gate 1 input capacitance $C_{g1ss} = f(V_{g2s})$ f = 200MHz

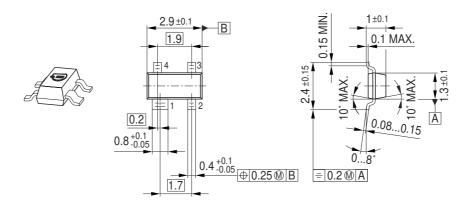


Output capacitance $C_{dss} = f(V_{G2S})$ f = 200MHz

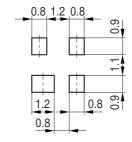




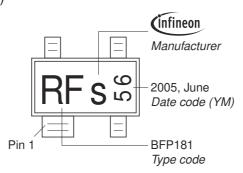
Package Outline



Foot Print

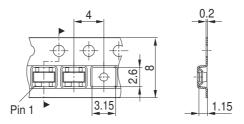


Marking Layout (Example)



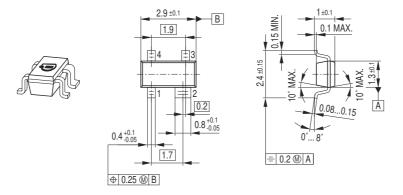
Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel

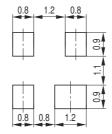




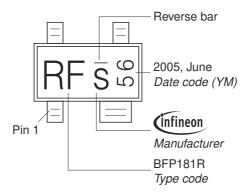
Package Outline



Foot Print

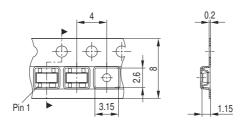


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel





Edition 2006-02-01 Published by Infineon Technologies AG 81726 München, Germany © Infineon Technologies AG 2007. All Rights Reserved.

Attention please!

The information given in this dokument shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie"). With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office (www.infineon.com).

Warnings

Due to technical requirements components may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in life-support devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system.

Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.

8

2007-04-20