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# BFG92A/X

### **NPN 5 GHz wideband transistor**

Rev. 06 — 12 March 2008

**Product data sheet** 

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### **NPN 5 GHz wideband transistor**

### BFG92A/X

#### **FEATURES**

- High power gain
- Low noise figure
- Gold metallization ensures excellent reliability.

### **APPLICATIONS**

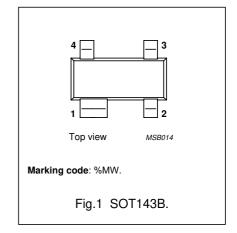
Wideband applications in the UHF and microwave range.

#### **DESCRIPTION**

Silicon NPN transistor in a 4-pin, dual-emitter SOT143B plastic package.

### **PINNING**

PIN	DESCRIPTION
1	collector
2	emitter
3	base
4	emitter



#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage		_	_	20	V
V <sub>CEO</sub>	collector-emitter voltage		_	_	15	V
I <sub>C</sub>	collector current (DC)		_	_	25	mA
P <sub>tot</sub>	total power dissipation	T <sub>s</sub> ≤ 60 °C	_	_	400	mW
C <sub>re</sub>	feedback capacitance	$I_C = i_c = 0; V_{CB} = 10 \text{ V}; f = 1 \text{ MHz}$	_	0.35	_	рF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 15 mA; V <sub>CE</sub> = 10 V; f = 500 MHz	3.5	5	_	GHz
G <sub>UM</sub>	maximum unilateral power gain	$I_{C}$ = 15 mA; $V_{CE}$ = 10 V; $T_{amb}$ = 25 °C; $f$ = 1 GHz	_	16	_	dB
		$I_{C}$ = 15 mA; $V_{CE}$ = 10 V; $T_{amb}$ = 25 °C; $f$ = 2 GHz	_	11	_	dB
F	noise figure	$\begin{split} \Gamma_{\text{S}} &= \Gamma_{\text{opt}}; \ I_{\text{C}} = 5 \ \text{mA}; \ V_{\text{CE}} = 10 \ \text{V}; \\ T_{\text{amb}} &= 25 \ ^{\circ}\text{C}; \ \text{f} = 1 \ \text{GHz} \end{split}$	_	2	_	dB

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### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	20	٧
$V_{CEO}$	collector-emitter voltage	open base	_	15	٧
V <sub>EBO</sub>	emitter-base voltage	open collector	_	2	٧
I <sub>C</sub>	collector current (DC)		_	25	mA
P <sub>tot</sub>	total power dissipation	T <sub>s</sub> ≤ 60 °C; note 1	_	400	mW
T <sub>stg</sub>	storage temperature range		-65	150	°C
Tj	junction temperature		_	175	°C

#### Note

1.  $T_{\text{s}}$  is the temperature at the soldering point of the collector pin.

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-s</sub>	thermal resistance from junction to soldering point	note 1	290	K/W

#### Note

1.  $T_s$  is the temperature at the soldering point of the collector pin.

#### **CHARACTERISTICS**

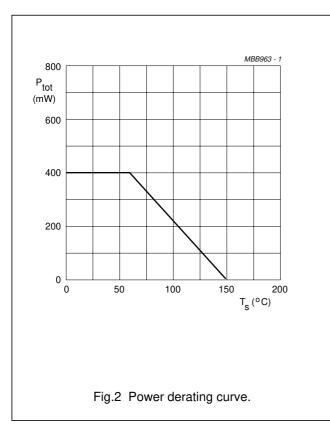
 $T_i = 25$  °C unless otherwise specified.

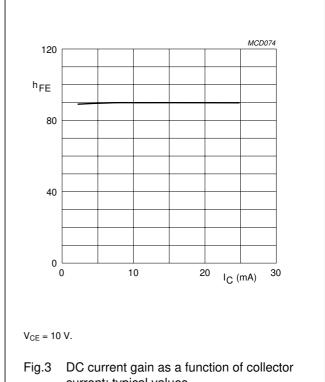
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector leakage current	I <sub>E</sub> = 0; V <sub>CB</sub> = 10 V	_	_	50	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 15 mA; V <sub>CE</sub> = 10 V	65	90	135	
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	0.6	_	pF
Ce	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	0.9	-	pF
C <sub>re</sub>	feedback capacitance	$I_C = i_c = 0$ ; $V_{CB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	0.35	_	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 15 mA; V <sub>CE</sub> = 10 V; f = 500 MHz	3.5	5	_	GHz
G <sub>UM</sub>	maximum unilateral power gain; note 1	$I_C = 15 \text{ mA}; V_{CE} = 10 \text{ V};$ $T_{amb} = 25 ^{\circ}C; f = 1 \text{ GHz}$	_	16	_	dB
		$I_C = 15 \text{ mA}; V_{CE} = 10 \text{ V};$ $T_{amb} = 25 ^{\circ}\text{C}; f = 2 \text{ GHz}$	_	11	_	dB
F	noise figure	$\Gamma_{\text{s}} = \Gamma_{\text{opt}}$ ; $I_{\text{C}} = 5$ mA; $V_{\text{CE}} = 10$ V; $T_{\text{amb}} = 25$ °C; $f = 1$ GHz	_	2	_	dB
		$\Gamma_{s} = \Gamma_{opt}$ ; $I_{C} = 5$ mA; $V_{CE} = 10$ V; $T_{amb} = 25$ °C; $f = 2$ GHz	_	3	_	dB

Note

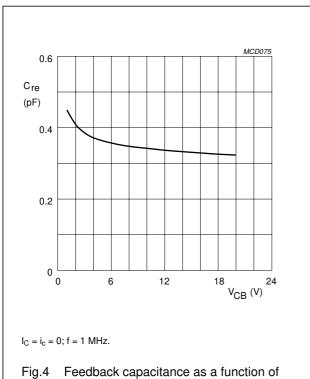
1.  $G_{UM}$  is the maximum unilateral power gain, assuming  $S_{12}$  is zero and  $G_{UM} = 10 \log \frac{|S_{21}|^2}{(1-|S_{11}|^2)(1-|S_{22}|^2)}$  dB.

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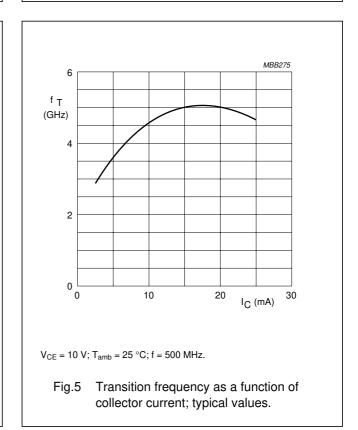




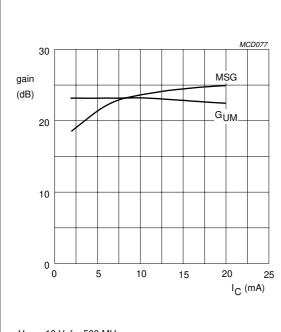
current; typical values.



collector-base voltage; typical values.



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 $V_{CE}$  = 10 V; f = 500 MHz.

Fig.6 Gain as a function of collector current; typical values.

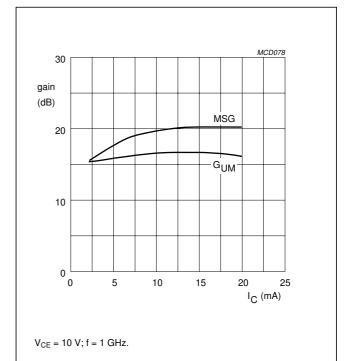


Fig.7 Gain as a function of collector current; typical values.

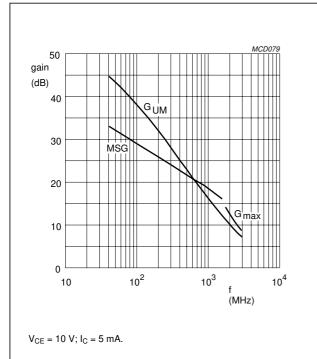
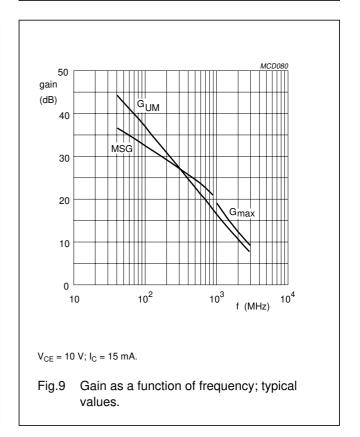
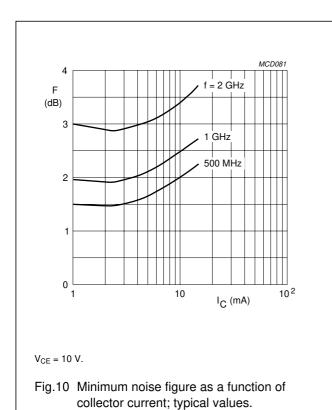
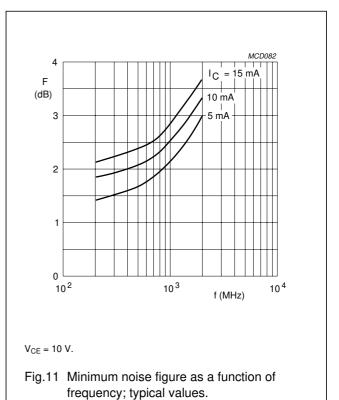


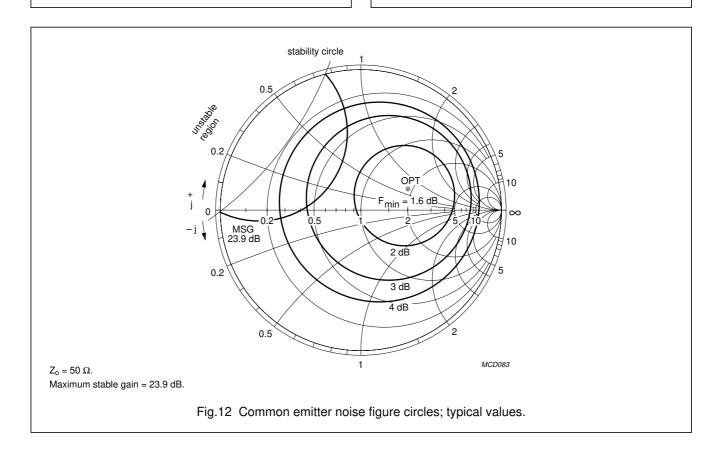
Fig.8 Gain as a function of frequency; typical values.



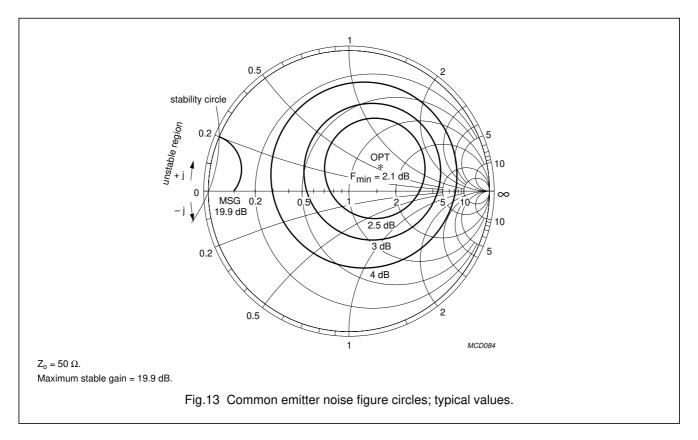
### NPN 5 GHz wideband transistor

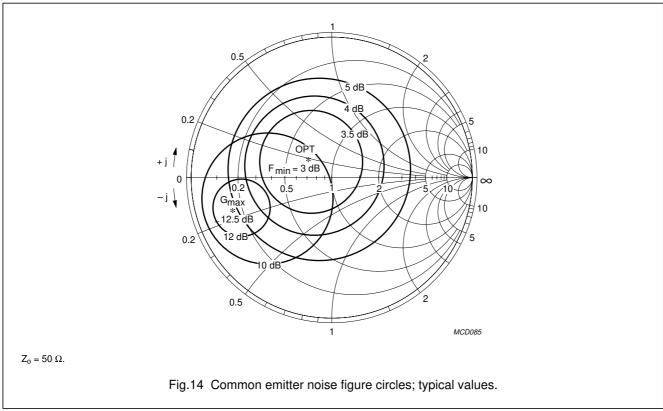




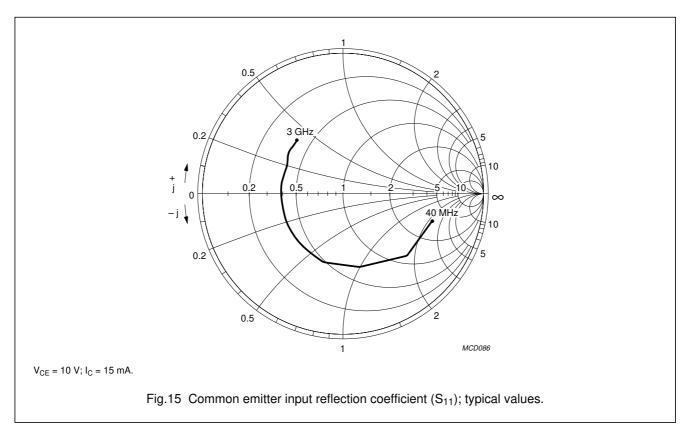


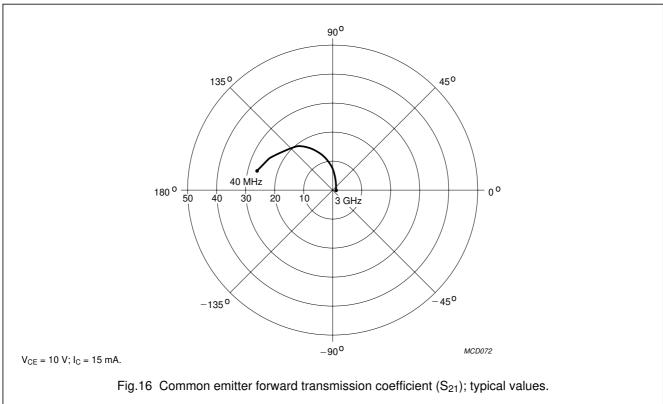
### NPN 5 GHz wideband transistor



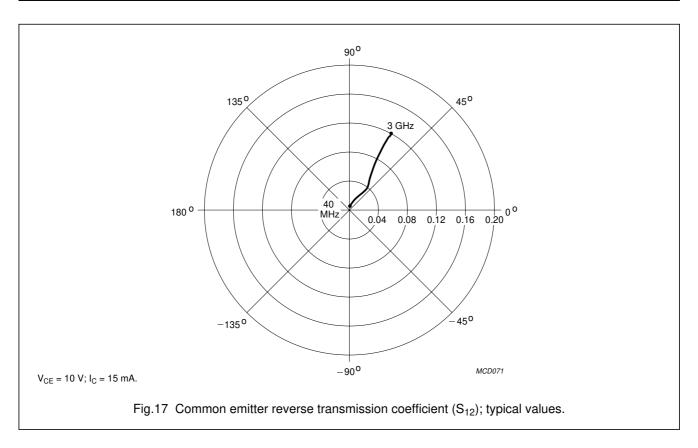


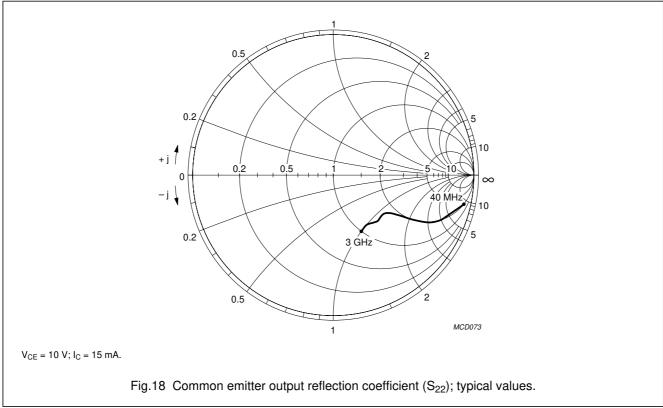
### NPN 5 GHz wideband transistor





### NPN 5 GHz wideband transistor





### BFG92A/X

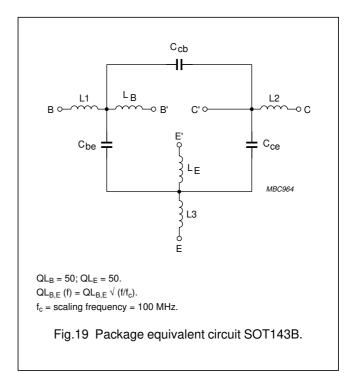
### SPICE parameters for BFR90A/X die

SEQUENCE No.	PARAMETER	VALUE	UNIT
1	IS	411.8	аА
2	BF	102.6	_
3	NF	997.2	m
4	VAF	62.67	٧
5	IKF	3.200	Α
6	ISE	4.010	fA
7	NE	1.577	_
8	BR	18.10	_
9	NR	996.2	m
10	VAR	3.369	V
11	IKR	1.281	Α
12	ISC	279.9	аА
13	NC	1.075	_
14	RB	10.00	Ω
15	IRB	1.000	μΑ
16	RBM	10.00	Ω
17	RE	1.164	Ω
18	RC	2.320	Ω
19 (note 1)	XTB	0.000	_
20 (note 1)	EG	1.110	eV
21 (note 1)	XTI	3.000	_
22	CJE	890.5	fF
23	VJE	600.0	mV
24	MJE	258.5	m
25	TF	15.49	ps
26	XTF	39.14	_
27	VTF	2.152	V
28	ITF	213.7	mA
29	PTF	0.000	deg
30	CJC	546.5	fF
31	VJC	380.8	mV
32	MJC	202.9	m
33	XCJC	150.0	m
34	TR	5.618	ns
35 (note 1)	CJS	0.000	F

SEQUENCE No.	PARAMETER	VALUE	UNIT
36 (note 1)	VJS	750.0	mV
37 (note 1)	MJS	0.000	_
38	FC	850.0	m

#### Note

1. These parameters have not been extracted, the default values are shown.



### List of components (see Fig.19)

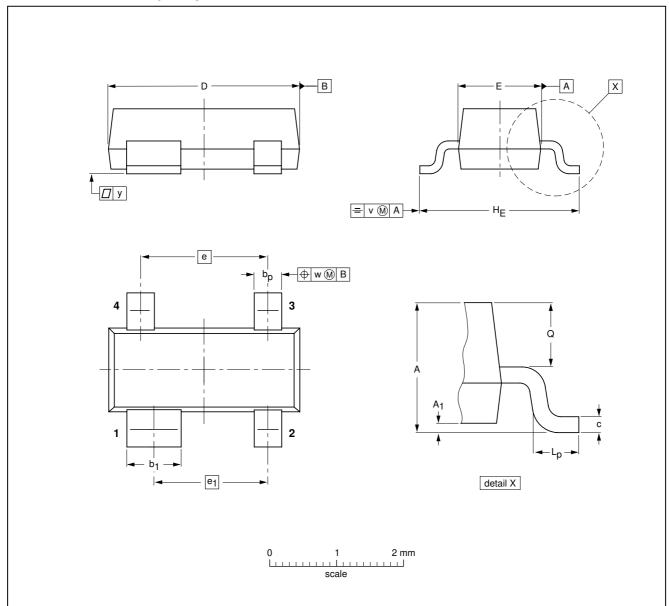
DESIGNATION	VALUE	UNIT
C <sub>be</sub>	84	fF
C <sub>cb</sub>	17	fF
C <sub>ce</sub>	191	fF
L1	0.12	nH
L2	0.21	nH
L3	0.06	nH
L <sub>B</sub>	0.95	nH
LE	0.40	nH

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### **PACKAGE OUTLINES**

### Plastic surface mounted package; 4 leads

SOT143B



#### **DIMENSIONS** (mm are the original dimensions)

UNIT	A	A <sub>1</sub> max	bp	b <sub>1</sub>	С	D	E	е	e <sub>1</sub>	HE	L <sub>p</sub>	Q	v	w	у
mm	1.1 0.9	0.1	0.48 0.38	0.88 0.78	0.15 0.09	3.0 2.8	1.4 1.2	1.9	1.7	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1	0.1

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT143B					97-02-28

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#### **Data sheet status**

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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- [2] The term 'short data sheet' is explained in section "Definitions"
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## **Revision history**

### **Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFG92AX_N_6	20080312	Product data sheet	-	BFG92AX_N_5
Modifications:	<ul> <li>Characteristi</li> </ul>	cs Table; DC current gain value	e changed	
BFG92AX_N_5	20071126	Product data sheet	-	BFG92AX_4
BFG92AX_4 (9397 750 04344)	19980923	Product specification	-	BFG92SERIES_3
BFG92SERIES_3	19950912	Product specification	-	BFG92SERIES_2
BFG92SERIES_2	19921101	Product specification	-	BFG92_SERIES_1
BFG92_SERIES_1	-	-	-	-

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