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# **BFQ19**

# NPN 5 GHz wideband transistor

Rev. 03 — 28 September 2007

**Product data sheet** 

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**NXP Semiconductors** 



# NPN 5 GHz wideband transistor

**BFQ19** 

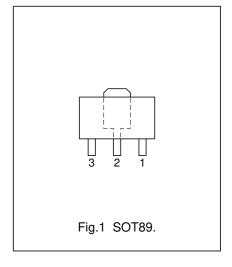
### **DESCRIPTION**

NPN transistor in a SOT89 plastic envelope intended for application in thick and thin-film circuits. It is primarily intended for use in UHF and microwave amplifiers such as in aerial amplifiers, radar systems, oscilloscopes, spectrum analysers etc.

The transistor features very low intermodulation distortion and high power gain. Due to its very high transition frequency, it also has excellent wideband properties and low noise up to high frequencies.

### **PINNING**

PIN	PIN DESCRIPTION			
Code: FB				
1	emitter			
2	collector			
3	base			



### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V <sub>CEO</sub>	collector-emitter voltage	open base	_	15	٧
Ic	DC collector current		_	100	mA
P <sub>tot</sub>	total power dissipation	up to $T_s = 145$ °C (note 1)	_	1	W
f <sub>T</sub>	transition frequency	$I_c = 50 \text{ mA}; V_{CE} = 10 \text{ V}; f = 500 \text{ MHz};$ $T_j = 25 ^{\circ}\text{C}$	5.5	_	GHz
C <sub>re</sub>	feedback capacitance	$I_c = 10 \text{ mA}; V_{CE} = 10 \text{ V}; f = 1 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	1.3	_	pF
F	noise figure	$I_c = 50 \text{ mA}; V_{CE} = 10 \text{ V}; Z_s = \text{opt.};$ $f = 500 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	3.3	_	dB

### **LIMITING VALUES**

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	20	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	15	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	3.3	V
I <sub>C</sub>	DC collector current		_	100	mA
I <sub>CM</sub>	peak collector current	f > 1 MHz	_	150	mA
P <sub>tot</sub>	total power dissipation	up to T <sub>s</sub> = 145 °C (note 1)	_	1	W
T <sub>stg</sub>	storage temperature		-65	150	°C
Ti	junction temperature		_	175	°C

### Note

1.  $T_s$  is the temperature at the soldering point of the collector tab.

**NXP Semiconductors** Product specification

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## THERMAL RESISTANCE

SYMBOL	PARAMETER	CONDITIONS	THERMAL RESISTANCE
R <sub>th j-s</sub>	thermal resistance from junction to soldering point	up to T <sub>s</sub> = 145 °C (note 1)	30 K/W

### Note

1.  $T_{\text{s}}$  is the temperature at the soldering point of the collector tab.

## **CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 10 V	_	_	100	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 70 mA; V <sub>CE</sub> = 10 V	25	80	_	
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	1.6	_	рF
C <sub>e</sub>	emitter capacitance	I <sub>C</sub> = i <sub>c</sub> = 0; V <sub>EB</sub> = 0.5 V; f = 1 MHz	_	5	_	рF
C <sub>re</sub>	feedback capacitance	$I_{C} = 10 \text{ mA}; V_{CE} = 10 \text{ V}; f = 1 \text{ MHz};$ $T_{amb} = 25 ^{\circ}\text{C}$	_	1.3	_	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 70 mA; V <sub>CE</sub> = 10 V; f = 500 MHz	4.4	5.5	_	GHz
G <sub>UM</sub>	maximum unilateral power gain (note 1)	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 10 V; f = 500 MHz; T <sub>amb</sub> = 25 °C	_	11.5	_	dB
		I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 10 V; f = 800 MHz; T <sub>amb</sub> = 25 °C	_	7.5	_	dB
F	noise figure	I <sub>C</sub> = 50 mA; V <sub>CE</sub> = 10 V; Z <sub>s</sub> = opt.; f = 500 MHz; T <sub>amb</sub> = 25 °C	_	3.3	_	dB

### Note

1. 
$$G_{UM}$$
 is the maximum unilateral power gain, assuming  $S_{12}$  is zero and 
$$G_{UM} = 10 \log \frac{\left|S_{21}\right|^2}{\left(1-\left|S_{11}\right|^2\right)\left(1-\left|S_{22}\right|^2\right)} \, dB.$$

**NXP Semiconductors** Product specification

# NPN 5 GHz wideband transistor

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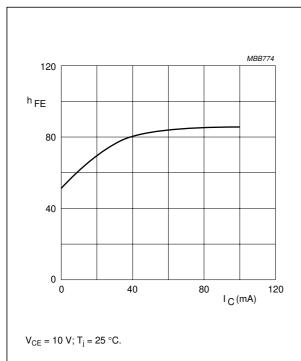
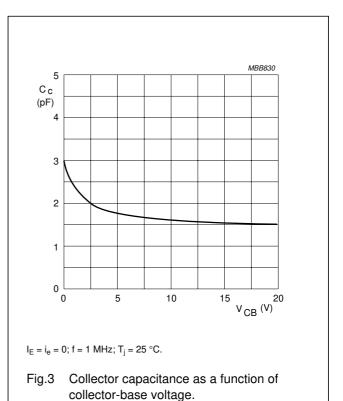
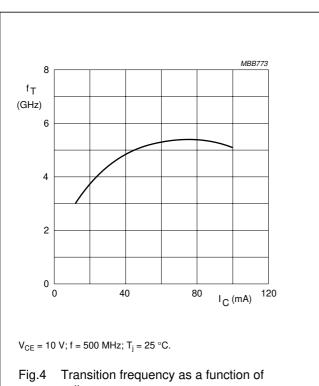


Fig.2 DC current gain as a function of collector current.





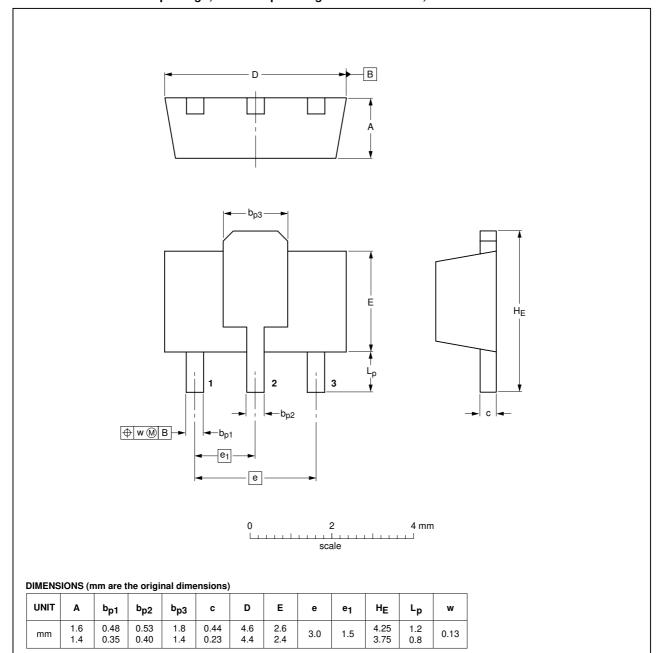
# NPN 5 GHz wideband transistor

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## **PACKAGE OUTLINE**

# Plastic surface-mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT89		TO-243	SC-62			<del>06-03-16</del> 06-08-29	

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#### NPN 5 GHz wideband transistor

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### **Data sheet status**

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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### **NPN 5 GHz wideband transistor**

# **Revision history**

## **Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
BFQ19_N_3	20070928	Product data sheet	-	BFQ19_CNV_2
Modifications:	<ul> <li>Fig. 1 and p</li> </ul>	ackage outline updated		
BFQ19_CNV_2	19950901	Product specification	-	-

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