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Product data sheet

1. Product profile

1.1 General description

NPN wideband transistor in a SOT89 plastic package.

1.2 Features and benefits

- High gain
- High output voltage
- Low noise

- Gold metallization ensures excellent reliability
- Low thermal resistance.

1.3 Applications

VHF, UHF and CATV amplifiers.

1.4 Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|---------------------------|--|-----|-----|-----|------|
| V_{CBO} | collector-base voltage | open emitter | | | 20 | V |
| V_{CES} | collector-emitter voltage | $R_{BE} = 0$ | | | 15 | V |
| V_{EBO} | emitter-base voltage | open collector | | | 2.5 | V |
| I _C | collector current (DC) | | | | 120 | mA |
| P _{tot} | total power dissipation | $T_s \le 60 ^{\circ}C$ | [1] | | 1.2 | W |
| h _{FE} | DC current gain | $I_{C} = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ $T_{j} = 25 \text{ °C}$ | 100 | 120 | 250 | |
| f _T | transition frequency | I_C = 40 mA; V_{CE} = 8 V; f = 1 GHz; T_{amb} = 25 °C | | 9 | | GHz |
| $ s_{21} ^2$ | insertion power gain | I_C = 40 mA; V_{CE} = 8 V; f = 900 MHz; T_{amb} = 25 °C | 12 | 13 | | dB |
| F | noise figure | I_{C} = 40 mA; V_{CE} = 8 V; f = 900 MHz; Γ_{S} = Γ_{opt} | | 1.9 | 2.4 | dB |

^[1] T_s is the temperature at the soldering point of the collector pin.



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2. Pinning information

Table 2. Pinning

| | 9 | | |
|-----|-------------|--------------------|----------------|
| Pin | Description | Simplified outline | Graphic symbol |
| 1 | emitter | | • |
| 2 | collector | | |
| 3 | base | 3 2 1 | 3 — |
| | | ÿ <u>-</u> . | sym042 |

3. Ordering information

Table 3. Ordering information

| Type number | Package | | | | |
|-------------|---------|--|---------|--|--|
| | Name | Description | Version | | |
| BFQ540 | - | plastic surface-mounted package; collector pad for good heat transfer; 3 leads | SOT89 | | |

4. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| BFQ540 | N4 |

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|------------------|--------------------------------|-------------------------|-----|------|------|
| V_{CBO} | collector-base voltage | open emitter | | 20 | V |
| V_{CES} | collector-emitter voltage | $R_{BE} = 0$ | | 15 | V |
| V_{EBO} | emitter-base voltage | open collector | | 2.5 | V |
| I _C | collector current (DC) | | | 120 | mA |
| P _{tot} | total power dissipation | $T_s \le 60 ^{\circ}C$ | | 1.2 | W |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| Tj | operating junction temperature | | | 175 | °C |

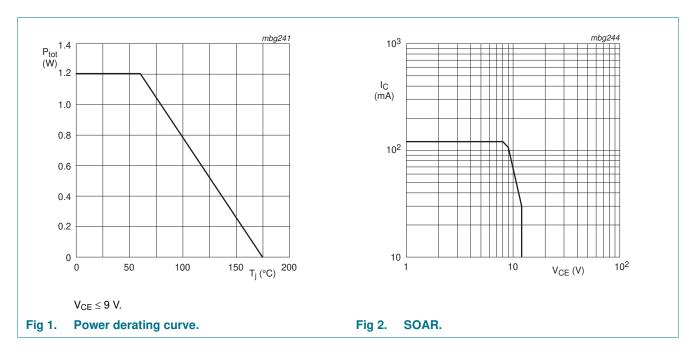
6. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | Тур | Unit |
|---------------------|---|--|-----|------|
| R _{th j-s} | thermal resistance from junction to soldering point | $T_s \le 60$ °C; $P_{tot} = 1.2 \text{ W}$ | 95 | K/W |

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7. Characteristics

Table 7. Characteristics

 $T_i = 25$ °C unless otherwise specified.

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|------------------|--|---|-----|------------|--------------|------|
| $V_{(BR)CBO}$ | collector-base breakdown voltage | open emitter; $I_C = 10 \mu A$; $I_E = 0$ | 20 | | | V |
| $V_{(BR)CES}$ | collector-emitter breakdown voltage | $R_{BE}=0;I_C=40\;\mu A$ | 15 | | | V |
| $V_{(BR)EBO}$ | emitter-base breakdown voltage | $I_E = 100 \ \mu A; \ I_C = 0$ | 2 | | | V |
| I _{CBO} | collector-base leakage current | $V_{CB} = 8 \text{ V}; I_{E} = 0$ | | | 50 | nA |
| I _{EBO} | emitter-base leakage current | $V_{CB} = 1 V; I_C = 0$ | | | 200 | nA |
| h _{FE} | DC current gain | $I_C = 40 \text{ mA}; V_{CE} = 8 \text{ V}$ | 100 | 120 | 250 | |
| f _T | transition frequency | $I_C = 40 \text{ mA}; V_{CE} = 8 \text{ V};$ $f_m = 1 \text{ GHz}$ | | 9 | | GHz |
| C _e | emitter capacitance | $I_C = I_e = 0$; $V_{EB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$ | | 2 | | pF |
| C _{re} | feedback capacitance | $I_C = 0$; $V_{CE} = 8 \text{ V}$; $f = 1 \text{ MHz}$ | | 0.9 | | pF |
| $ s_{21} ^2$ | insertion power gain | I_C = 40 mA; V_{CE} = 8 V; f = 900 MHz; T_{amb} = 25 °C | 12 | 13 | | dB |
| Vo | output voltage | | [1] | 500 | | mV |
| | | | [2] | 350 | | mV |
| d ₂ | second order intermodulation distortion | | [3] | | -53 | dB |
| F | noise figure | I_C = 40 mA; VCE = 8 V; f = 900 MHz; Γ_S = Γ_{opt} | | 1.9 | 2.4 | dB |
| | All information are ideal in this day, man | Proceedings of the second | | @ NIVE E V | 0010 All -:- | |

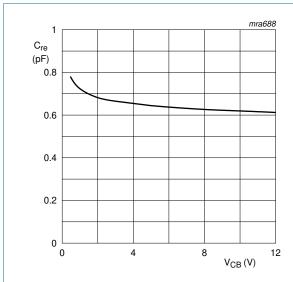
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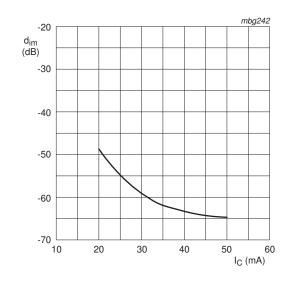
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- [1] $d_{im} = -60 \text{ dB (DIN45004B)}; V_{CE} = 8 \text{ V; } I_{C} = 40 \text{ mA; } R_{L} = 50 \Omega; V_{p} = V_{o}; V_{q} = V_{o} 6 \text{ dB; } V_{r} = V_{o} 6 \text{ dB; } f_{p} = 795.25 \text{ MHz; } f_{q} = 803.25 \text{ MHz; } f_{r} = 805.5 \text{ MHz; } measured \text{ at } f_{p} + f_{q} f_{r} = 793.25 \text{ MHz.}$
- [2] $d_{im} = -60 \text{ dB (DIN 45004B)}$; $I_C = 40 \text{ mA}$; $V_{CE} = 8 \text{ V}$; $R_L = 50 \Omega$; $V_p = V_q = V_o$; $f_p = 806 \text{ MHz}$; $f_q = 810 \text{ MHz}$; measured at $2f_p f_q = 802 \text{ MHz}$.
- [3] $I_C = 40$ mA; $V_{CE} = 8$ V; $R_L = 50$ Ω ; $V_p = V_q = 225$ mV; $f_p = 250$ MHz; $f_q = 560$ MHz; measured at $f_p + f_q = 810$ MHz.



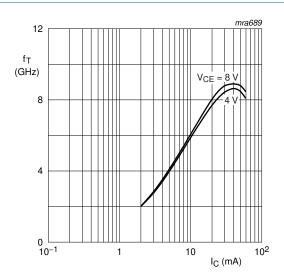
 $I_C = 0$; f = 1 MHz.

Fig 3. Feedback capacitance as a function of collector-base voltage; typical values.



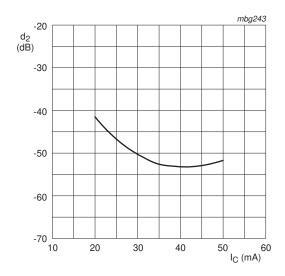
 $V_{CE}=8~V;~V_{o}=475~mV;~R_{L}=50~\Omega.~f_{p}+f_{q}-f_{r}=793.25$ MHz; $T_{amb}=25~^{\circ}C.$

Fig 5. Intermodulation distortion as a function of collector current; typical values.



f = 1 GHz; $T_{amb} = 25 \text{ }^{\circ}\text{C}$.

Fig 4. Transition frequency as a function of collector current; typical values.



 $V_{CE}=8$ V; $V_{o}=225$ mV; $R_{L}=50~\Omega;$ $f_{p}+f_{q}=810$ MHz; $T_{amb}=25~^{\circ}C.$

Fig 6. Second order intermodulation distortion as a function of collector current; typical values.

8. Package outline

Plastic surface-mounted package; exposed die pad for good heat transfer; 3 leads

SOT89

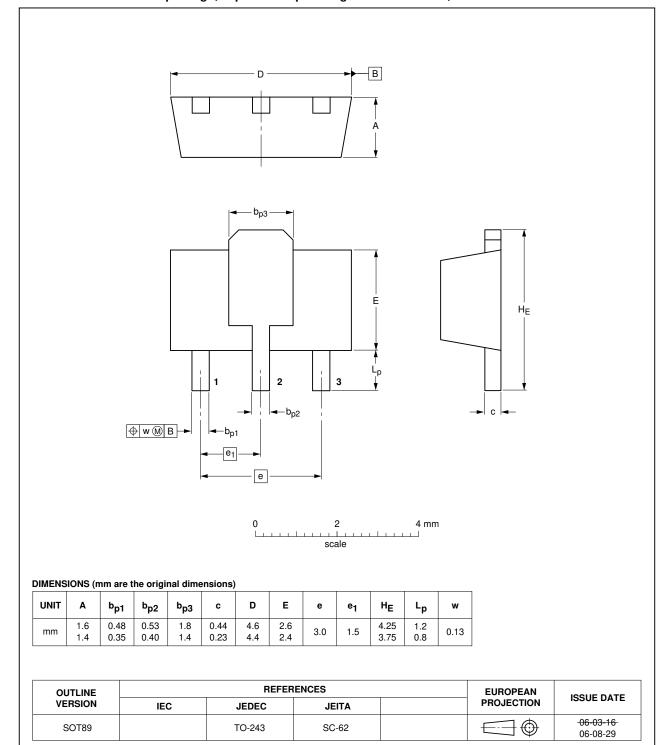


Fig 7. Package outline SOT89 (TO-243).

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9. Revision history

Table 8. Revision history

| - | | | |
|---|---|---|---|
| Release date | Data sheet status | Change notice | Supersedes |
| 20130321 | Product data sheet | - | BFQ540_N_4 |
| | | esigned to comply w | ith the new identity |
| Legal texts h | ave been adapted to the new o | company name whe | re appropriate. |
| Package outl | ine drawings have been updat | ed to the latest vers | ion. |
| V_{EBO} value u | pdated. | | |
| 20070925 | Product data sheet | - | BFQ540_3 |
| 20000523 | Product specification | | BFQ540_2 |
| 19980827 | Product specification | | BFQ540_1 |
| 19950904 | Product specification | | |
| | The format of guidelines of Eegal texts have Package outly VEBO value us 20070925 20000523 19980827 | 20130321 Product data sheet The format of this data sheet has been redeguidelines of NXP Semiconductors. Legal texts have been adapted to the new of Package outline drawings have been updated. V_{EBO} value updated. 20070925 Product data sheet 20000523 Product specification 19980827 Product specification | 20130321 Product data sheet - The format of this data sheet has been redesigned to comply w guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name when Package outline drawings have been updated to the latest vers V_{EBO} value updated. 20070925 Product data sheet - 20000523 Product specification 19980827 Product specification |

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| Document status[1][2] | Product status[3] | Definition |
|--------------------------------|-------------------|---|
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- [2] The term 'short data sheet' is explained in section "Definitions"
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