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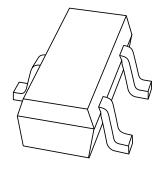






DISCRETE SEMICONDUCTORS

DATA SHEET



BFR505TNPN 9 GHz wideband transistor

Product specification Supersedes data of 2000 Mar 14 2000 May 17



NPN 9 GHz wideband transistor

BFR505T

FEATURES

- · Low current consumption
- · High power gain
- · Low noise figure
- High transition frequency
- Gold metallization ensures excellent reliability
- SOT416 (SC-75) package.

APPLICATIONS

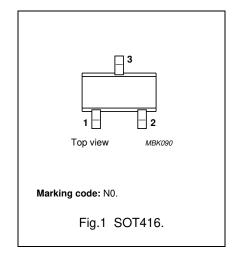
Low power amplifiers, oscillators and mixers particularly in RF portable communication equipment (cellular phones, cordless phones and pagers) up to 2 GHz.

DESCRIPTION

NPN transistor in a plastic SOT416 (SC-75) package.

PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	_	20	٧
V _{CES}	collector-emitter voltage	R _{BE} = 0	_	_	15	٧
I _C	DC collector current		_	_	18	mA
P _{tot}	total power dissipation	$T_s \le 75$ °C; note 1	_	_	150	mW
h _{FE}	DC current gain	$I_C = 5 \text{ mA}; V_{CE} = 6 \text{ V}; T_j = 25 ^{\circ}\text{C}$	60	120	250	
f _T	transition frequency	$I_C = 5 \text{ mA}; V_{CE} = 6 \text{ V}; f = 1 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	_	9	_	GHz
G _{UM}	maximum unilateral power gain	$I_C = 5 \text{ mA}; V_{CE} = 6 \text{ V}; f = 900 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$	_	17	_	dB
F	noise figure	I _C = 1.25 mA; V _{CE} = 6 V; f = 900 MHz; T _{amb} = 25 °C	_	1.2	1.7	dB

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	-	20	٧
V _{CE}	collector-emitter voltage	R _{BE} = 0	_	15	٧
V _{EBO}	emitter-base voltage	open collector	_	2.5	٧
I _C	DC collector current		-	18	mA
P _{tot}	total power dissipation	$T_s \le 75$ °C; note 1	-	150	mW
T _{stg}	storage temperature		-65	+150	°C
T _i	junction temperature		_	150	°C

Note

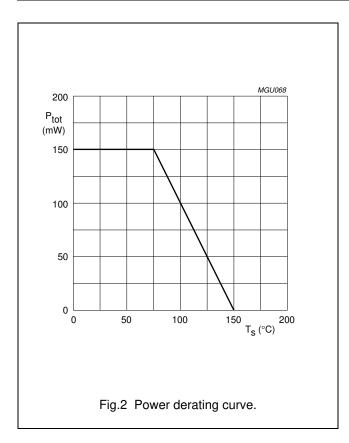
1. T_s is the temperature at the soldering point of the collector pin.

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THERMAL RESISTANCE

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-s}	thermal resistance from junction to soldering point		K/W



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CHARACTERISTICS

 $T_i = 25$ °C; unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 6 V	_	_	50	nA
h _{FE}	DC current gain	I _C = 5 mA; V _{CE} = 6 V	60	120	250	
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 6 \text{ V}$; $f = 1 \text{ MHz}$	_	0.4	-	pF
C _e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = 0.5 \text{ V}$; $f = 1 \text{ MHz}$	-	0.4	-	pF
C _{re}	feedback capacitance	I _C = 0; V _{CB} = 6 V; f = 1 MHz	_	0.3	_	pF
f _T	transition frequency	$I_C = 5 \text{ mA}; V_{CE} = 6 \text{ V}; f = 1 \text{ GHz};$ $T_{amb} = 25 \text{ °C}$	_	9	_	GHz
G _{UM}	maximum unilateral power gain; note 1	I _C = 5 mA; V _{CE} = 6 V; T _{amb} = 25 °C; f = 900 MHz f = 2 GHz	_	17 10	_	dB dB
S ₂₁ ²	insertion power gain	I _C = 5 mA; V _{CE} = 6 V; f = 900 MHz; T _{amb} = 25 °C	13	14	_	dB
F	noise figure	$\Gamma_{\text{S}} = \Gamma_{\text{opt}}; \ I_{\text{C}} = 1.25 \ \text{mA}; \ V_{\text{CE}} = 6 \ \text{V}; \ f = 900 \ \text{MHz}; \ T_{\text{amb}} = 25 \ ^{\circ}\text{C}$	_	1.2	1.7	dB
		$\Gamma_{\text{S}} = \Gamma_{\text{opt}}$; $I_{\text{C}} = 5$ mA; $V_{\text{CE}} = 6$ V; $f = 900$ MHz; $T_{\text{amb}} = 25$ °C	_	1.6	2.1	dB
		$\Gamma_{\text{s}} = \Gamma_{\text{opt}}; \ I_{\text{C}} = 1.25 \text{ mA}; \ V_{\text{CE}} = 6 \text{ V}; \ f = 2 \text{ GHz}; \ T_{\text{amb}} = 25 \text{ °C}$	_	1.9	_	dB
P _{L1}	output power at 1 dB gain compression	$I_C = 5 \text{ mA}; V_{CE} = 6 \text{ V}; R_L = 50 \Omega;$ f = 900 MHz; $T_{amb} = 25 ^{\circ}\text{C}$	_	4	_	dBm
ITO	third-order intercept point	note 2	=	10	=	dBm

Notes

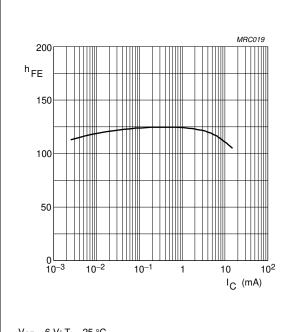
1. G_{UM} is the maximum unilateral power gain, assuming S_{12} is zero and

$$G_{UM} = 10 \log \frac{|S_{21}|^2}{(1 - |S_{11}|^2)(1 - |S_{22}|^2)} dB$$

2. $I_C = 5$ mA; $V_{CE} = 6$ V; $R_L = 50$ Ω ; f = 900 MHz; $T_{amb} = 25$ °C; $f_p = 900$ MHz; $f_q = 902$ MHz; measured at $f_{(2p-q)} = 898$ MHz and at $f_{(2q-p)} = 904$ MHz.

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 V_{CE} = 6 V; T_j = 25 °C.

Fig.3 DC current gain as a function of collector current.

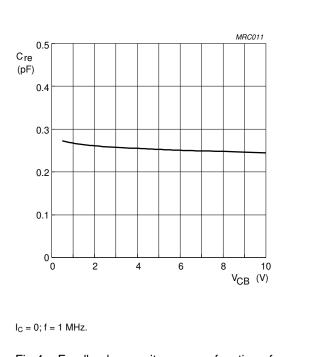
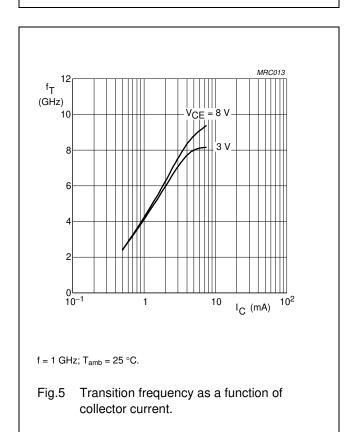


Fig.4 Feedback capacitance as a function of collector-base voltage.

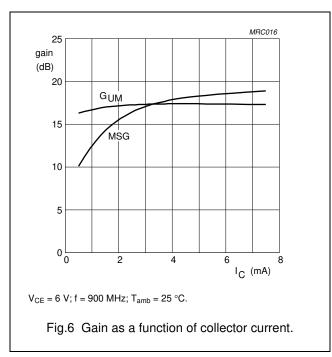


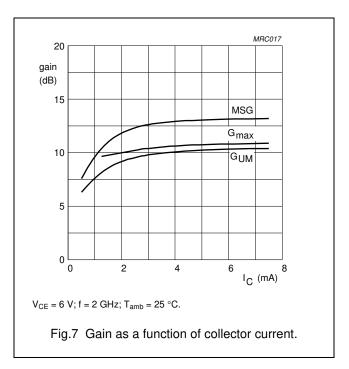
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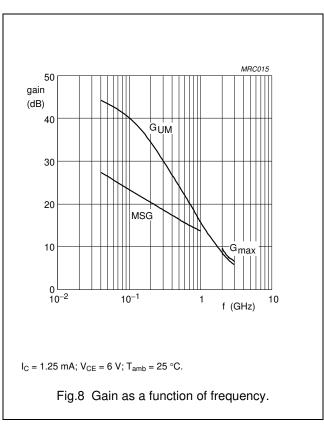
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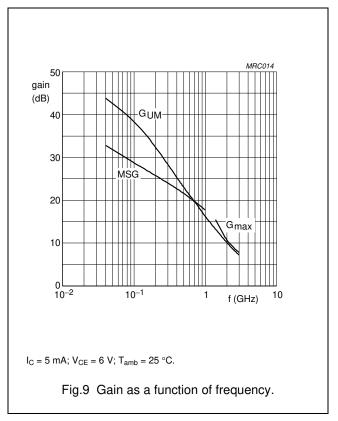
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In Figs 6 to 9, G_{UM} = maximum unilateral power gain; MSG = maximum stable gain; G_{max} = maximum available gain.







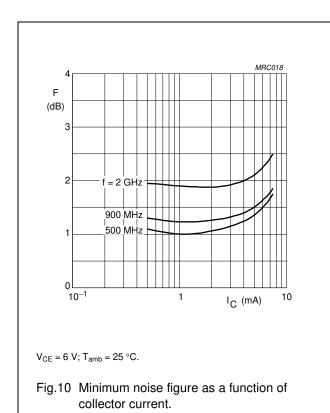


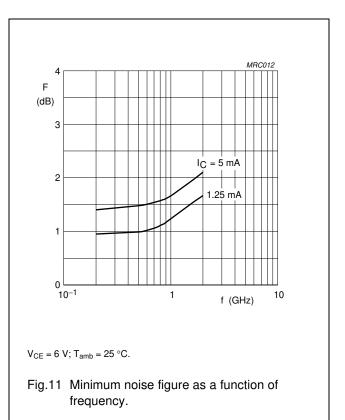
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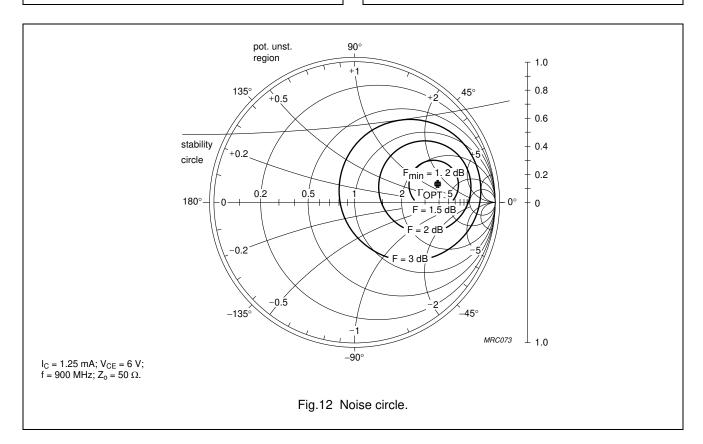
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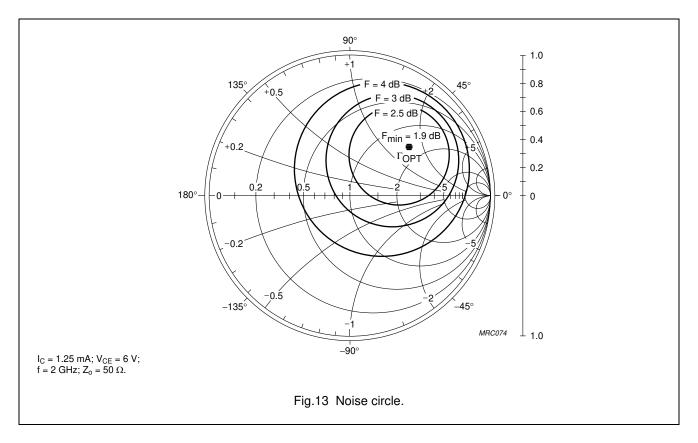


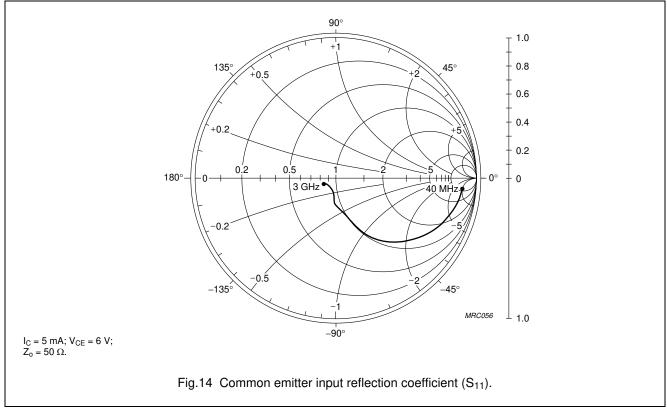




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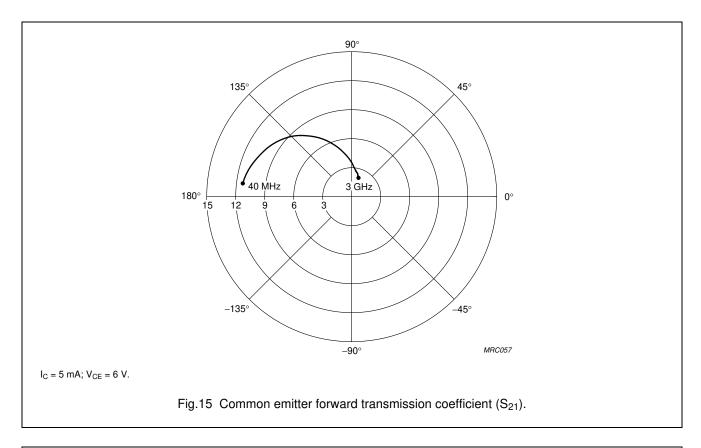
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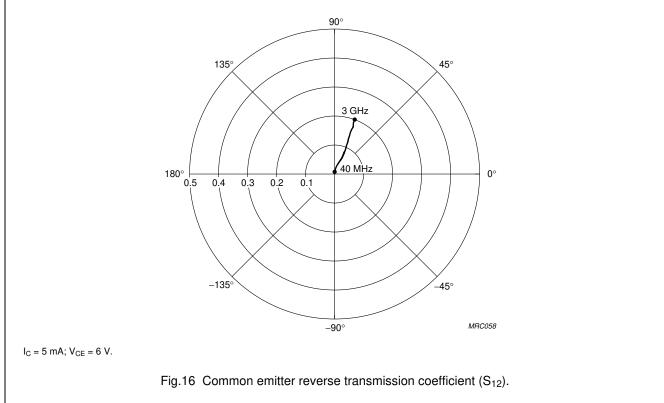




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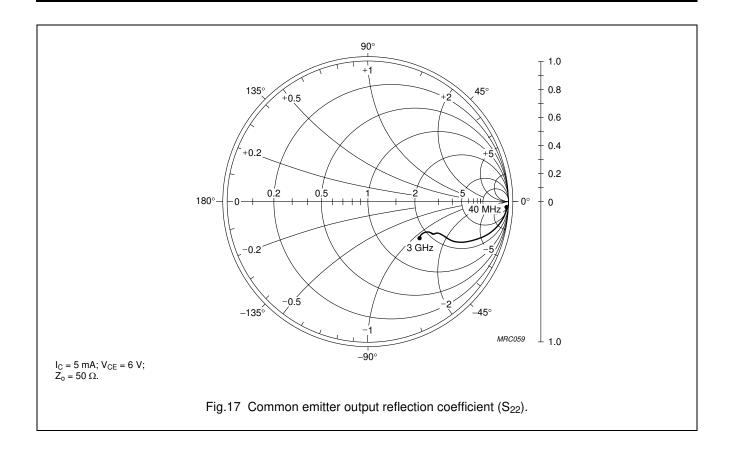
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PACKAGE OUTLINE

UNIT

 mm

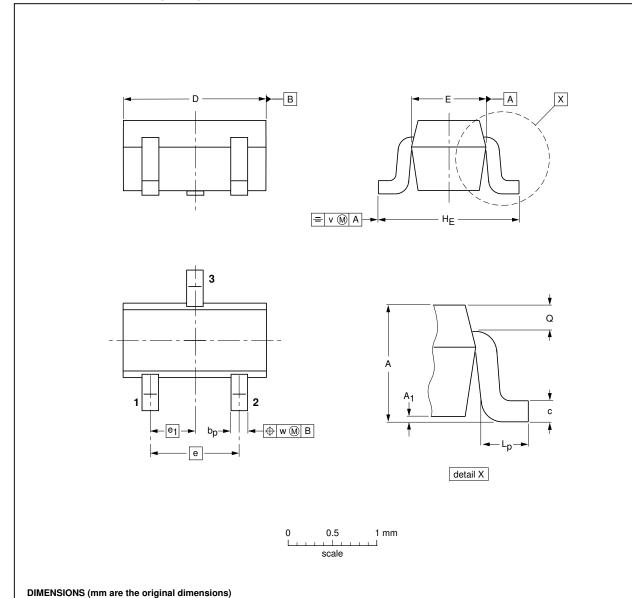
max

0.1

0.95 0.60 0.30 0.15 0.25 0.10

Plastic surface-mounted package; 3 leads

SOT416



OUTLINE	REFERENCES			EUROPEAN	ICCUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT416			SC-75			04-11-04 06-03-16

 ${\sf H}_{\sf E}$

1.75 1.45 L_{p}

0.45

0.15

Q

0.23

0.13

٧

w

0.2

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D

1.8 1.4 Ε

0.9 0.7 е

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DATA SHEET STATUS

DOCUMENT STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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