imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





BFR520 NPN 9 GHz wideband transistor Rev. 4 — 13 September 2011

Product data sheet

1. Product profile

1.1 General description

The BFR520 is an NPN silicon planar epitaxial transistor in a SOT23 plastic package.

1.2 Features and benefits

- High power gain
- Low noise figure
- High transition frequency
- Gold metallization ensures excellent reliability.

1.3 Applications

- RF front end wideband applications in the GHz range
 - Analog and digital cellular telephones
 - Cordless telephones (CT1, CT2, DECT, etc.)
 - Radar detectors
 - Pagers and satellite TV tuners (SATV)
 - Repeater amplifiers in fiber-optic systems.

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CBO}	collector-base voltage			-	-	20	V
V _{CES}	collector-emitter voltage	$R_{BE} = 0 \ \Omega$		-	-	15	V
I _C	collector current (DC)			-	-	70	mA
P _{tot}	total power dissipation	up to $T_{sp} = 97 \ ^{\circ}C$	[1]	-	-	300	mW
h _{FE}	DC current gain	$I_{C} = 20 \text{ mA}; V_{CE} = 6 \text{ V}$		60	120	250	
C _{re}	feedback capacitance	$\begin{split} I_{C} &= i_{c} = 0 \text{ A}; V_{CB} = 6 \text{V}; \\ f &= 1 \text{MHz} \end{split}$		-	0.4	-	рF
f _T	transition frequency	I _C = 20 mA; V _{CE} = 6 V; f = 1 GHz		-	9	-	GHz
G _{UM}	maximum unilateral power gain	$ I_C = 20 \text{ mA}; V_{CE} = 6 \text{ V}; $					
		f = 900 MHz		-	15	-	dB
		f = 2 GHz		-	9	-	dB



NPN 9	GHz	wideband	transistor
-------	-----	----------	------------

Table 1.	QUICK reference data	acontinued				
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
s ₂₁ ²	insertion power gain	$ I_{C} = 20 \text{ mA}; V_{CE} = 6 \text{ V}; $	13	14	-	dB
NF	noise figure	$\Gamma_{s} = \Gamma_{opt}; T_{amb} = 25 \ ^{\circ}C$				
		$I_{C} = 5 \text{ mA}; V_{CE} = 6 \text{ V};$ f = 900 MHz	-	1.1	1.6	dB
		$\begin{split} I_{C} &= 20 \text{ mA}; V_{CE} = 6 \text{ V}; \\ f &= 900 \text{ MHz} \end{split}$	-	1.6	2.1	dB
		I _C = 5 mA; V _{CE} = 8 V; f = 2 GHz	-	1.9	-	dB

 Table 1.
 Quick reference data ...continued

[1] T_{sp} is the temperature at the soldering point of the collector tab.

2. Pinning information

Pin	Description	Simplified outline	Symbol
1	base		_
2	emitter		3
3	collector	1 2	1-

3. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
BFR520	-	plastic surface mounted package; 3 leads	SOT23		

4. Marking

Table 4. Marking	
Type number	Marking code ^[1]
BFR520	32*

[1] * = p: Made in Hong Kong

- * = t: Made in Malaysia
- * = W: Made in China.

BFR520

sym021

Limiting values 5.

V_{CBO} collector-base voltageopen emitter-20 V_{CES} collector-emitter voltage $R_{BE} = 0 \Omega$ -15 V_{EBO} emitter-base voltageopen collector-2.5 I_C collector current (DC)-70	Unit V
V _{CES} collector-emitter voltage $R_{BE} = 0 \Omega$ -15V _{EBO} emitter-base voltageopen collector-2.5I _C collector current (DC)-70	-
V _{EBO} emitter-base voltageopen collector-2.5I _C collector current (DC)-70	
I _C collector current (DC) - 70	V
	V
P_{tot} total power dissipation up to $T_{sp} = 97 \degree C$ [1] - 300	mA
	mW
T _{stg} storage temperature -65 150	°C
T _j junction temperature - 175	°C

[1] T_{sp} is the temperature at the soldering point of the collector tab.

Thermal characteristics 6.

Table 6. Thermal characteristics
--

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-s)}	thermal resistance from junction to soldering point		<mark>[1]</mark> 260	K/W

[1] T_{sp} is the temperature at the soldering point of the collector tab.

Characteristics 7.

Table 7.Characteristics $T_{1} = 25 \ ^{\circ}$ C unless otherwise s

Symbol	Parameter	Conditions	Mi	n	Тур	Max	Unit
I _{CBO}	collector cut-off current	$I_{E} = 0 \text{ A}; V_{CB} = 6 \text{ V}$	-		-	50	nA
h _{FE}	DC current gain	$I_{C} = 20 \text{ mA}; V_{CE} = 6 \text{ V}$	60		120	250	
C _e	emitter capacitance	$\begin{split} I_{C} &= i_{c} = 0 \text{ A}; V_{EB} = 0.5 \text{ V}; \\ f &= 1 MHz \end{split}$	-		1	-	pF
C _c	collector capacitance	$\begin{split} I_{E} &= i_{e} = 0 \; A; \; V_{CB} = 6 \; V; \\ f &= 1 \; MHz \end{split}$	-		0.5	-	pF
C _{re}	feedback capacitance	$\begin{split} I_{C} &= 0 \text{ A}; \text{V}_{CB} = 6 \text{V}; \\ \text{f} &= 1 \text{MHz} \end{split}$	-		0.4	-	pF
f _T		$I_C = 20 \text{ mA}; V_{CE} = 6 \text{ V};$ f = 1 GHz	-		9	-	GHz
G _{UM}	maximum unilateral power	I_{C} = 20 mA; V_{CE} = 6 V; T_{amb} = 25 °C	[1]				
	gain	f = 900 MHz	-		15	-	dB
		f = 2 GHz	-		9	-	dB
s ₂₁ ²	insertion power gain	$ I_{C} = 20 \text{ mA}; V_{CE} = 6 \text{ V}; $	13		14	-	dB

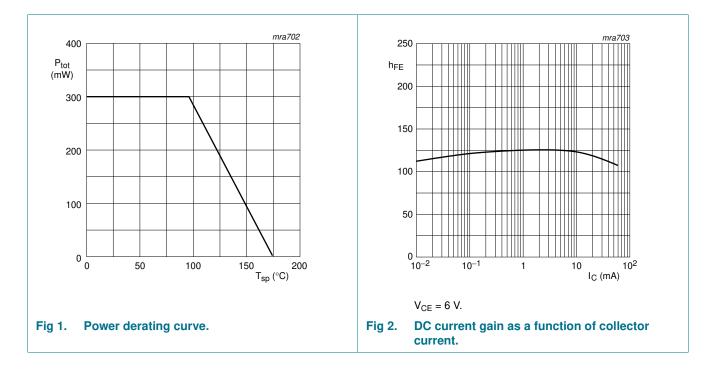
,		•				
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
NF	noise figure	$\Gamma_{s} = \Gamma_{opt}; V_{CE} = 6 V;$ $T_{amb} = 25 \ ^{\circ}C$				
		I _C = 5 mA; f = 900 MHz	-	1.1	1.6	dB
		I _C = 20 mA; f = 900 MHz	-	1.6	2.1	dB
		I _C = 5 mA; f = 2 GHz	-	1.9	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	$ I_{C} = 20 \text{ mA}; V_{CE} = 6 \text{ V}; \\ R_{L} = 50 \ \Omega; T_{amb} = 25 \ ^{\circ}C; \\ f = 900 \text{ MHz} $	-	17	-	dBm
ITO	third order intercept point		[2] -	26	-	dBm

Tab	le 7.	Char	acteristics	continued
T	05 00 .		athanicaa	a la a ifi a d

[1] G_{UM} is the maximum unilateral power gain, assuming s_{12} is zero and

$$G_{UM} = 10 \log \frac{|s_{21}|^2}{(1 - |s_{11}|^2)(1 - |s_{22}|^2)} dB.$$

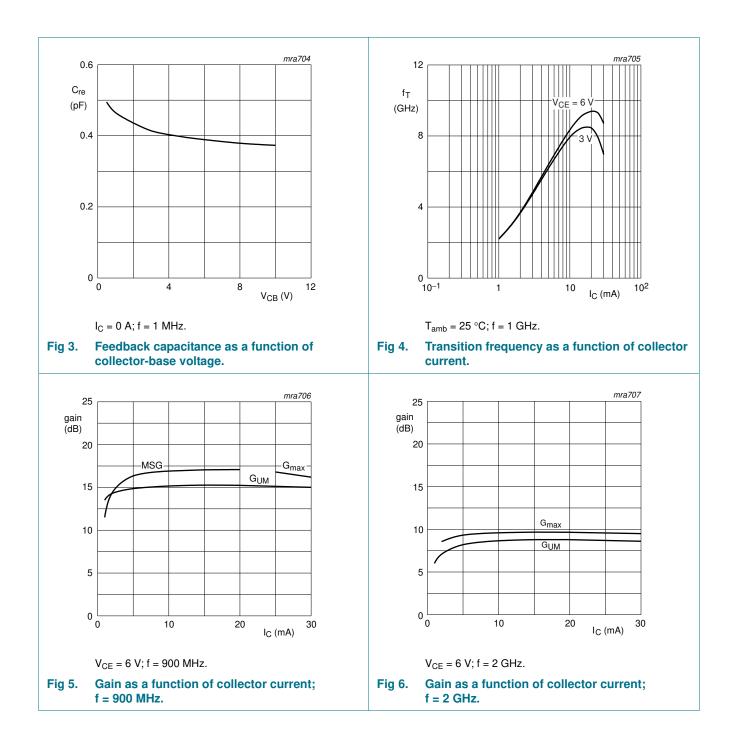
 $\label{eq:loss} \begin{array}{ll} \mbox{[2]} & I_C = 20 \mbox{ mA}; \mbox{ } V_{CE} = 6 \mbox{ } V; \mbox{ } R_L = 50 \ \Omega; \mbox{ } T_{amb} = 25 \ ^\circ C; \mbox{ } f_p = 900 \mbox{ } MHz; \mbox{ } f_q = 902 \mbox{ } MHz \mbox{ } MHz \mbox{ } max{ } ma$



NXP Semiconductors

NPN 9 GHz wideband transistor

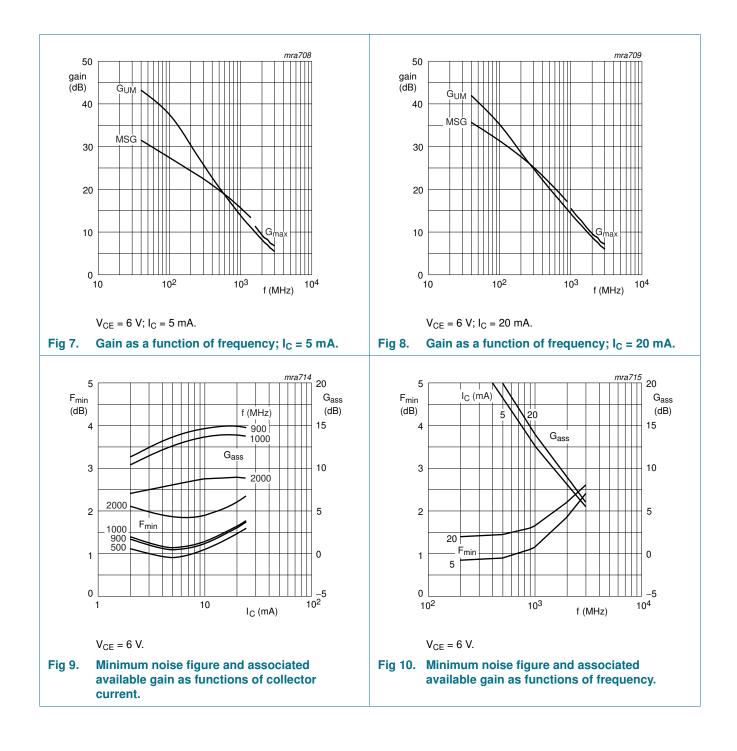
BFR520



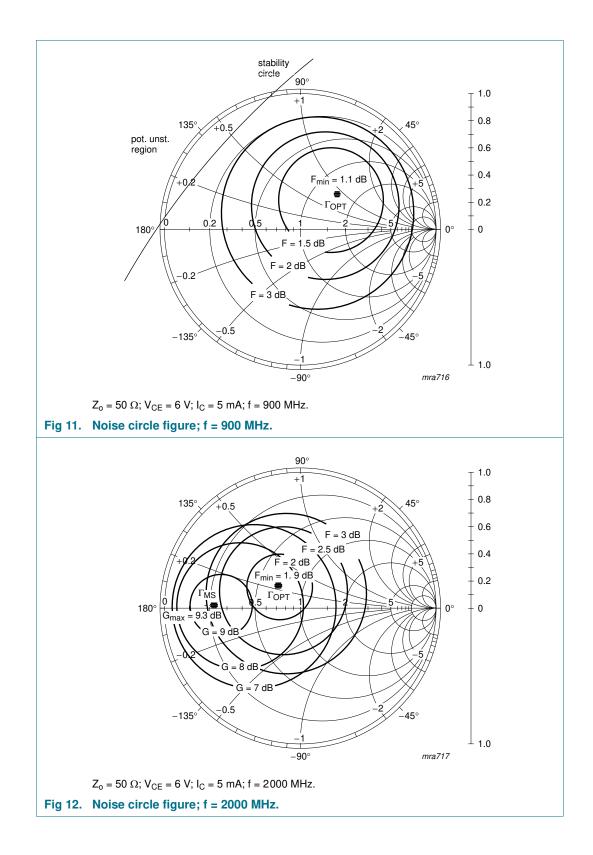
NXP Semiconductors

NPN 9 GHz wideband transistor

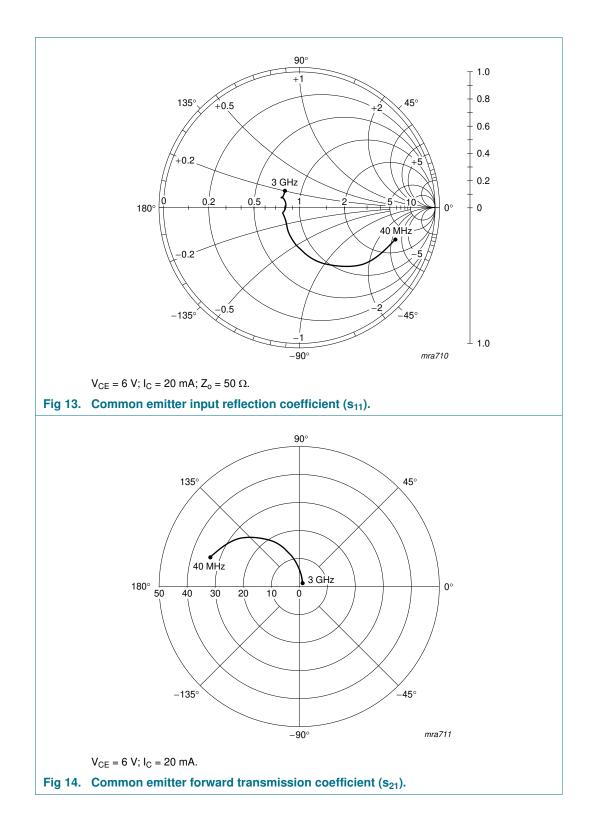
BFR520



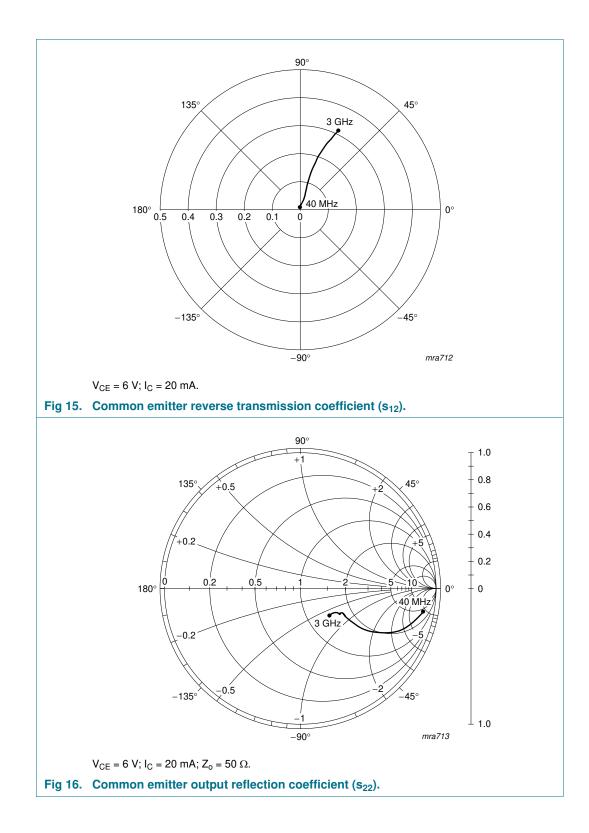
BFR520



BFR520



BFR520



BFR520

8. Package outline

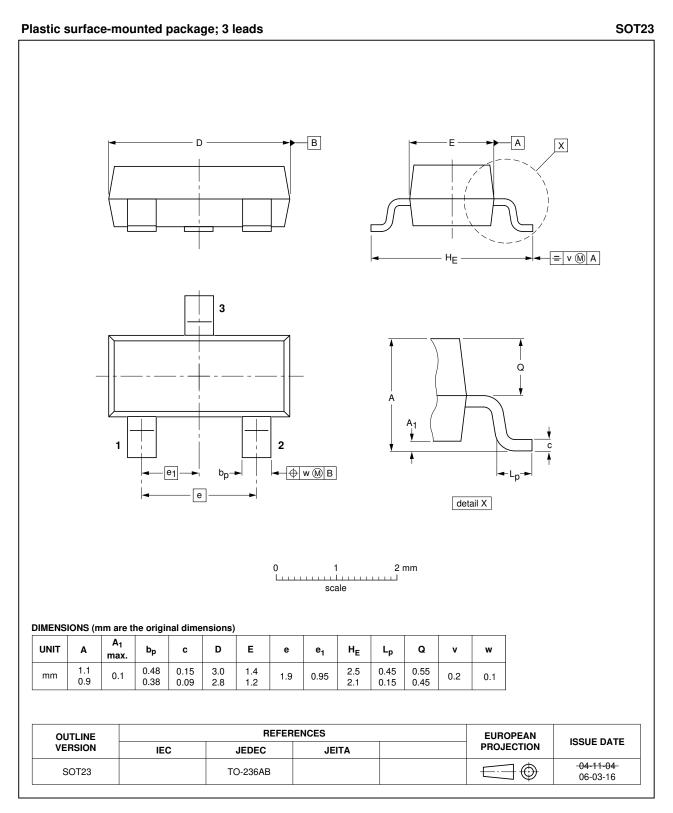


Fig 17. Package outline SOT23 (TO-236AB).

All information provided in this document is subject to legal disclaimers.

9. Revision history

Table 8. Revision h	istory					
Document ID	Release date	Data sheet status	Change notice	Supersedes		
BFR520 v.4	20110913	Product data sheet	-	BFR520 v.3		
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 					
	 Legal texts have been adapted to the new company name where appropriate. 					
	 Package outline drawings have been updated to the latest version. 					
BFR520 v.3 (9397 750 13397)	20040901	Product data sheet	-	BFR520_CNV v.2		
BFR520_CNV v.2	19971204	Product specification	-	-		

10. Legal information

10.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

10.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

10.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

12 of 14

BFR520

NPN 9 GHz wideband transistor

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the

11. Contact information

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

10.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

For more information, please visit: http://www.nxp.com

For sales office addresses, please send an email to: salesaddresses@nxp.com

BFR520

12. Contents

1	Product profile 1
1.1	General description 1
1.2	Features and benefits 1
1.3	Applications 1
1.4	Quick reference data 1
2	Pinning information 2
3	Ordering information 2
4	Marking 2
5	Limiting values 3
6	Thermal characteristics 3
7	Characteristics 3
8	Package outline 10
9	Revision history 11
10	Legal information 12
10.1	Data sheet status 12
10.2	Definitions 12
10.3	Disclaimers 12
10.4	Trademarks 13
11	Contact information 13
12	Contents 14

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2011.

All rights reserved.

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 13 September 2011 Document identifier: BFR520