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# DISCRETE SEMICONDUCTORS

# DATA SHEET

# **BFR93A**NPN 6 GHz wideband transistor

Product specification Supersedes data of September 1995



# NPN 6 GHz wideband transistor

# BFR93A

#### **FEATURES**

- High power gain
- Low noise figure
- Very low intermodulation distortion.

#### **DESCRIPTION**

NPN wideband transistor in a plastic SOT23 package.

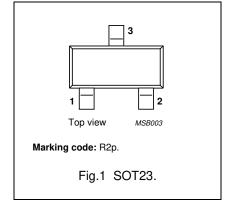
PNP complement: BFT93.

### **APPLICATIONS**

RF wideband amplifiers and oscillators.

#### **PINNING**

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	



#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	15	V
$V_{CEO}$	collector-emitter voltage	open base	_	12	V
I <sub>C</sub>	collector current (DC)		_	35	mA
P <sub>tot</sub>	total power dissipation	T <sub>s</sub> ≤ 95 °C	_	300	mW
C <sub>re</sub>	feedback capacitance	I <sub>C</sub> = 0; V <sub>CE</sub> = 5 V; f = 1 MHz	0.6	_	pF
f <sub>T</sub>	transition frequency	$I_C = 30 \text{ mA}; V_{CE} = 5 \text{ V}; f = 500 \text{ MHz}$	6	_	GHz
G <sub>UM</sub>	maximum unilateral power gain	$I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V}; f = 1 \text{ GHz}; T_{amb} = 25 ^{\circ}\text{C}$	13	_	dB
		$I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V}; f = 2 \text{ GHz}; T_{amb} = 25 ^{\circ}\text{C}$	7	_	dB
F	noise figure	$I_C$ = 5 mA; $V_{CE}$ = 8 V; f = 1 GHz; $\Gamma_s$ = $\Gamma_{opt}$ ; $\Gamma_{amb}$ = 25 °C	1.9	_	dB
Vo	output voltage	$\begin{aligned} &d_{im} = -60 \text{ dB; } I_C = 30 \text{ mA; } V_{CE} = 8 \text{ V;} \\ &R_L = 75 \Omega; T_{amb} = 25 \text{ °C;} \\ &f_p + f_q - f_r = 793.25 \text{ MHz} \end{aligned}$	425	_	mV

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{CBO}$	collector-base voltage	open emitter	_	15	V
$V_{CEO}$	collector-emitter voltage	open base	_	12	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	2	V
I <sub>C</sub>	collector current (DC)		_	35	mA
P <sub>tot</sub>	total power dissipation	T <sub>s</sub> ≤ 95 °C; note 1	_	300	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
T <sub>i</sub>	junction temperature		_	+175	°C

## Note

1.  $T_s$  is the temperature at the soldering point of the collector pin.

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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-s</sub>	thermal resistance from junction to soldering point	T <sub>s</sub> ≤ 95 °C; note 1	260	K/W

#### Note

1.  $T_s$  is the temperature at the soldering point of the collector pin.

#### **CHARACTERISTICS**

T<sub>i</sub> = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 5 V	_	_	50	nA
h <sub>FE</sub>	DC current gain	$I_C = 30 \text{ mA}; V_{CE} = 5 \text{ V}$	40	90	-	
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 5 \text{ V}$ ; $f = 1 \text{ MHz}$	-	0.7	_	pF
C <sub>e</sub>	emitter capacitance	$I_C = I_c = 0$ ; $V_{EB} = 0.5 \text{ V}$ ; $f = 1 \text{ MHz}$	-	1.9	-	pF
C <sub>re</sub>	feedback capacitance	$I_C = I_c = 0$ ; $V_{CE} = 5$ V; $f = 1$ MHz; $T_{amb} = 25$ °C	_	0.6	_	pF
f <sub>T</sub>	transition frequency	$I_C = 30 \text{ mA}; V_{CE} = 5 \text{ V}; f = 500 \text{ MHz}$	4.5	6	_	GHz
G <sub>UM</sub>	maximum unilateral power gain (note 1)	$I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V}; f = 1 \text{ GHz}; $ $T_{amb} = 25 \text{ °C}$	-	13	_	dB
		$I_C = 30 \text{ mA}; V_{CE} = 8 \text{ V}; f = 2 \text{ GHz}; $ $T_{amb} = 25 \text{ °C}$	_	7	_	dB
F	noise figure (note 2)	$I_C$ = 5 mA; $V_{CE}$ = 8 V; f = 1 GHz; $\Gamma_S$ = $\Gamma_{opt}$ ; $T_{amb}$ = 25 °C	-	1.9	_	dB
		$I_C$ = 5 mA; $V_{CE}$ = 8 V; f = 2 GHz; $\Gamma_S$ = $\Gamma_{opt}$ ; $T_{amb}$ = 25 °C	_	3	_	dB
Vo	output voltage	notes 2 and 3	-	425	-	mV
d <sub>2</sub>	second order intermodulation distortion	notes 2 and 4	_	-50	_	dB

**Notes** 

1.  $G_{UM}$  is the maximum unilateral power gain, assuming  $S_{12}$  is zero and  $G_{UM} = 10 \log \frac{\left|S_{21}\right|^2}{\left(1 - \left|S_{11}\right|^2\right)\left(1 - \left|S_{22}\right|^2\right)} dB$ .

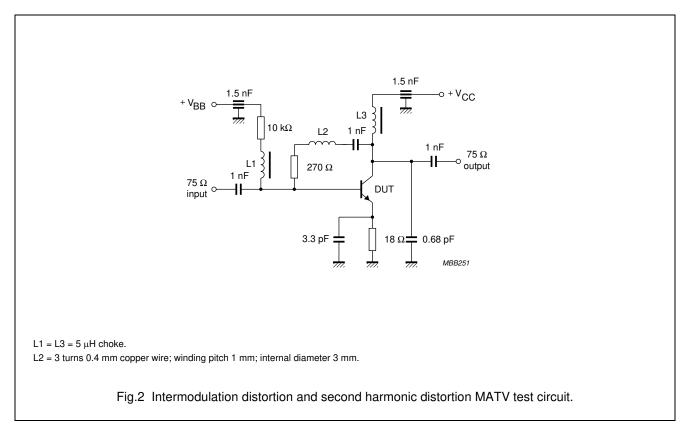
2. Measured on the same die in a SOT37 package (BFR91A).

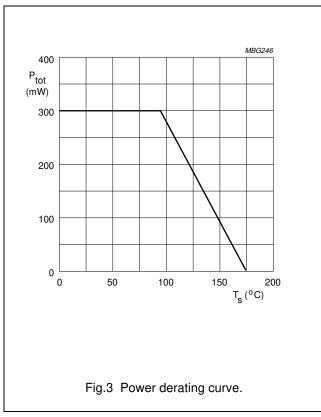
3.  $d_{im} = -60 \text{ dB (DIN 45004B)}$ ;  $I_C = 30 \text{ mA}$ ;  $V_{CE} = 8 \text{ V}$ ;  $R_L = 75 \Omega$ ;  $T_{amb} = 25 \,^{\circ}\text{C}$ ;  $V_p = V_O$  at  $d_{im} = -60$  dB;  $f_p = 795.25$  MHz;  $V_q = V_O - 6 \text{ dB at } f_q = 803.25 \text{ MHz};$  $V_r = V_O - 6 \text{ dB at } f_r = 805.25 \text{ MHz};$ measured at  $f_p + f_q - f_r = 793.25$  MHz.

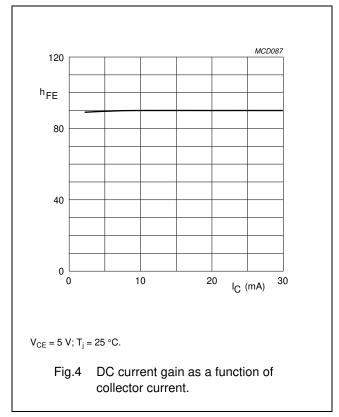
4.  $I_C = 30$  mA;  $V_{CE} = 8$  V;  $R_L = 75$   $\Omega$ ;  $T_{amb} = 25$  °C;  $V_p = 200 \text{ mV}$  at  $f_p = 250 \text{ MHz}$ ;  $V_q = 200 \text{ mV at } f_q = 560 \text{ MHz};$ measured at  $f_p + f_q = 810 \text{ MHz}$ .

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# NPN 6 GHz wideband transistor

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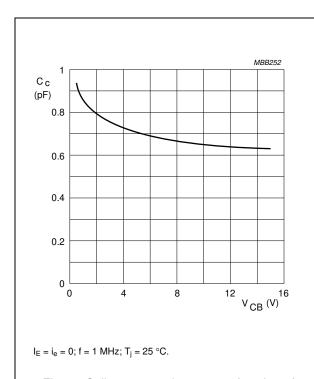
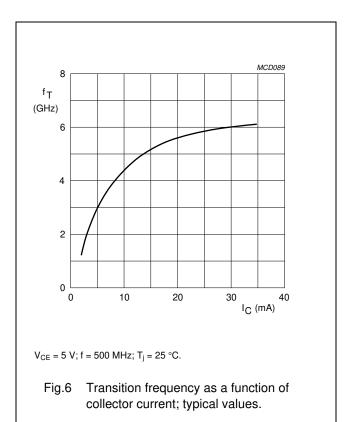
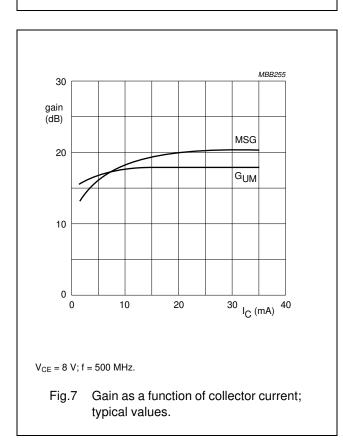
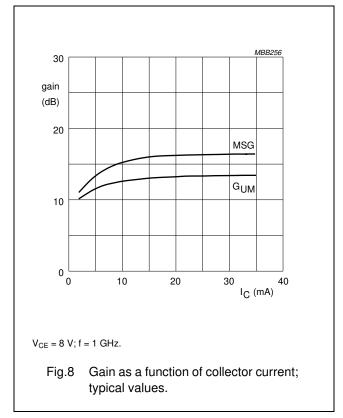


Fig.5 Collector capacitance as a function of collector-base voltage; typical values.

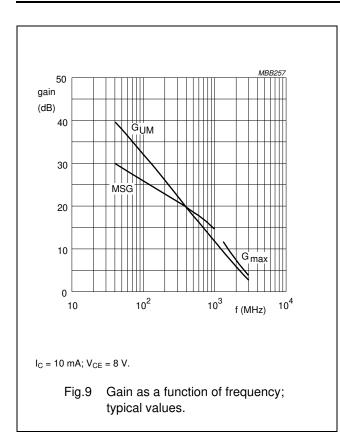


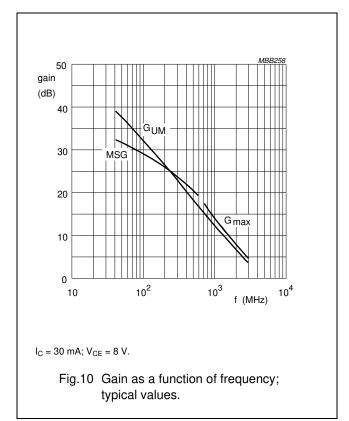


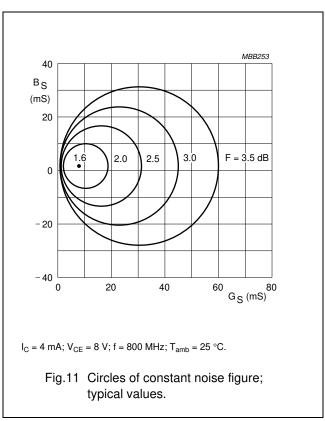


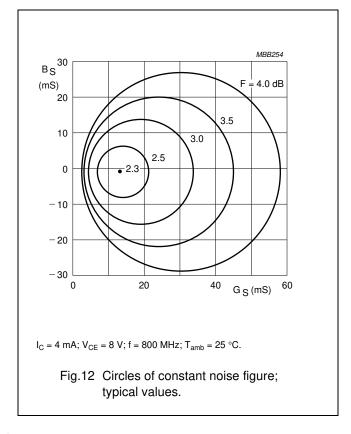
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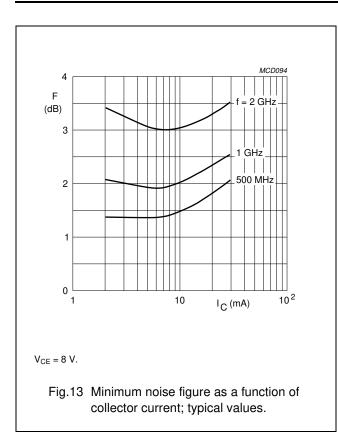


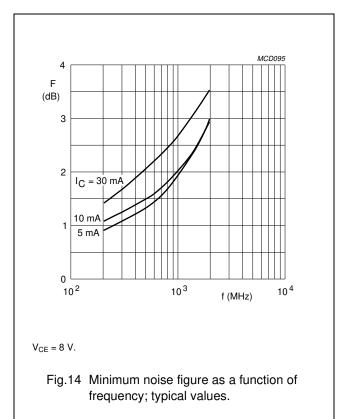


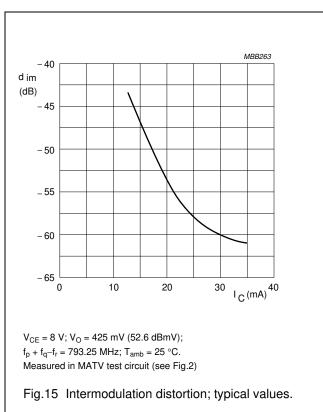


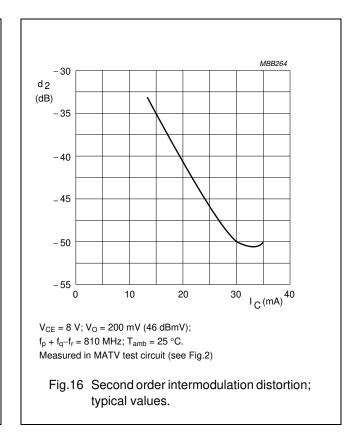
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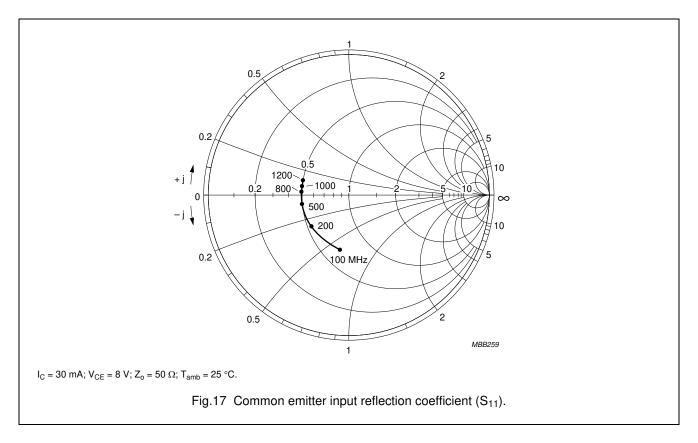


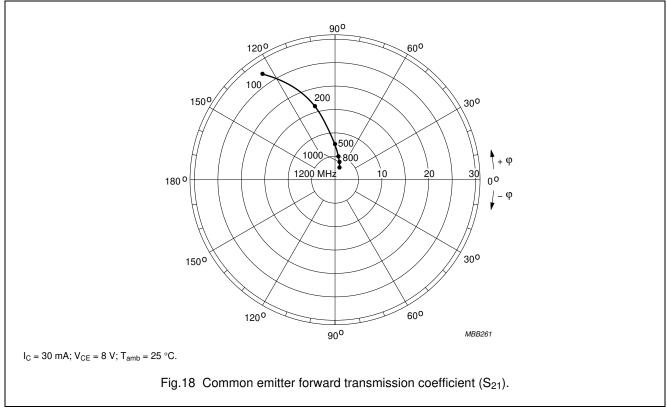




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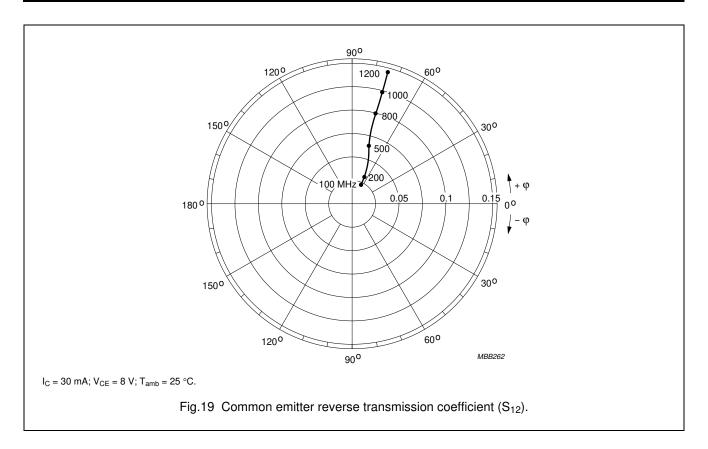
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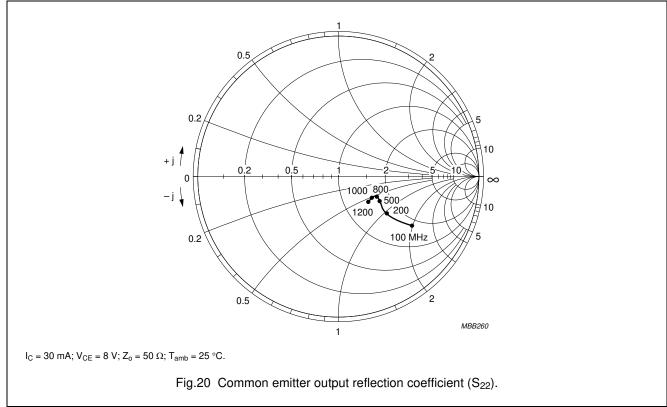




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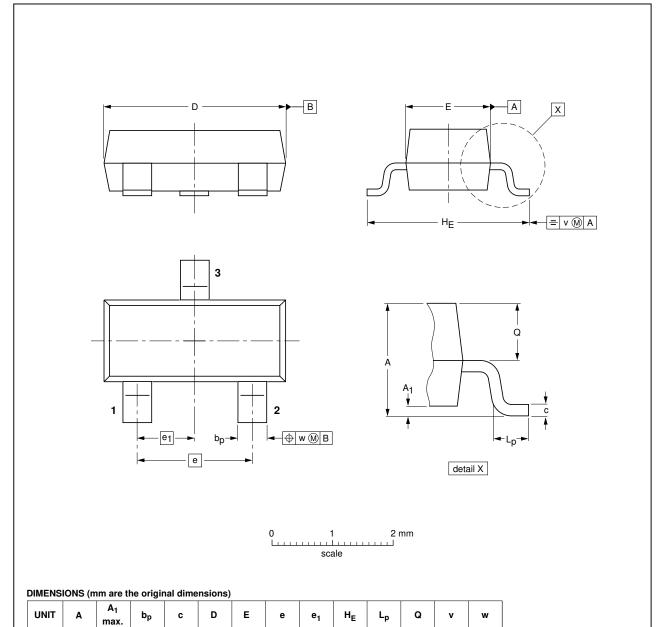
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#### **PACKAGE OUTLINE**

Plastic surface-mounted package; 3 leads

SOT23



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC JEDEC JEITA		PROJECTION	1330E DATE		
SOT23		TO-236AB			<del>-04-11-04-</del> 06-03-16	

0.95

2.5

2.1

0.45

0.55

0.2

0.1

1997 Oct 29

0.48

0.38

0.1

0.15

0.09

3.0

1.9

1.1

0.9

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#### **DATA SHEET STATUS**

DOCUMENT STATUS(1)	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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#### **Contact information**

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