# imall

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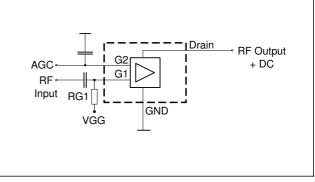
BG3130...

# **DUAL N-Channel MOSFET Tetrode**

- Two gain controlled input stage for UHF and VHF -tuners e.g. (NTSC, PAL)
- Two AGC amplifiers in one single package
- Integrated gate protection diodes
- High AGC-range, low noise figure, high gain
- · Improved cross modulation at gain reduction

#### **BG3130 BG3130R**





ESD (Electrostatic discharge) sensitive device, observe handling precaution!

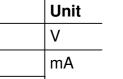
Туре	Package		Marking					
BG3130	SOT363	1=G1*	2=G2	3=D*	4=D**	5=S	6=G1**	KAs
BG3130R	SOT363	1=G1*	2=S	3=D*	4=D**	5=G2	6=G1**	KHs

\* For amp. A; \*\* for amp. B

180° rotated tape loading orientation available

#### **Maximum Ratings**

Parameter	Symbol	Value	Unit
Drain-source voltage	V <sub>DS</sub>	8	V
Continuous drain current	/ <sub>D</sub>	25	mA
Gate 1/ gate 2-source current	± <i>I</i> <sub>G1/2SM</sub>	1	
Gate 1/ gate 2-source voltage	± <i>V</i> <sub>G1/G2S</sub>	6	V
Total power dissipation	P <sub>tot</sub>	200	mW
Storage temperature	T <sub>stg</sub>	-55 150	°C
Channel temperature	T <sub>ch</sub>	150	





#### **Thermal Resistance**

Parameter	Symbol	Value	Unit
Channel - soldering point <sup>1)</sup>	R <sub>thchs</sub>	≤ 280	K/W

#### **Electrical Characteristics**

Parameter	Symbol	Values			Unit	
			typ.	max.	1	
DC Characteristics	•			•	•	
Drain-source breakdown voltage	V <sub>(BR)DS</sub>	12	-	-	V	
$I_{\rm D}$ = 10 µA, $V_{\rm G1S}$ = 0 V, $V_{\rm G2S}$ = 0 V						
Gate1-source breakdown voltage	+V <sub>(BR)G1SS</sub>	6	-	15		
$+I_{G1S} = 10 \text{ mA}, V_{G2S} = 0 \text{ V}, V_{DS} = 0 \text{ V}$						
Gate2-source breakdown voltage	+V(BR)G2SS	6	-	15		
$+I_{G2S} = 10 \text{ mA}, V_{G1S} = 0 \text{ V}, V_{DS} = 0 \text{ V}$						
Gate1-source leakage current	+ <i>I</i> G1SS	-	-	50	μA	
$V_{G1S} = 6 \text{ V}, V_{G2S} = 0 \text{ V}$						
Gate2-source leakage current	+ <i>I</i> G2SS	-	-	50	nA	
$V_{G2S} = 8 \text{ V}, V_{G1S} = 0 \text{ V}, V_{DS} = 0 \text{ V}$						
Drain current	I <sub>DSS</sub>	-	-	10	μΑ	
$V_{\text{DS}} = 5 \text{ V}, \ V_{\text{G1S}} = 0 \text{ V}, \ V_{\text{G2S}} = 4.5 \text{ V}$						
Drain-source current	<i>I</i> DSX	-	10	-	mA	
$V_{\rm DS}$ = 5 V, $V_{\rm G2S}$ = 4 V, $R_{\rm G1}$ = 120 k $\Omega$						
Gate1-source pinch-off voltage	V <sub>G1S(p)</sub>	-	0.7	-	V	
$V_{\text{DS}} = 5 \text{ V}, \ V_{\text{G2S}} = 4 \text{ V}, \ I_{\text{D}} = 20 \ \mu\text{A}$						
Gate2-source pinch-off voltage	V <sub>G2S(p)</sub>	-	0.6	-		
$V_{\rm DS} = 5 \text{ V}, \ I_{\rm D} = 20 \ \mu\text{A}$						

<sup>1</sup>For calculation of  $R_{thJA}$  please refer to Application Note Thermal Resistance



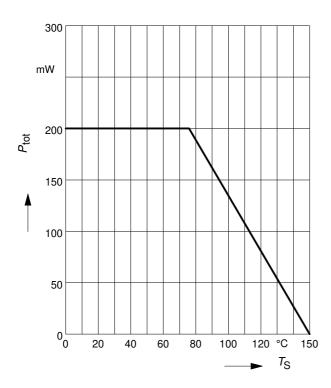
## **Electrical Characteristics**

Parameter	Symbol	Values			Unit	
		min.	typ.	max.		
<b>AC Characteristics</b> $V_{DS} = 5V$ , $V_{G2S} = 4V$ , ( $I_D = 14$ mA) (verified by random sampling)						
Forward transconductance	g <sub>fs</sub>	-	33	-	mS	
Gate1 input capacitance	C <sub>g1ss</sub>	-	1.9	-	pF	
<i>f</i> = 10 MHz						
Output capacitance	C <sub>dss</sub>	-	1.1	-		
<i>f</i> = 10 MHz						
Power gain	Gp				dB	
<i>f</i> = 800 MHz		-	24	-		
<i>f</i> = 45 MHz		-	31	-		
Noise figure	F				dB	
<i>f</i> = 800 MHz		-	1.3	-		
<i>f</i> = 45 MHz		-	1.7	-		
Gain control range	$\Delta G_{p}$	45	-	-		
<i>V</i> <sub>G2S</sub> = 4 0 V, <i>f</i> = 800 MHz						
Cross-modulation $k=1\%$ , $f_w=50MHz$ , $f_{unw}=60MHz$	X <sub>mod</sub>				-	
AGC = 0  dB		90	-	-		
<i>AGC</i> = 10 dB		-	87	-		
AGC = 40  dB		96	100	-		

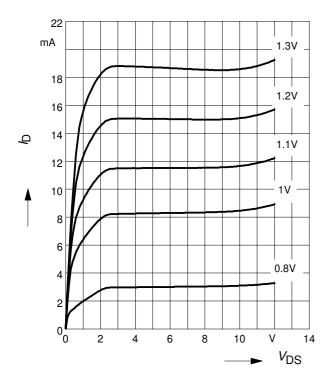


Total power dissipation  $P_{tot} = f(T_S)$ 

amp. A = amp. B

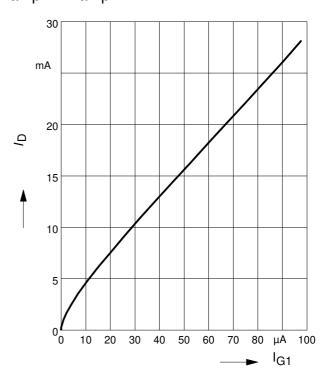


**Output characteristics I**<sub>D</sub> =  $f(V_{DS})$ amp. A = amp. B

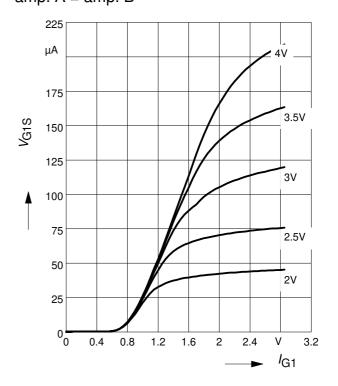


**Drain current**  $I_D = f(I_{G1})$ 

 $V_{G2S} = 4V$ amp. A = amp. B



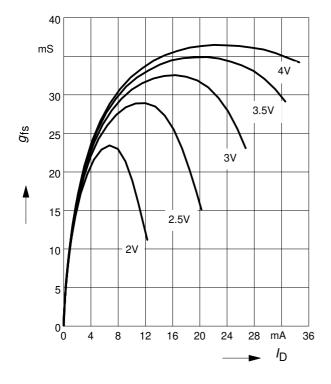
Gate 1 current  $I_{G1} = f(V_{G1S})$  $V_{DS} = 5V$ ,  $V_{G2S} =$  Parameter amp. A = amp. B



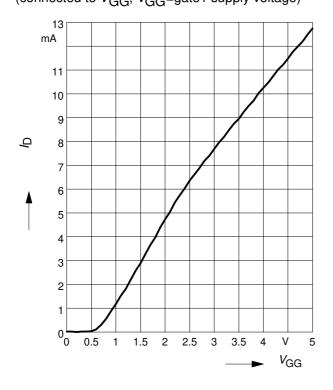


#### Gate 1 forward transconductance

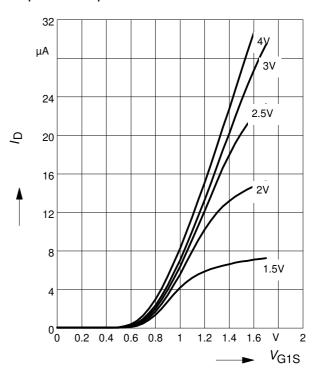
 $g_{fs} = f(I_D), V_{DS} = 5V, V_{G2S} = Parameter$ amp. A = amp. B



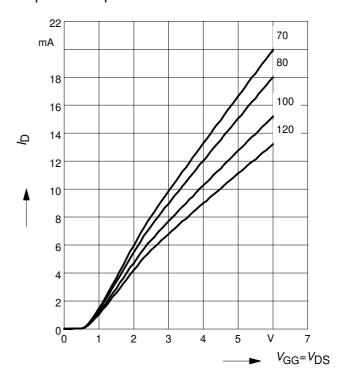
**Drain current**  $I_D = f(V_{GG})$  amp.A=amp.B  $V_{DS} = 5V$ ,  $V_{G2S} = 4V$ ,  $R_{G1} = 120k\Omega$ (connected to  $V_{GG}$ ,  $V_{GG}$ =gate1 supply voltage)



**Drain current**  $I_D = f(V_{G1S})$  $V_{DS} = 5V, V_{G2S} = Parameter$ amp. A = amp. B



**Drain current**  $I_D = f(V_{GG})$  $V_{G2S} = 4V$ ,  $R_{G1} = Parameter in kΩ$ amp. A = amp. B

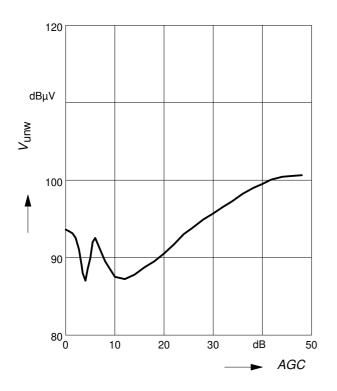




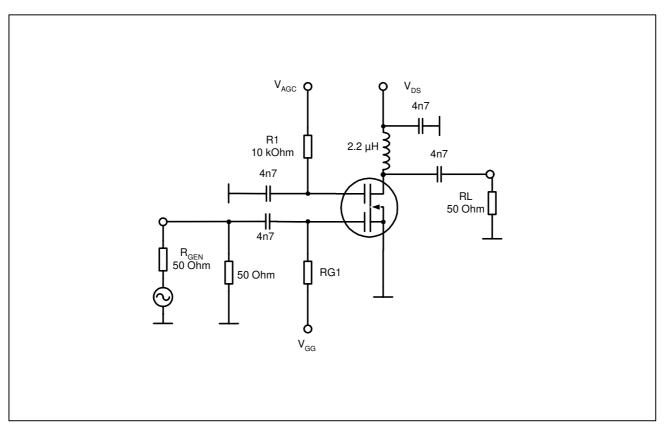
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**Crossmodulation**  $V_{\text{unw}} = (AGC)$ 

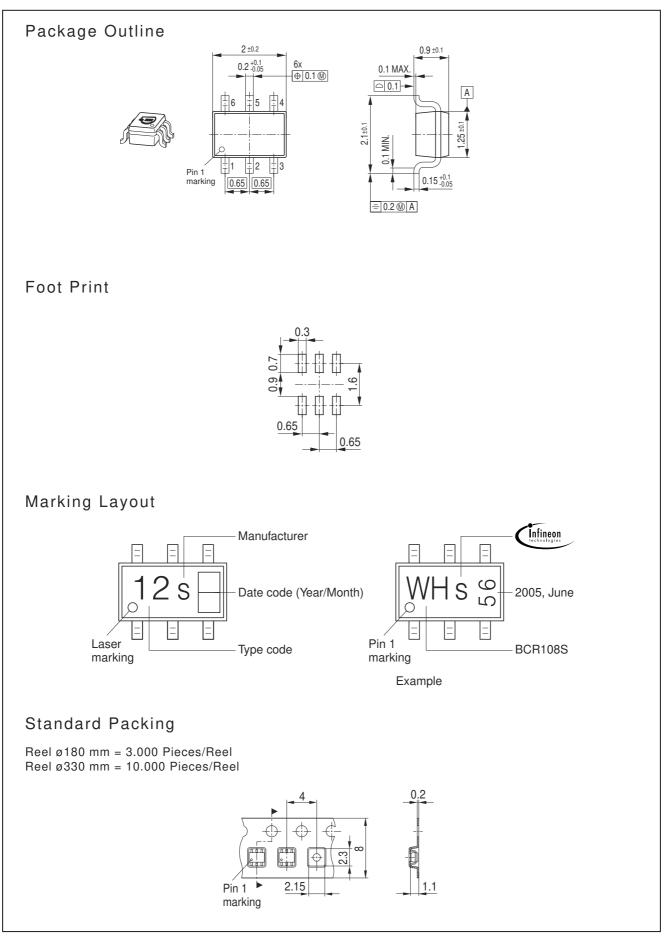
 $V_{\text{DS}} = 5 \text{ V}, R_{\text{g1}} = 68 \text{ k}\Omega$ 



**Crossmodulation test circuit** 









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