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Product data sheet

Product profile

1.1 General description

Hybrid high dynamic range amplifier module in a SOT115J package operating at a voltage supply of 24 V (DC).

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features and benefits

- Excellent linearity
- Extremely low noise
- Silicon nitride passivation
- Rugged construction
- TiPtAu metallized crystals ensure optimal reliability

1.3 Applications

Reverse amplifier in two-way CATV systems in the 5 MHz to 200 MHz frequency

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G_p	power gain	f = 10 MHz	23.5	-	24.5	dB
I _{tot}	total current consumption (DC)		[1] -	215	230	mA

^[1] The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.



200 MHz, 24 dB gain reverse amplifier

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Symbol
1	input		
2	common	1 3 5 7 9	5
3	common		$\frac{1}{2}$
5	+V _B		2 3 7 8
7	common		sym095
8	common		,
9	output		

3. Ordering information

Table 3. Ordering information

Туре	Package				
number	Name	Description	Version		
BGY67A	-	rectangular single-ended package; aluminium flange; 2 vertical mounting holes; $2\times 6\text{-}32$ UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads	SOT115J		

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_i	RF input voltage		-	65	dBmV
T _{stg}	storage temperature		-40	+100	°C
T _{mb}	mounting base temperatur	е	-20	+90	°C

5. Characteristics

Table 5. Characteristics

Bandwidth 5 MHz to 200 MHz; $V_B = 24 \text{ V}$; $T_{mb} = 30 \text{ °C}$; $Z_S = Z_L = 75 \Omega$; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Tvn	Max	Unit
Зунион	raiailletei	Conditions	WIII	Тур	IVIAX	Utill
G_p	power gain	f = 10 MHz	23.5	-	24.5	dB
SL	slope cable equivalent	f = 5 MHz to 200 MHz	-0.2	-	+0.5	dB
FL	flatness of frequency response	f = 5 MHz to 200 MHz	-	-	±0.2	dB
S ₁₁	input return losses	f = 5 MHz to 200 MHz	20	-	-	dB
S ₂₂	output return losses	f = 5 MHz to 200 MHz	20	-	-	dB
СТВ	composite triple beat	22 channels flat; V _o = 50 dBmV; measured at 175.25 MHz	-	-	-67	dB
X _{mod}	cross modulation	22 channels flat; $V_0 = 50 \text{ dBmV}$; measured at 55.25 MHz	-	-	-59	dB
d_2	second order distortion	$V_0 = 50 \text{ dBmV}$	[1] _	-	-67	dB
Vo	output voltage	$d_{im} = -60 \text{ dB}$	<u>[2]</u> 67	-	-	dBmV
			[<u>3</u>] 64	-	-	dBmV
F	noise figure	f = 200 MHz	-	-	5.5	dB
I _{tot}	total current consumption (DC)		[4] _	215	230	mA

 $^{[1] \}quad f_p = 83.25 \text{ MHz}; \ V_p = 50 \text{ dBmV}; \ f_q = 109.25 \text{ MHz}; \ V_q = 50 \text{ dBmV}; \ measured \ at \ f_p + f_q = 192.5 \text{ MHz}.$

^[2] Measured according to DIN45004B; $f_p = 35.25 \text{ MHz}; \ V_o = V_p; \ f_q = 42.25 \text{ MHz}; \ V_q = V_o - 6 \text{ dB}; \ f_r = 44.25 \text{ MHz}; \ V_r = V_o - 6 \text{ dB}; \ measured at } f_p + f_q - f_r = 33.25 \text{ MHz}.$

^[3] Measured according to DIN45004B; $f_p = 187.25 \text{ MHz}; \ V_o = V_p; \ f_q = 194.25 \text{ MHz}; \ V_q = V_o - 6 \text{ dB}; \ f_r = 196.25 \text{ MHz}; \ V_r = V_o - 6 \text{ dB}; \ measured at } f_p + f_q - f_r = 185.25 \text{ MHz}.$

^[4] The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.

6. Package outline

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J

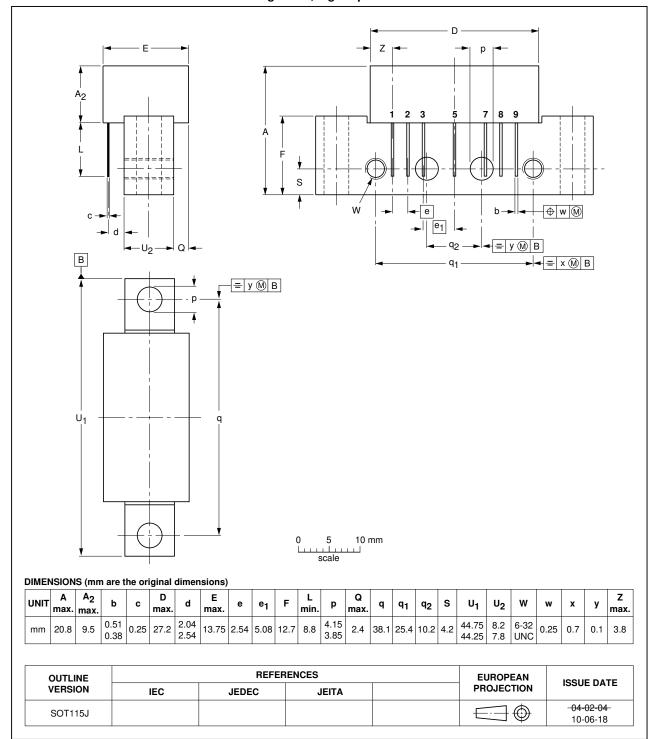


Fig 1. Package outline SOT115J

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200 MHz, 24 dB gain reverse amplifier

7. Revision history

Table 6. Revision history

Release date	Data sheet status	Change notice	Supersedes
20110919	Product data sheet	-	BGY67A v.4
		esigned to comply w	ith the new identity
 Legal texts h 	ave been adapted to the new o	company name whe	re appropriate.
 Package outl 	ine drawings have been updat	ed to the latest vers	ion.
20050314	Product data sheet	-	BGY67A v.3
20011018	Product specification	-	BGY67A v.2
19970409	Product specification	-	BGY67A v.1
	20110919 • The format of guidelines of the Legal texts here. Package out the 20050314	 20110919 Product data sheet The format of this data sheet has been redeguidelines of NXP Semiconductors. Legal texts have been adapted to the new of Package outline drawings have been updated 20050314 Product data sheet 20011018 Product specification 	 20110919 Product data sheet - The format of this data sheet has been redesigned to comply we guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name whee Package outline drawings have been updated to the latest verse 20050314 Product data sheet - 20011018 Product specification -

8. Legal information

8.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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BGY67A

NXP Semiconductors

BGY67A

200 MHz, 24 dB gain reverse amplifier

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200 MHz, 24 dB gain reverse amplifier

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