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# Wide band IF detector for RF remote control units BH4126FV

The BH4126FV is an IC equipped with internal mixer, IF amplifier, and FM detector circuits, developed for use with RF remote control units.

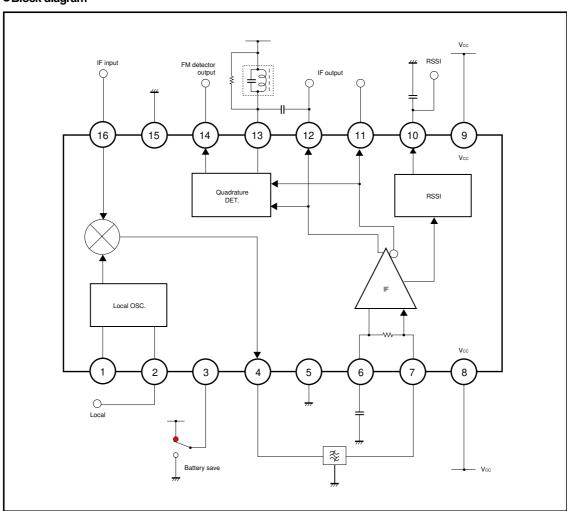
#### Applications

Keyless entry

#### Features

- 1) Equipped with internal mixer, IF, RSSI, and FM detection circuits.
- 2) Can be operated at mixer input frequencies ranging from 20MHz to 300MHz.
- 3) Equipped with a battery power saving function.
- 4) Fast RSSI response.
- 5) High FM detection sensitivity. (21.2mV / kHz)

## ●Block diagram



#### ● **Absolute maximum ratings** (Ta=25°C, for measurement circuit)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	7.0	V
Power dissipation	P□	350 <sup>*1</sup>	mW
Storage temperature	Tstg	-55~+125	°C

<sup>\*1</sup> Reduced by 3.5 mW for each increase in Ta of 1°C over 25°C.

## Operating range

Parameter	Symbol	Limits	Unit
Operating power supply voltage	Vcc	2.3~5.5	V
Operating temperature	Topr	-40~+85	°C

#### ●Pin descriptions

Pin No.	Function	Internal peripheral circuit	DC voltage(V)
1	Local oscillator pin (base)  Connect crystal resonator and capacitor	Voc ¥ŏ ¥ŏ	Vcc - 0.6
2	Local oscillator pin (emitter)  Connect capacitor or input local signal from external oscillator	2   To MIXER	Vcc
3	Battery save pin  "Pin 3 voltage" ≤ 0.2 : Battery save 2 V ≤ "Pin 3 voltage" ≤ Vcc : Active	30k W	-
4	Mixer output pin Connect ceramic filter Output impedance: 330Ω	270 WW	Vcc - 1.5
5	GND pin	GND for IF stages and FM detection stages	GND

Pin No.	Function	Internal peripheral circuit	DC voltage (V)
6	IF amplifier bypass pin Connect capacitor	V <sub>CC</sub> → × × × × × × × × × × × × × × × × × ×	Vcc
7	IF amplifier input pin Connect ceramic filter Input impedance: 330Ω	7	Vcc
8	Vcc pin 1	Vcc for MIX stages and IF front stage	Vcc
9	Vcc pin 2	Vcc for IF rear stage and FM detection stage	Vcc
10	RSSI output pin Connect capacitor	10 You	0.1
11 12	IF amplifier output pin Pins 11 and 12 are opposite-phase output	100 11 100	Vcc - 1

Pin No.	Function	Internal peripheral circuit	DC voltage(V)
13	Discriminator pin  Connect phase shift coil or ceramic discriminator	13 Voc	Vcc
14	FM demodulation signal output pin Output impedance is $360\Omega$	330 14	0.9
15	GND pin	GND for MIX stage	GND
16	Mixer pin Connect first IF signal from DC cutoff	7cc Vcc Vcc Vcc Vcc Vcc Vcc Vcc Vcc Vcc	1.0

• Electrical characteristics (unless otherwise noted, Ta=25 $^{\circ}$ C, Vcc=3.0V) Signal source : fin (MIX) = 248.45MHz, fin (LO) = 237.75MHz, 100dB $\mu$ V

AC level to be indicated by termination

Parameter	Symbol	Min.	Тур.	Мах.	Unit	Conditions
Quiescent current	lα	4.4	5.5	6.6	mA	With local oscillation OFF
Battery save quiescent current	IQ (BS)	-	0	5	μΑ	
Dattery save function input valters	<b>V</b> тн-н	2	-	Vcc	V	Active
Battery save function input voltage	V <sub>TH-L</sub>	GND	-	0.2	V	Battery save
〈 MIX - Oscillator section 〉						
Mixer operating frequency	fмıx	20	-	300	MHz	
Mixer conversion gain	Gvc	16	20	24	dB	$V_{IN (MIX)} = 60 dB \mu V$
-1dB compression output level	Vом	-	103	-	dBμV	
3rd order intercept point	IP3	-	110	-	dBμV	f1=248.75MHz,f2=249.05MHz
Noise figure	NF	-	9.7	-	dB	LC matching input
Mixer input admittance	YIN (MIX)	-	1.25+j7.47	-	ms	f=248.45MHz
Mixer output resistance	Ro (міх)	-	330	-	Ω	
Local oscillator operating frequency	fLO	20	-	120	MHz	
Local input level	VIN (LO)	95	100	105	dBμV	
Local input admittance	YIN (LO)	-	1.36+j9.72	-	ms	f=237.75MHz

Signal source : fin (MIX) = 248.45MHz, fin (LO) = 237.75MHz,100dB $\mu$ V, fin (IF) = 10.7MHz; AC level to be indicated by termination

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
⟨ IF section ⟩						
IF operating frequency	fiF	4	-	15	MHz	
IF amplifier gain	Gv	-	75	-	dB	
IF input resistance	RIN (IF)	-	330	-	Ω	
IF output level	Voif	0.4	0.5	0.6	V <sub>P-P</sub>	$V_{IN (IF)} = 80dB\mu V$
IF duty ratio	DR	40	50	60	%	$V_{IN\;(IF)}=80dB\mu V,\;C_L=10pF$
〈 RSSI section 〉	•					
Output voltage 1	VRSSI1	-	0.15	0.4	V	No input
Output voltage 2	VRSSI2	1.0	1.2	1.4	V	$V_{IN\;(IF)}=70dB\mu V$
Output voltage 3	V <sub>RSSI3</sub>	1.8	2.0	2.2	V	$V_{IN\ (IF)} = 100dB\mu V$
Dynamic range	DR	-	70	-	dB	
Output resistance	Ro (RSSI)	12	15	18	kΩ	
Rise time at power on	Ton	-	20	-	μs	$C_L = 100 pF$ , $V_{IN (MIX)} = 60 dB \mu V$
Fall time at power off	Toff	-	5	-	μs	C <sub>L</sub> = 100pF, V <sub>IN (MIX)</sub> = 60dBμV
RSSI rise time	TR	-	9	-	μs	C <sub>L</sub> = 100pF, V <sub>IN (MIX)</sub> = 60dBμV
RSSI fall time	TF	-	11	-	μs	C <sub>L</sub> = 100pF, V <sub>IN (MIX)</sub> = 60dBμV

Signal source : fin (IF) =10.7MHz,  $\Delta f$ =±10kHz dev, fm=1kHz; AC level to be indicated by termination

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
〈 Detector unit 〉							
Detection sensitivity	SDET	-	21.2	-	mV / kHz	$V_{IN}(IF) = 80dB\mu V$	
Detection output level	Vo	110	150	195	mVrms	$V_{IN (IF)} = 80dB\mu V$	
Detection frequency	fdet	-	100	-	kHz	$V_{IN (IF)} = 80 dB \mu V$	
12 dB SINAD sensitivity	S (12dB)	12	16	20	dBμV		
S / N ratio	S/N	40	48	-	dB	$V_{IN\;(IF)}=80dB\mu V$	
AM rejection ratio	AMR	-	40	-	dB	$V_{IN\;(IF)}=80dB\mu V,\;AM=30\%$	

#### Measurement circuit

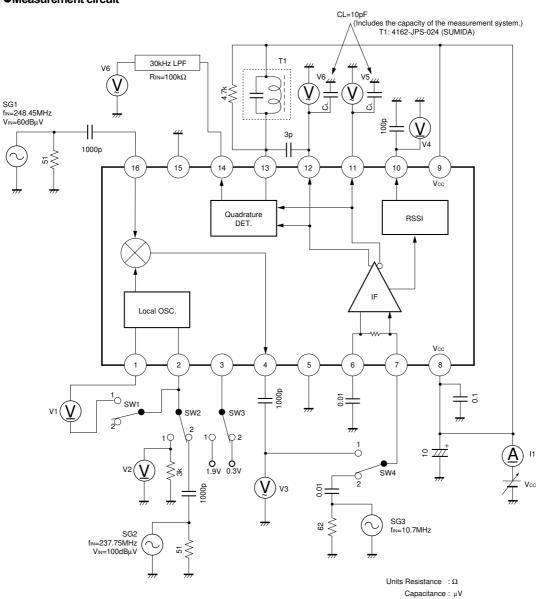


Fig. 1

#### Application example

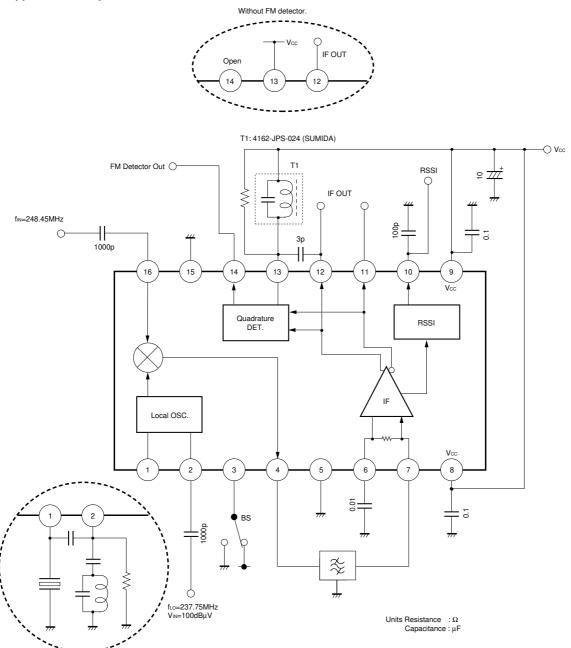
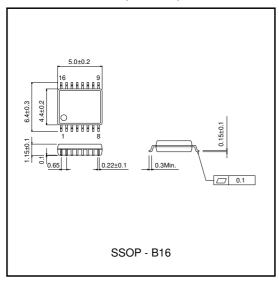


Fig. 2

# ●External dimensions (Units: mm)



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