



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



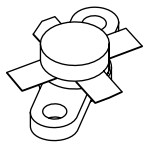
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BLF145

HF power MOS transistor

Rev. 04 — 5 January 2007

Product data sheet

IMPORTANT NOTICE

Dear customer,

As from October 1st, 2006 Philips Semiconductors has a new trade name - NXP Semiconductors, which will be used in future data sheets together with new contact details.

In data sheets where the previous Philips references remain, please use the new links as shown below.

<http://www.philips.semiconductors.com> use <http://www.nxp.com>

<http://www.semiconductors.philips.com> use <http://www.nxp.com> (Internet)

sales.addresses@www.semiconductors.philips.com use salesaddresses@nxp.com (email)

The copyright notice at the bottom of each page (or elsewhere in the document, depending on the version)

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is replaced with:

- © NXP B.V. (year). All rights reserved. -

If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or phone (details via salesaddresses@nxp.com). Thank you for your cooperation and understanding,

NXP Semiconductors

HF power MOS transistor

BLF145

FEATURES

- High power gain
- Low noise figure
- Good thermal stability
- Withstands full load mismatch.

DESCRIPTION

Silicon N-channel enhancement mode vertical D-MOS transistor designed for SSB transmitter applications in the HF frequency range. The transistor is encapsulated in a 4-lead, SOT123A flange package, with a ceramic cap. All leads are isolated from the flange. Matched gate-source voltage (V_{GS}) groups are available on request.

PINNING - SOT123A

PIN	DESCRIPTION
1	drain
2	source
3	gate
4	source

PIN CONFIGURATION

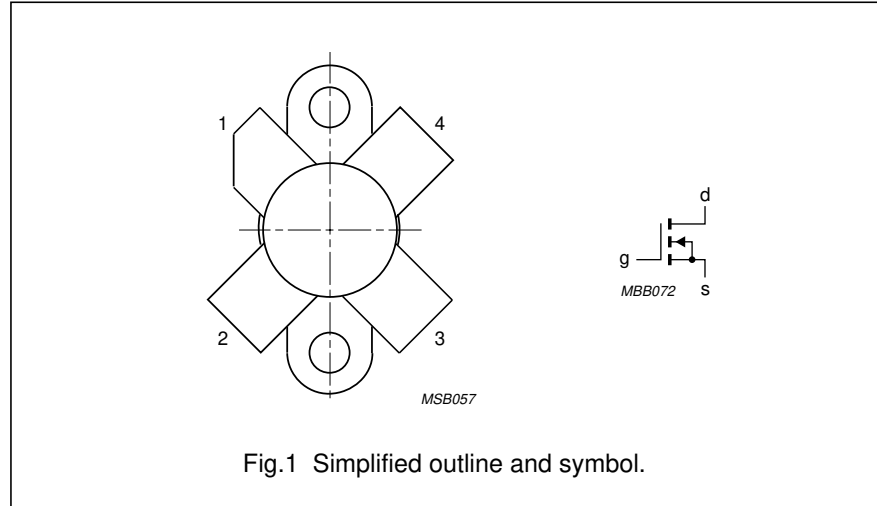


Fig.1 Simplified outline and symbol.

CAUTION

This product is supplied in anti-static packing to prevent damage caused by electrostatic discharge during transport and handling. For further information, refer to Philips specs.: SNW-EQ-608, SNW-FQ-302A, and SNW-FQ-302B.

WARNING

Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

QUICK REFERENCE DATA

RF performance at $T_h = 25\text{ }^\circ\text{C}$ in a common source test circuit.

MODE OF OPERATION	f (MHz)	V_{DS} (V)	I_D (A)	P_L (W)	G_p (dB)	η_D (%) ⁽¹⁾	d_3 (dB)
SSB, class-A	28	28	1.3	8 (PEP)	>24	–	<-40
SSB, class-AB	28	28	–	30 (PEP)	typ. 20	typ. 40	typ. -35

Note

1. 2-tone efficiency.

HF power MOS transistor

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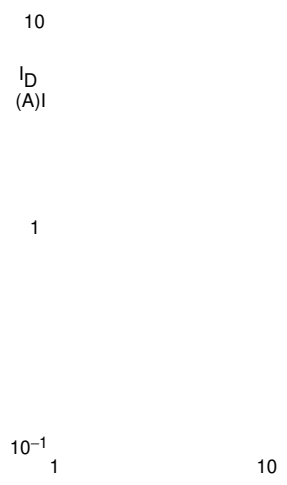
LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{DSS}	drain-source voltage		–	65	V
V _{GSS}	gate-source voltage		–	±20	V
I _D	drain current (DC)		–	6	A
P _{tot}	total power dissipation	T _{mb} ≤ 25 °C	–	68	W
T _{stg}	storage temperature		–65	150	°C
T _j	junction temperature		–	200	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-mb}	thermal resistance from junction to mounting base	2.6	K/W
R _{th mb-h}	thermal resistance from mounting base to heatsink	0.3	K/W



(1) Current in this area may be limited by R_{DS(on)}.
 (2) T_{mb} = 25 °C.

Fig.2 DC SOAR.