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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832 Email & Skype: info@chipsmall.com Web: www.chipsmall.com Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



# BLF2425M7L250P; BLF2425M7LS250P

Power LDMOS transistor

Rev. 5 — 1 September 2015

AMPLEON Product data sheet

## 1. Product profile

### 1.1 General description

250 W LDMOS power transistor for Industrial, Scientific and Medical (ISM) applications at frequencies from 2400 MHz to 2500 MHz.

The BLF2425M7L250P and BLF2425M7LS250P are designed for high-power CW applications and are assembled in high performance ceramic packages, available in eared and earless versions

#### Table 1. Typical performance

RF performance at  $T_{case} = 25 \ ^{\circ}C$  in a common source class-AB production test circuit.

Test signal	f	V <sub>DS</sub>	P <sub>L(AV)</sub>	G <sub>p</sub>	η <sub>D</sub>
	(MHz)	(V)	(W)	(dB)	(%)
CW	2450	28	250	15	51

### **1.2 Features and benefits**

- High efficiency
- Easy power control
- Excellent ruggedness
- Excellent thermal stability
- Integrated ESD protection
- Designed for broadband operation (2400 MHz to 2500 MHz)
- Internally matched
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

### 1.3 Applications

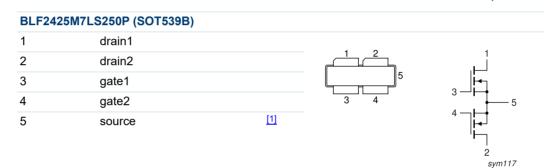
RF power amplifiers for CW applications in the 2400 MHz to 2500 MHz frequency range such as ISM and industrial heating.

**Power LDMOS transistor** 

# 2. Pinning information

Pin	Description		Simplified outline	Graphic symbol
BLF2425	M7L250P (SOT539A)			
1	drain1			
2	drain2			1
3	gate1			3
4	gate2		3 4	5
5	source	<u>[1]</u>		

2 sym117



[1] Connected to flange.

# 3. Ordering information

#### Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLF2425M7L250P	-	flanged balanced ceramic package; 2 mounting holes; 4 leads	SOT539A
BLF2425M7LS250P	-	earless flanged balanced ceramic package; 4 leads	SOT539B

# 4. Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage		-	65	V
V <sub>GS</sub>	gate-source voltage		-0.5	+13	V
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	225	°C

# 5. Thermal characteristics

Table 5.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-case)</sub>	thermal resistance from junction to case	T <sub>case</sub> = 80 °C; P <sub>L</sub> = 250 W	0.19	K/W

## 6. Characteristics

#### Table 6. DC characteristics

 $T_i = 25 \ ^{\circ}C$  per section; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$V_{GS}$ = 0 V; I <sub>D</sub> = 2.2 mA	65	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 220 mA	1.5	1.9	2.3	V
I <sub>DSS</sub>	drain leakage current	$V_{GS}$ = 0 V; $V_{DS}$ = 28 V	-	-	3	μA
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	-	39	-	А
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = 11 V; $V_{DS}$ = 0 V	-	-	300	nA
9 <sub>fs</sub>	forward transconductance	V <sub>DS</sub> = 10 V; I <sub>D</sub> = 11 A	-	16	-	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ $I_D = 7.7 A$	-	0.08	-	Ω

#### Table 7. RF characteristics

Test signal: CW at 2450 MHz; RF performance at  $V_{DS} = 28$  V;  $I_{Dq} = 20$  mA;  $T_{case} = 25$  °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G <sub>p</sub>	power gain	P <sub>L</sub> = 250 W	14	15	-	dB
RLin	input return loss	P <sub>L</sub> = 250 W	-	-18	-10	dB
$\eta_D$	drain efficiency	P <sub>L</sub> = 250 W	46	51	-	%

# 7. Test information

#### 7.1 Ruggedness in class-AB operation

The BLF2425M7L250P and BLF2425M7LS250P are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS}$  = 28 V;  $I_{Dq}$  = 20 mA;  $P_L$  = 250 W (CW); f = 2450 MHz.

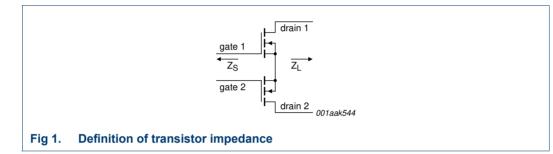
## 7.2 Impedance information

#### Table 8.Typical impedance

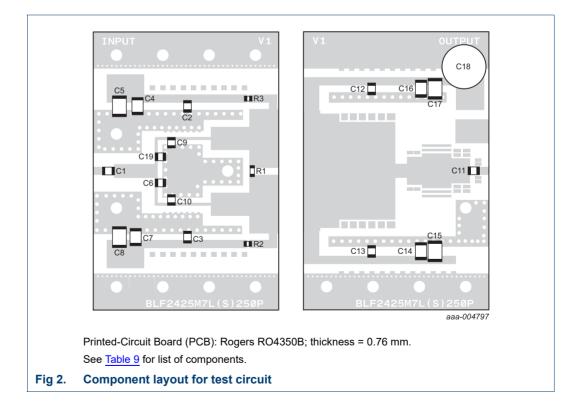
Measured load-pull data half device. Typical values unless otherwise specified.  $I_{Dq} = 20 \text{ mA}$ ;  $V_{DS} = 28 \text{ V}$ .

 $Z_S$  and  $Z_L$  defined in Figure 1.

f	Zs	ZL
(MHz)	(Ω)	(Ω)
2400	2.3 – 6.3j	3.8 – 2.7j
2450	3.3 – 6.0j	2.5 – 2.9j
2500	4.1 – 6.0j	3.3 – 2.3j



### 7.3 Test circuit

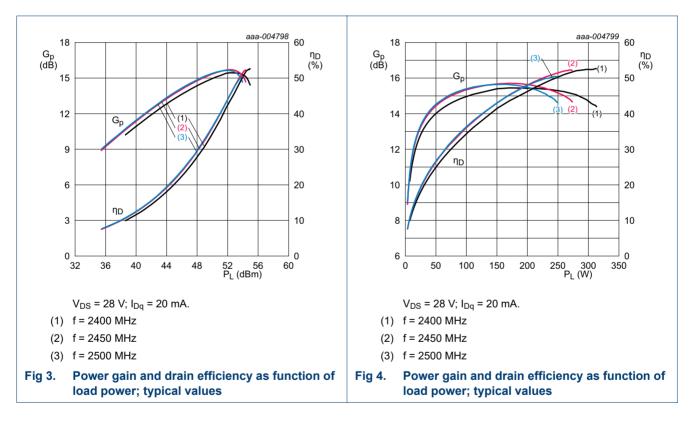


**Power LDMOS transistor** 

# Table 9. List of components For test circuit, see Figure 2

For test circuit, see r	<u>iyure z</u> .		
Component	Description	Value	Remarks
C1, C2, C3, C11, C12, C13	multilayer ceramic chip capacitor	36 pF	ATC800B
C4, C7, C14, C16	SMD capacitor	470 nF, 50 V	
C5, C8, C15, C17	SMD capacitor	10 μF, 50 V	
C6, C19	multilayer ceramic chip capacitor	1.4 pF	ATC100B
C9, C10	multilayer ceramic chip capacitor	1.8 pF	ATC100B
C18	electrolytic capacitor	470 μF, 63 V	
R1	resistor	9.1 Ω	SMD 0805
R2, R3	resistor	5.1 Ω	SMD 0805

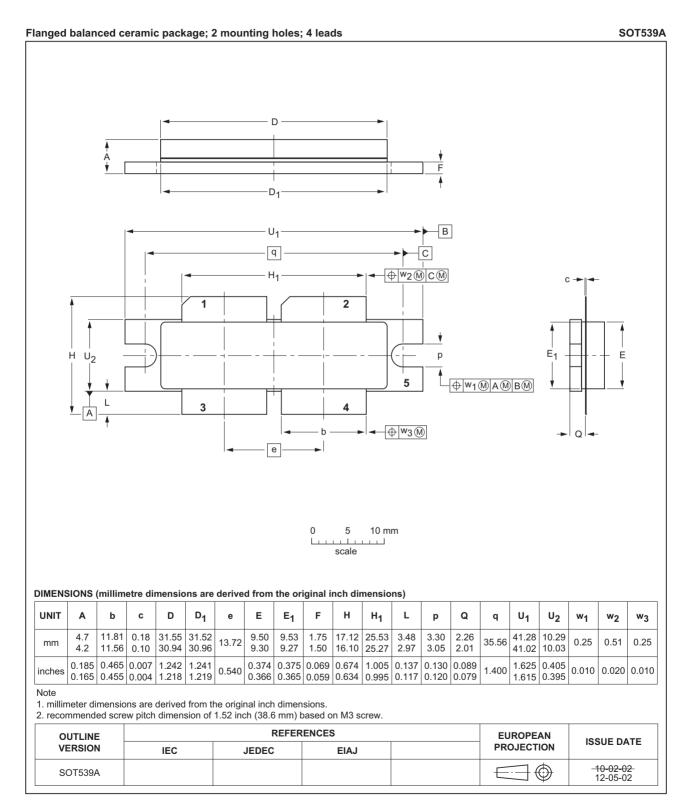
### 7.4 Graphical data



BLF2425M7L(S)250P

**Power LDMOS transistor** 

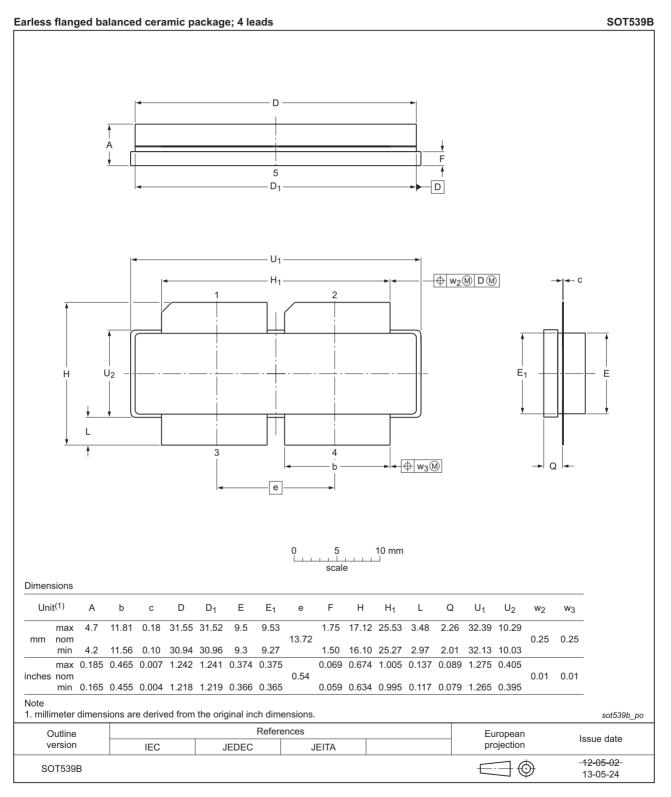
# 8. Package outline



#### Fig 5. Package outline SOT539A

BLF2425M7L(S)250P

**Power LDMOS transistor** 



#### Fig 6. Package outline SOT539B

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# 9. Handling information

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A equivalent standards.

# **10. Abbreviations**

Table 10. Abbreviations			
Acronym	Description		
CW	Continuous Wave		
ESD	ElectroStatic Discharge		
LDMOS	Laterally Diffused Metal-Oxide Semiconductor		
SMD	Surface Mounted Device		
VSWR	Voltage Standing-Wave Ratio		

# 11. Revision history

### Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF2425M7L250P_2425M7LS250P#5	20150901	Product data sheet	-	BLF2425M7L250P_2425M7LS250P v.4
Modifications:	• The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.			
	<ul> <li>Legal</li> </ul>	texts have been ada	pted to the	e new company name where appropriate.
BLF2425M7L250P_2425M7LS250P v.4	20130712	Product data sheet	-	BLF2425M7L250P_2425M7LS250P v.3
BLF2425M7L250P_2425M7LS250P v.3	20130226	Product data sheet	-	BLF2425M7L250P_2425M7LS250P v.2
BLF2425M7L250P_2425M7LS250P v.2	20120906	Objective data sheet	-	BLF2425M7L250P_2425M7LS250P v.1
BLF2425M7L250P_2425M7LS250P v.1	20110718	Objective data sheet	-	-

# 12. Legal information

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Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition		
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.		
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.		
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[2] The term 'short data sheet' is explained in section "Definitions".

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