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# **BLF6G15L-40BRN**

# **Power LDMOS transistor**

**AMPLEON** 

Rev. 3 — 1 September 2015

**Product data sheet** 

### 1. Product profile

#### 1.1 General description

40 W LDMOS power transistor for base station applications at frequencies from 1450 MHz to 1550 MHz.

Table 1. Typical performance

Typical RF performance at  $T_{case} = 25$  °C in a class-AB production test circuit.

Mode of operation	f	V <sub>DS</sub>	P <sub>L(AV)</sub>	Gp	η <sub>D</sub>	ACPR
	(MHz)	(V)	(W)	(dB)	(%)	(dBc)
2-carrier W-CDMA	1476 to 1511	28	2.5	22.0	13.0	–45 <mark>[1]</mark>

<sup>[1]</sup> Test signal: 3GPP test model 1, 64 DPCH; PAR = 7.5 dB at probability of 0.01% on CCDF carrier; carrier spacing 5 MHz.

#### **CAUTION**



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

#### 1.2 Features and benefits

- Typical 2-carrier W-CDMA performance at frequencies of 1476 MHz and 1511 MHz, a supply voltage of 28 V and an I<sub>Dq</sub> of 330 mA:
  - ◆ Average output power = 2.5 W
  - ◆ Power gain = 22.0 dB
  - ◆ Efficiency = 13.0 %
  - ◆ ACPR = -45 dBc
- Easy power control
- Integrated ESD protection
- Enhanced ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (1450 MHz to 1550 MHz)
- Internally matched for ease of use
- Compliant to Restriction of Hazardous Substances (RoHS) Directive 2002/95/EC.
- Integrated current sense

### 1.3 Applications

RF power amplifiers for W-CDMA base stations and multi carrier applications in the 1450 MHz to 1550 MHz frequency range

### 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	drain		
2	gate	45 	1 4, 5 . 🗀 🔲
3	source		
4, 5	sense drain		3 sym126
6, 7	sense gate	6 7	

<sup>[1]</sup> Connected to flange.

### 3. Ordering information

Table 3. Ordering information

Type number	Packaç	ge	
	Name	Description	Version
BLF6G15L-40BRN	-	flanged ceramic package; 2 mounting holes; 6 leads	SOT1112A

### 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	65	V
$V_{GS}$	gate-source voltage		-0.5	+11	V
V <sub>GS(sense)</sub>	sense gate-source voltage		-0.5	+9	V
$I_{D}$	drain current		-	11	Α
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	200	°C

#### 5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-case)</sub>	thermal resistance from junction to case	$T_{case}$ = 80 °C; $P_L$ = 2.5 W (CW)	1.6	K/W

#### 6. Characteristics

Table 6. Characteristics

 $T_i = 25$  °C per section; unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 0.59 \text{ mA}$	65	-	-	V
V <sub>GS(th)</sub>	gate-source threshold voltage	$V_{DS}$ = 10 V; $I_{D}$ = 59 mA	1.4	1.9	2.4	V
$I_{Dq}$	quiescent drain current	sense transistor: $I_{DS}$ = 5.1 mA; $V_{DS}$ = 12 V main transistor: $V_{DS}$ = 28 V	280	330	380	mA
I <sub>DSS</sub>	drain leakage current	$V_{GS} = 0 \text{ V}; V_{DS} = 28 \text{ V}$	-	-	1.4	μΑ
I <sub>DSX</sub>	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	8.8	10	-	Α
I <sub>GSS</sub>	gate leakage current	$V_{GS} = 11 \text{ V}; V_{DS} = 0 \text{ V}$	-	-	140	nΑ
9 <sub>fs</sub>	forward transconductance	$V_{DS}$ = 10 V; $I_{D}$ = 2.9 A	2.7	4.3	-	S
R <sub>DS(on)</sub>	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 2.06 \text{ A}$	0.09	0.25	0.39	Ω

### 7. Application information

#### Table 7. 2-carrier W-CDMA RF performance

Class-AB production test circuit; PAR 7.5 dB at 0.01 % probability on CCDF; 3GPP test model 1; 64 DPCH;  $f_1$  = 1473.4 MHz;  $f_2$  = 1478.4 MHz;  $f_3$  = 1508.4 MHz;  $f_4$  = 1513.4 MHz; RF performance at  $V_{DS}$  = 28 V;  $I_{Dq}$  = 330 mA;  $T_{case}$  = 25 °C; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$P_{L(AV)}$	average output power		-	2.5	-	W
Gp	power gain	$P_{L(AV)} = 2.5 W$	19.8	22.0	-	dB
RLin	input return loss	$P_{L(AV)} = 2.5 W$	10	15	-	dB
$\eta_{D}$	drain efficiency	$P_{L(AV)} = 2.5 W$	11	13	-	%
ACPR	adjacent channel power ratio	$P_{L(AV)} = 2.5 W$	-	-45	-40	dBc

#### Table 8. 1 carrier W-CDMA PAR performance

Class-AB production test circuit; PAR 7.5 dB at 0.01 % probability on CCDF; 3GPP test model 1; 64 DPCH;  $f_1$  = 1510.9 MHz; RF performance at  $V_{DS}$  = 28 V;  $I_{Dq}$  = 330 mA;  $T_{case}$  = 25 °C; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
PAR <sub>O</sub>	output peak-to-average ratio	P <sub>L(AV)</sub> = 10 W at 0.01 % probability on CCDF	5.3	6.0	-	dB

#### 7.1 Ruggedness in class-AB operation

The BLF6G15L-40BRN is capable of withstanding a load mismatch corresponding to VSWR = 10 :1 through all phases under the following conditions:  $V_{DS}$  = 28 V;  $I_{Dq}$  = 330 mA;  $P_{L}$  = 30 W; f = 1475 MHz (CW).

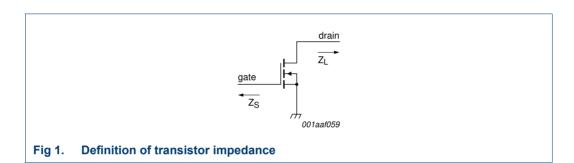
### 7.2 Impedance information

Table 9. Typical impedance per section

 $I_{Dq} = 330 \text{ mA}$ ; main transistor  $V_{DS} = 28 \text{ V}$ 

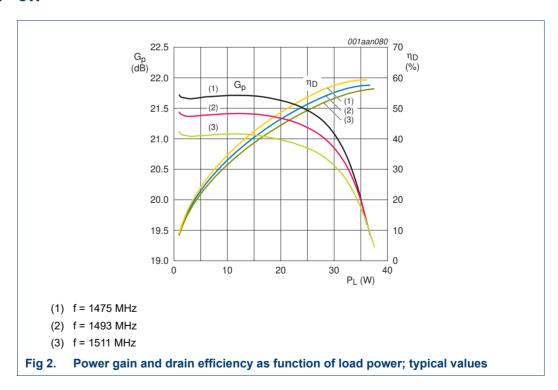
f	Z <sub>S</sub> [1]	Z <sub>L</sub> [1]
(MHz)	(Ω)	(Ω)
1480	3.2 – j6.3	4.6 – j4.5
1510	4.4 – j6.5	4.6 – j4.5

[1] Z<sub>S</sub> and Z<sub>L</sub> defined in Figure 1.



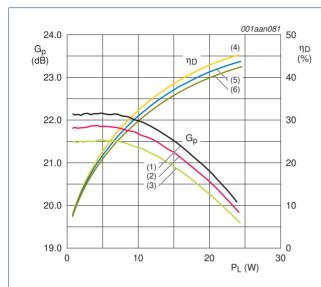
### 7.3 Graphs

#### 7.3.1 CW



001aan082

#### 7.3.2 2C-WCDMA (5 MHz spacing)



3GPP, test model 1; 64 DPCH, PAR = 7.5 dB at 0.01 % probability per carrier. 5 MHz carrier spacing.

- (1) f = 1475 MHz
- (2) f = 1493 MHz
- (3) f = 1511 MHz

-15
ACPR
(dBc)
-25
-35
-45
-55
0
10
20
PL (W)

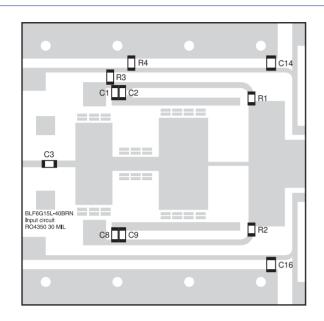
3GPP, test model 1; 64 DPCH, PAR = 7.5 dB at 0.01 % probability per carrier. 5 MHz carrier spacing.

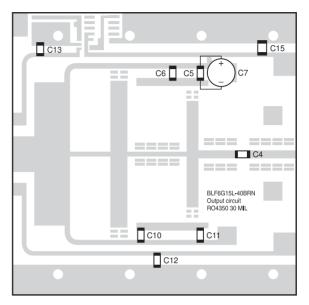
- (1) f = 1475 MHz
- (2) f = 1493 MHz
- (3) f = 1511 MHz

Fig 3. Power gain and drain efficiency as function of load power; typical values

Fig 4. Adjacent channel power ratio as a function of load power; typical values

### 8. Test information





014aab103

Printed-Circuit Board (PCB): Rogers RO4350;  $\varepsilon_r = 3.5$  F/m; thickness = 0.762 mm; thickness copper plating = 35  $\mu$ m.

The vias can be as a reference to place components.

The above layout shows the test circuit used to measure the devices in production. A more appropriate application demonstration for specific customer needs can be provided.

See Table 10 for list of components.

Fig 5. Component layout

**Table 10. List of components** See Figure 5 for component layout.

ultilayer ceramic chip capacitor		
annayor ocranno omp capacitor	68 pF	[1]
ultilayer ceramic chip capacitor	160 pF	<u>[1]</u>
ultilayer ceramic chip capacitor	24 pF	[2]
ultilayer ceramic chip capacitor	47 pF	<u>[1]</u>
ectrolytic capacitor	470 μF; 63 V	
ultilayer ceramic chip capacitor	15 pF	<u>[1]</u>
ultilayer ceramic chip capacitor	43 pF	<u>[1]</u>
ultilayer ceramic chip capacitor	20 pF	<u>[1]</u>
ultilayer ceramic chip capacitor	1 μF	Murata 0603
ultilayer ceramic chip capacitor	100 pF	
ip resistor	15 Ω	Philips 0603
nip resistor	820 Ω	Philips 0603
nip resistor	1.8 kΩ	Philips 0603
	ultilayer ceramic chip capacitor ultilayer ceramic chip capacitor ectrolytic capacitor ultilayer ceramic chip capacitor ip resistor	ultilayer ceramic chip capacitor $24 \text{ pF}$ ultilayer ceramic chip capacitor $47 \text{ pF}$ ectrolytic capacitor $470 \text{ µF}$ ; $63 \text{ V}$ ultilayer ceramic chip capacitor $15 \text{ pF}$ ultilayer ceramic chip capacitor $43 \text{ pF}$ ultilayer ceramic chip capacitor $20 \text{ pF}$ ultilayer ceramic chip capacitor $1 \text{ µF}$ ultilayer ceramic chip capacitor $100 \text{ pF}$ ip resistor $15 \Omega$ ip resistor $820 \Omega$

<sup>[1]</sup> American Technical Ceramics type 100B or capacitor of same quality.

**Product data sheet** 

<sup>[2]</sup> American Technical Ceramics type 800B or capacitor of same quality.

### 9. Package outline

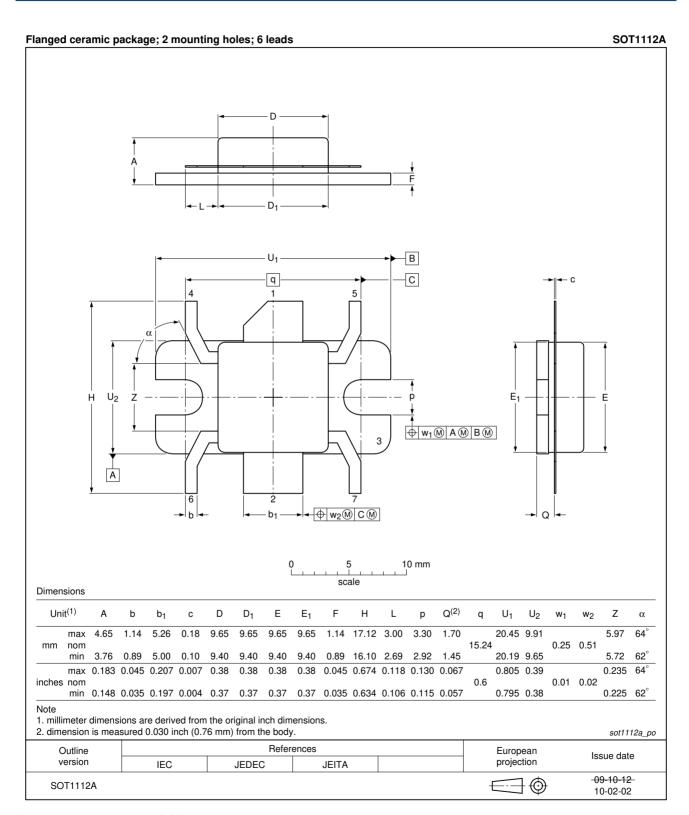


Fig 6. Package outline SOT1112A

### 10. Abbreviations

Table 11. Abbreviations

Acronym	Description
3GPP	3rd Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
PAR	Peak-to-Average power Ratio
DPCH	Dedicated Physical Channel
RF	Radio Frequency
VSWR	Voltage Standing-Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

# 11. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
BLF6G15L-40BRN#3	20150901	Product data sheet	-	BLF6G15L-40BRN v.2		
Modifications:	guidelines of A	The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.				
	<ul> <li>Legal texts na</li> </ul>	ive been adapted to the n	ew company name w	/nere appropriate.		
BLF6G15L-40BRN v.2	20101112	Product data sheet	-	BLF6G15L-40BRN v.1		
BLF6G15L-40BRN v.1	20100914	Preliminary data sheet	-	-		

### 12. Legal information

#### 12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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# **AMPLEON**

# BLF6G15L-40BRN

**Power LDMOS transistor** 

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