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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







BLF6G22-180RN; BLF6G22LS-180RN Power LDMOS transistor

AMPLEON

Rev. 2 — 1 September 2015

Product data sheet

Product profile

General description

180 W LDMOS power transistor for base station applications at frequencies from 2000 MHz to 2200 MHz.

Table 1. Typical performance

Typical RF performance at $T_{case} = 25$ °C in a class-AB production test circuit.

Mode of operation	f	V _{DS}	P _{L(AV)}	Gp	η_D	IMD3	ACPR
	(MHz)	(V)	(W)	(dB)	(%)	(dBc)	(dBc)
2-carrier W-CDMA	2110 to 2170	30	40	16.0	25	-38 <mark>[1]</mark>	-42 <mark>[1]</mark>

^[1] Test signal: 3GPP; test model 1; 64 DPCH; PAR = 7 dB at 0.01 % probability on CCDF per carrier; carrier spacing 10 MHz.

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Therefore care should be taken during transport and handling.

1.2 Features

- Typical 2-carrier W-CDMA performance at frequencies of 2110 MHz and 2170 MHz, a supply voltage of 30 V and an I_{Dq} of 1400 mA:
 - Average output power = 40 W
 - ◆ Power gain = 16.0 dB
 - ◆ Efficiency = 25 %
 - ◆ IMD3 = -38 dBc
 - ◆ ACPR = -42 dBc
- Easy power control
- Integrated ESD protection
- Enhanced ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (2000 MHz to 2200 MHz)
- Internally matched for ease of use

 Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

 RF power amplifiers for GSM, GSM EDGE, W-CDMA and CDMA base stations and multi carrier applications in the 2000 MHz to 2200 MHz frequency range

2. Pinning information

Table 2. Pinning

Pin	Description		Simplified outline	Graphic symbol
BLF6G22	2-180RN (SOT502A)			
1	drain			,
2	gate			نے.
3	source	<u>[1]</u>		2
				3 sym112
BLF6G22	2LS-180RN (SOT502B)			
1	drain			_
2	gate			'لــا
3	source	<u>[1]</u>	2	2
				3 sym112
				Symiiz

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package							
	Name	Description	Version					
BLF6G22-180RN	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A					
BLF6G22LS-180RN	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B					

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		-0.5	+13	V
I _D	drain current		-	49	Α
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		-	225	°C

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5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Туре	Тур	Unit
R _{th(j-case)}	thermal resistance from	$T_{case} = 80 ^{\circ}C;$	BLF6G22-180RN	0.50	K/W
	junction to case	$P_L = 40 W$	BLF6G22LS-180RN	0.37	K/W

6. Characteristics

Table 6. Characteristics

 $T_i = 25$ °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 0.9 \text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	V_{DS} = 10 V; I_{D} = 270 mA	1.4	2.0	2.4	V
V_{GSq}	gate-source quiescent voltage	V_{DS} = 28 V; I_{D} = 1.62 A	1.5	2.0	2.5	V
I _{DSS}	drain leakage current	$V_{GS} = 0 \text{ V}; V_{DS} = 28 \text{ V}$	-	-	5	μΑ
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	40	45	-	Α
I _{GSS}	gate leakage current	$V_{GS} = 13 \text{ V}; V_{DS} = 0 \text{ V}$	-	-	450	nA
9 _{fs}	forward transconductance	V_{DS} = 10 V; I_{D} = 13.5 A	-	19.5	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 9.45 \text{ A}$	-	0.06	-	Ω
C _{rs}	feedback capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 30 \text{ V};$ f = 1 MHz	-	3.3	-	pF

7. Application information

Table 7. Application information

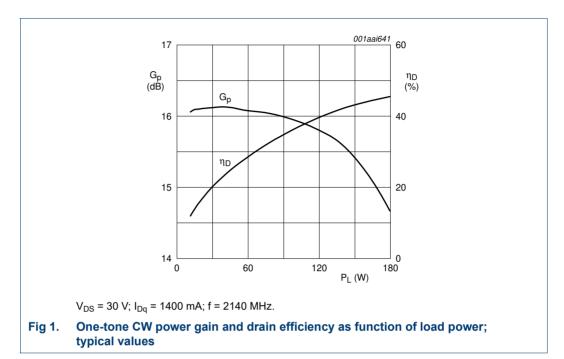
Mode of operation: 2-carrier W-CDMA; PAR = 7 dB at 0.01 % probability on the CCDF; 3GPP test model 1; 1-64 PDPCH; f_1 = 2112.5 MHz; f_2 = 2122.5 MHz; f_3 = 2157.5 MHz; f_4 = 2167.5 MHz; RF performance at V_{DS} = 30 V; I_{Dq} = 1400 mA; T_{case} = 25 °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$P_{L(AV)}$	average output power		-	40	-	W
Gp	power gain	$P_{L(AV)} = 40 W$	15.0	16.0	-	dB
RLin	input return loss	$P_{L(AV)} = 40 W$	-	-11	-8	dB
η_{D}	drain efficiency	$P_{L(AV)} = 40 W$	22	25	-	%
IMD3	third order intermodulation distortion	$P_{L(AV)} = 40 W$	-	-38	-34.5	dBc
ACPR	adjacent channel power ratio	$P_{L(AV)} = 40 \text{ W}$	-	-42	-39	dBc

7.1 Ruggedness in class-AB operation

The BLF6G22-180RN and BLF6G22LS-180RN are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 30 V; I_{Dq} = 1400 mA; P_{L} = 180 W (CW); f = 2170 MHz.

7.2 One-tone CW



7.3 Two-tone CW

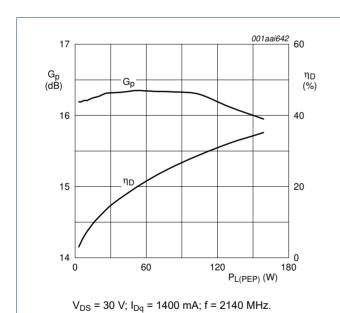
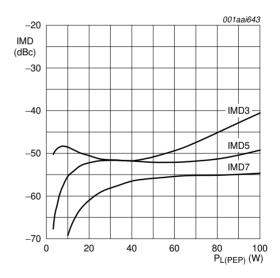


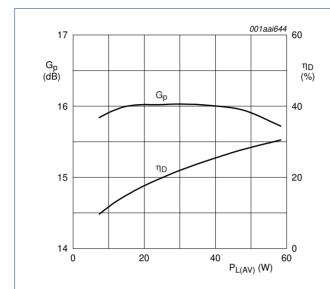
Fig 2. Two-tone CW power gain and drain efficiency as function of peak envelope load power; typical values



 $V_{DS} = 30 \text{ V}; I_{Dq} = 1400 \text{ mA}; f = 2140 \text{ MHz}.$

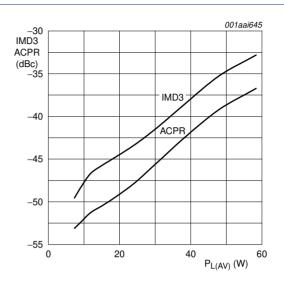
Fig 3. Two-tone CW intermodulation distortion as a function of peak envelope load power; typical values

7.4 2-carrier W-CDMA



 V_{DS} = 30 V; I_{Dq} = 1400 mA; f = 2140 MHz (± 5 MHz); carrier spacing 10 MHz.

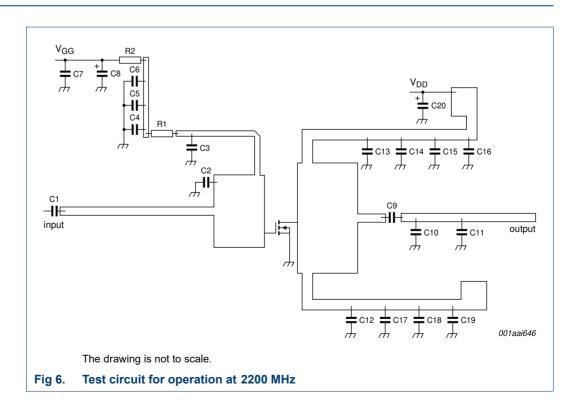
Fig 4. 2-carrier W-CDMA power gain and drain efficiency as function of average load power; typical values



 V_{DS} = 30 V; I_{Dq} = 1400 mA; f = 2140 MHz (±5 MHz); carrier spacing 10 MHz.

Fig 5. 2-carrier W-CDMA adjacent channel power ratio and third order intermodulation distortion as function of average load power; typical values

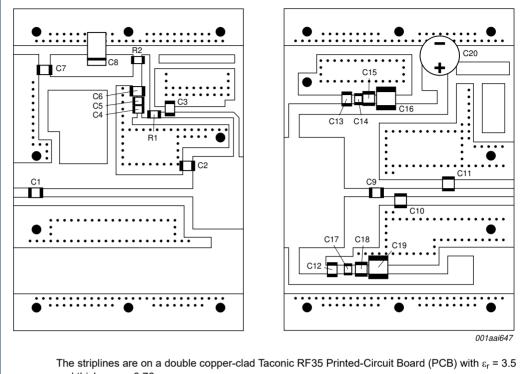
8. Test information



BLF6G22-180RN_22LS-180RN#2

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and thickness = 0.76 mm.

See Table 8 for list of components.

The drawing is not to scale.

Fig 7. **Component layout**

List of components (see Figure 6 and Figure 7)

The Printed-Circuit Board (PCB) used is a double copper-clad Taconic RF35 with $\varepsilon_r = 3.5$ and thickness = 0.76 mm.

Component	Description	Value		Remarks
C1, C3, C12, C13	multilayer ceramic chip capacitor	13 pF	[1]	ATC 100B or capacitor of same quality
C2	multilayer ceramic chip capacitor	1.4 pF	[1]	ATC 100B or capacitor of same quality
C4, C5, C14, C17	multilayer ceramic chip capacitor	220 nF		Vishay or capacitor of same quality
C6, C7	multilayer ceramic chip capacitor	100 nF		Vishay or capacitor of same quality
C8	multilayer ceramic chip capacitor	10 μF		
C9	multilayer ceramic chip capacitor	12 pF	[1]	ATC 100B or capacitor of same quality
C10	multilayer ceramic chip capacitor	1.1 pF	[1]	ATC 100B or capacitor of same quality
C11	multilayer ceramic chip capacitor	0.7 pF	[1]	ATC 100B or capacitor of same quality
C15, C18	multilayer ceramic chip capacitor	1.5 μF		
C16, C19	multilayer ceramic chip capacitor	10 μF; 50 V		TDK or capacitor of same quality
C20	electrolytic capacitor	220 μF; 63 V		
L1	ferrite SMD bead	-		Ferroxcube BDS 3/3/4.6-4S2 or equivalent
R1	SMD resistor	2.7 Ω		
R2, R3	SMD resistor	6.8 Ω		

Solder vertically.

9. Package outline

Flanged ceramic package; 2 mounting holes; 2 leads

SOT502A

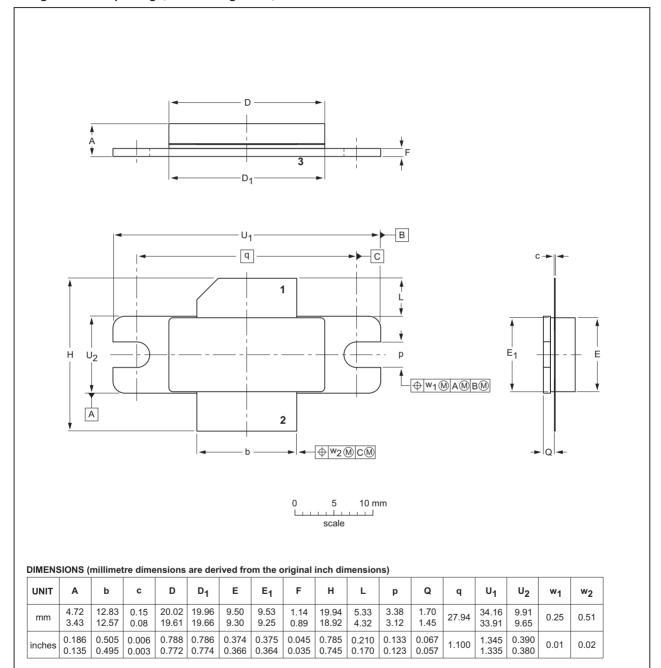


Fig 8. Package outline SOT502A

IEC

OUTLINE

VERSION

SOT502A

JEITA

REFERENCES

JEDEC

ISSUE DATE

-03-01-10

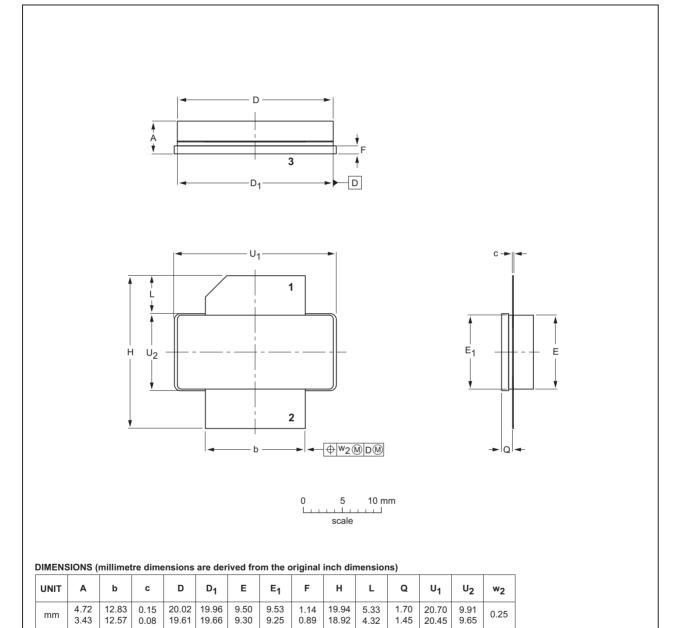
12-05-02

EUROPEAN

PROJECTION

Earless flanged ceramic package; 2 leads

SOT502B



OLITLINE REFERENCES									FUROP			
inches	0.186 0.135		0.006 0.003								l	1 () () () ()

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT502B					07-05-09 12-05-02

Fig 9. Package outline SOT502B

10. Abbreviations

Table 9. Abbreviations

Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CDMA	Code Division Multiple Access
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
EDGE	Enhanced Data rates for GSM Evolution
GSM	Global System for Mobile communications
LDMOS	Laterally Diffused Metal-Oxide Semiconductor
LDMOST	Laterally Diffused Metal-Oxide Semiconductor Transistor
PAR	Peak-to-Average power Ratio
PDPCH	transmission Power of the Dedicated Physical CHannel
RF	Radio Frequency
VSWR	Voltage Standing-Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

11. Revision history

Table 10. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
BLF6G22-180RN_22LS-180RN#2	20150901	Product data sheet	-	BLF6G22-180RN_22LS-180R N_1				
Modifications:	The format of guidelines of		been redesigned t	to comply with the new identity				
	 Legal texts have been adapted to the new company name where appropriate. 							
BLF6G22-180RN_22LS-180RN_1	20081120	Product data sheet	-	-				

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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BLF6G22(LS)-180RN

Power LDMOS transistor

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BLF6G22(LS)-180RN

Power LDMOS transistor

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