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BLF8G20LS-230V

Power LDMOS transistor

Rev. 3 — 1 September 2015

AMPLEON

Product data sheet

1. Product profile

1.1 General description

230 W LDMOS power transistor with improved video bandwidth for base station applications at frequencies from 1800 MHz to 2000 MHz.

Table 1. Typical performance

Typical RF performance at T_{case} = 25 °C in a common source class-AB production test circuit.

Test signal	f (MHz)	I_{Dq} (mA)	V_{DS} (V)	$P_{L(AV)}$ (W)	G_p (dB)	η_D (%)	ACPR (dBc)
2-carrier W-CDMA	1805 to 1880	1800	28	55	18	31.7	–29 [1]

[1] Test signal: 3GPP test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF; carrier spacing 5 MHz.

1.2 Features and benefits

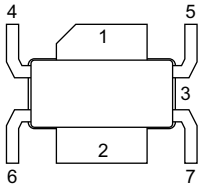
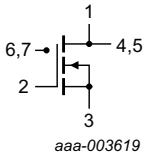
- Excellent ruggedness
- High efficiency
- Low thermal resistance providing excellent thermal stability
- Designed for broadband operation
- Lower output capacitance for improved performance in Doherty applications
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

- RF power amplifiers for multi standard and multi carrier applications in the 1800 MHz to 2000 MHz frequency range

2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	drain		
2	gate		
3	source [1]		
4	decoupling lead		
5	decoupling lead		
6	n.c.		
7	n.c.		

[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLF8G20LS-230V	-	earless flanged LDMOST ceramic package; 6 leads	SOT1239B

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		−0.5	+13	V
T_{stg}	storage temperature		−65	+150	°C
T_j	junction temperature	[1]	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the on-line MTF calculator.

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	T_{case} = 80 °C; P_L = 55 W; V_{DS} = 28 V; I_{Dq} = 1600 mA	0.27	K/W

6. Characteristics

Table 6. DC characteristics

T_j = 25 °C, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	V_{GS} = 0 V; I_D = 2.7 mA	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	V_{DS} = 10 V; I_D = 270 mA	1.5	1.9	2.3	V
V_{GSq}	gate-source quiescent voltage	V_{DS} = 28 V; I_D = 1.6 A	1.7	2.1	2.5	V
I_{DSS}	drain leakage current	V_{GS} = 0 V; V_{DS} = 28 V	-	-	4.2	μA
I_{DSX}	drain cut-off current	V_{GS} = $V_{GS(th)}$ + 3.75 V; V_{DS} = 10 V	-	50.6	-	A
I_{GSS}	gate leakage current	V_{GS} = 11 V; V_{DS} = 0 V	-	-	420	nA
g_{fs}	forward transconductance	V_{DS} = 10 V; I_D = 13.5 A	-	19.6	-	S
$R_{DS(on)}$	drain-source on-state resistance	V_{GS} = $V_{GS(th)}$ + 3.75 V; I_D = 9.45 A	-	0.06	-	Ω

Table 7. RF characteristics

Test signal: 2-carrier W-CDMA; PAR = 8.4 dB at 0.01 % probability on CCDF; 3GPP test model 1; 64 DPCH; f_1 = 1807.5 MHz; f_2 = 1812.5 MHz; f_3 = 1872.5 MHz; f_4 = 1877.5 MHz; RF performance at V_{DS} = 28 V; I_{Dq} = 1800 mA; T_{case} = 25 °C; unless otherwise specified; in a production circuit.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
G_p	power gain	$P_{L(AV)}$ = 55 W	16.8	18	-	dB
η_D	drain efficiency	$P_{L(AV)}$ = 55 W	27	31.7	-	%
RL_{in}	input return loss	$P_{L(AV)}$ = 55 W	-	−10	−6	dB
ACPR	adjacent channel power ratio	$P_{L(AV)}$ = 55 W	-	−29	−24	dBc

7. Test information

7.1 Ruggedness in class-AB operation

The BLF8G20LS-230V is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 28 V; I_{Dq} = 1800 mA; P_L = 200 W (CW); f = 1805 MHz.

7.2 Impedance information

Table 8. Typical impedance

Measured load-pull data; I_{Dq} = 1600 mA; V_{DS} = 28 V. Typical values unless otherwise specified.

f (MHz)	Z_S[1] (Ω)	Z_L[1] (Ω)
1805	1.26 – j3.29	0.90 – j2.12
1843	1.87 – j3.56	0.88 – j2.16
1880	1.97 – j3.73	0.88 – j2.18

[1] Z_S and Z_L defined in Figure 1.

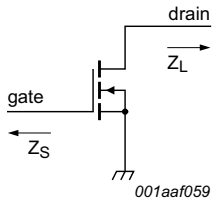
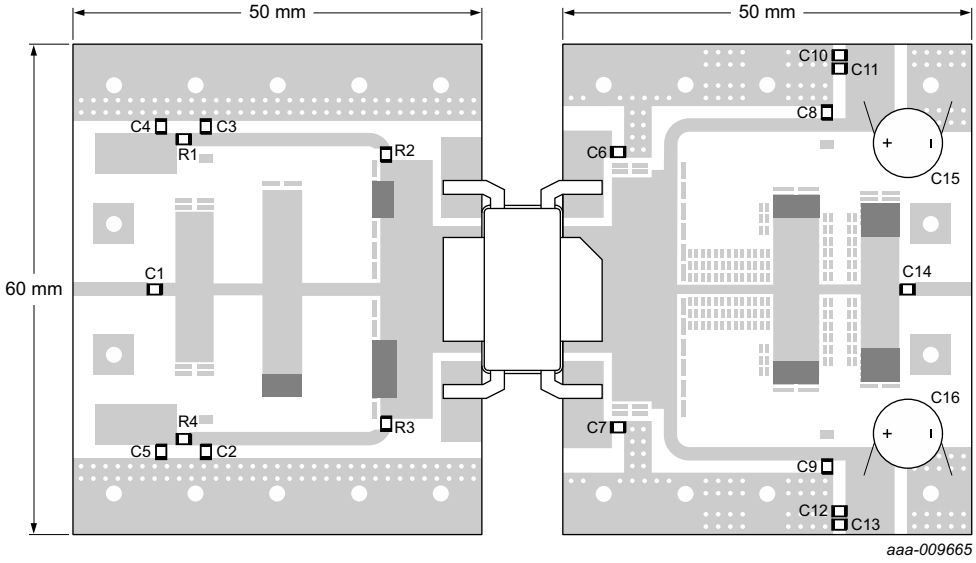


Fig 1. Definition of transistor impedance

7.3 VBW in class-AB operation

The BLF8G20LS-230V has a video bandwidth of 65 MHz (typical) when measured in a class-AB test circuit operating at a center frequency of 1843 MHz for V_{DS} = 28 V and I_{Dq} = 1600 mA.

7.4 Test circuit



Printed-Circuit Board (PCB): Rogers RO4350B; thickness = 0.76 mm.

See Table 9 for list of components.

Fig 2. Component layout for test circuit

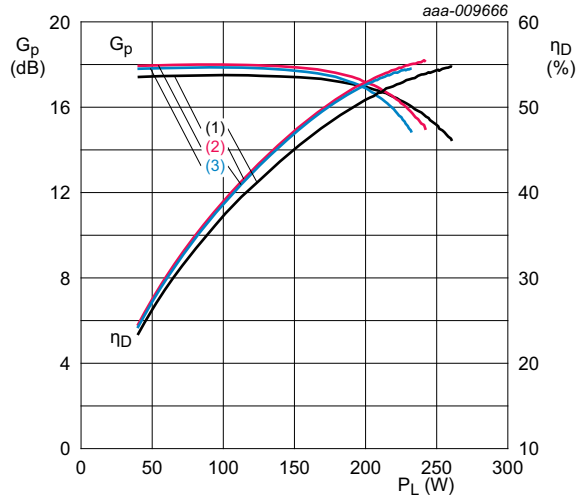
Table 9. List of components

For test circuit, see Figure 2.

Component	Description	Value	Remarks
C1, C2, C3, C8, C9, C14	multilayer ceramic chip capacitor	24 pF	ATC800B
C4, C5, C11, C12	multilayer ceramic chip capacitor	1 μF, 50 V	Murata
C6, C7, C10, C13	multilayer ceramic chip capacitor	10 μF, 50 V	Murata
C15, C16	electrolytic capacitor	2200 μF, 63 V	
R1, R2, R3, R4	chip resistor	5.1 Ω	SMD 0805

7.5 Graphical data

7.5.1 Pulsed CW



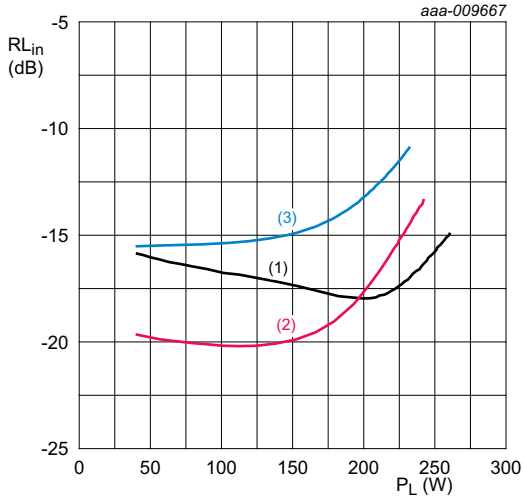
V_{DS} = 28 V; I_{Dq} = 1600 mA; t_p = 100 μs; δ = 10 %.

(1) f = 1808 MHz

(2) f = 1843 MHz

(3) f = 1880 MHz

Fig 3. Power gain and drain efficiency as function of output power; typical values



V_{DS} = 28 V; I_{Dq} = 1600 mA; t_p = 100 μs; δ = 10 %.

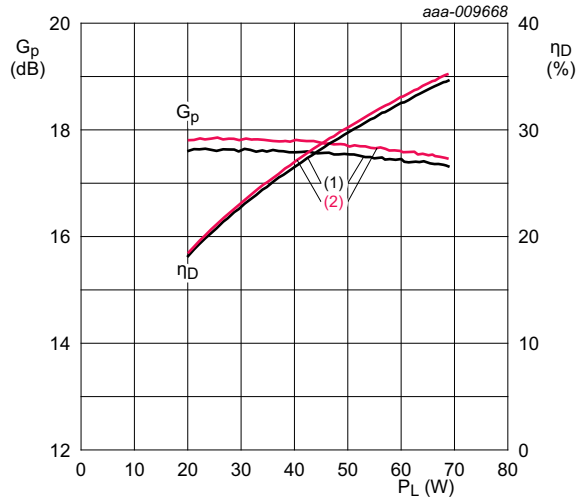
(1) f = 1808 MHz

(2) f = 1843 MHz

(3) f = 1880 MHz

Fig 4. Input return loss as a function of output power; typical values

7.5.2 2-Carrier W-CDMA

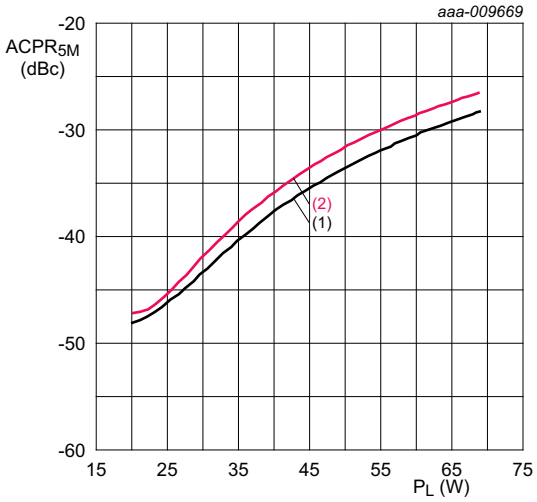


V_{DS} = 28 V; I_{Dq} = 1600 mA; carrier spacing = 5 MHz; δ = 46 %.

(1) f = 1810 MHz

(2) f = 1875 MHz

Fig 5. Power gain and drain efficiency as function of output power; typical values

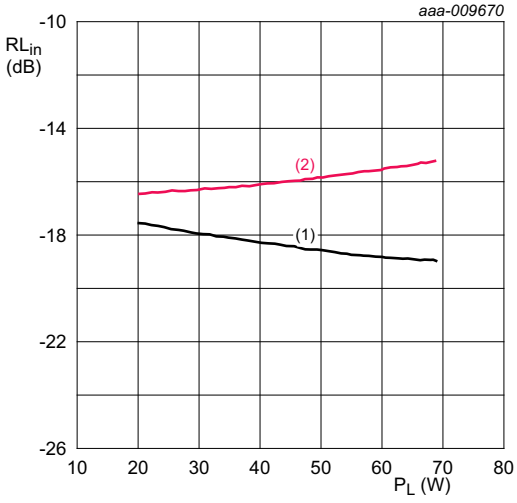


V_{DS} = 28 V; I_{Dq} = 1600 mA; carrier spacing = 5 MHz; δ = 46 %.

(1) f = 1810 MHz

(2) f = 1875 MHz

Fig 6. Adjacent channel power ratio (5 MHz) as a function of output power; typical values



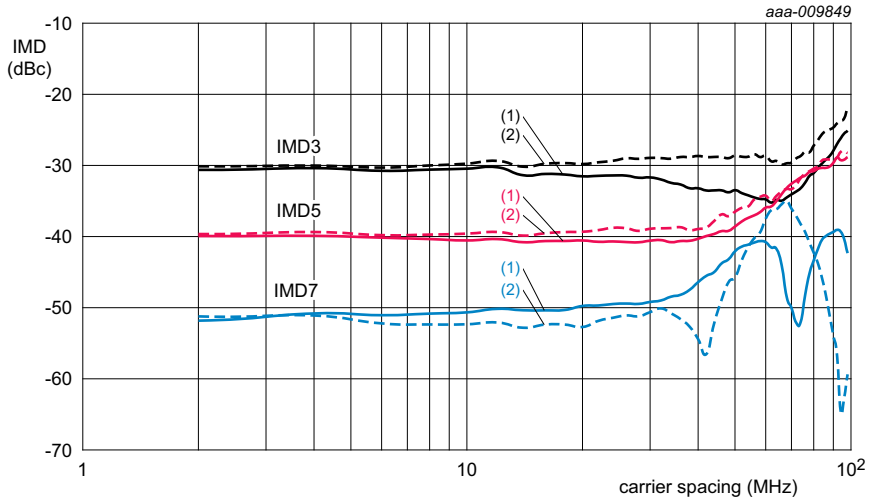
V_{DS} = 28 V; I_{Dq} = 1600 mA; carrier spacing = 5 MHz; δ = 46 %.

(1) f = 1810 MHz

(2) f = 1875 MHz

Fig 7. Input return loss as a function of output power; typical values

7.5.3 2-Tone VBW



V_{DS} = 28 V; I_{Dq} = 1600 mA; f_c = 1843 MHz.

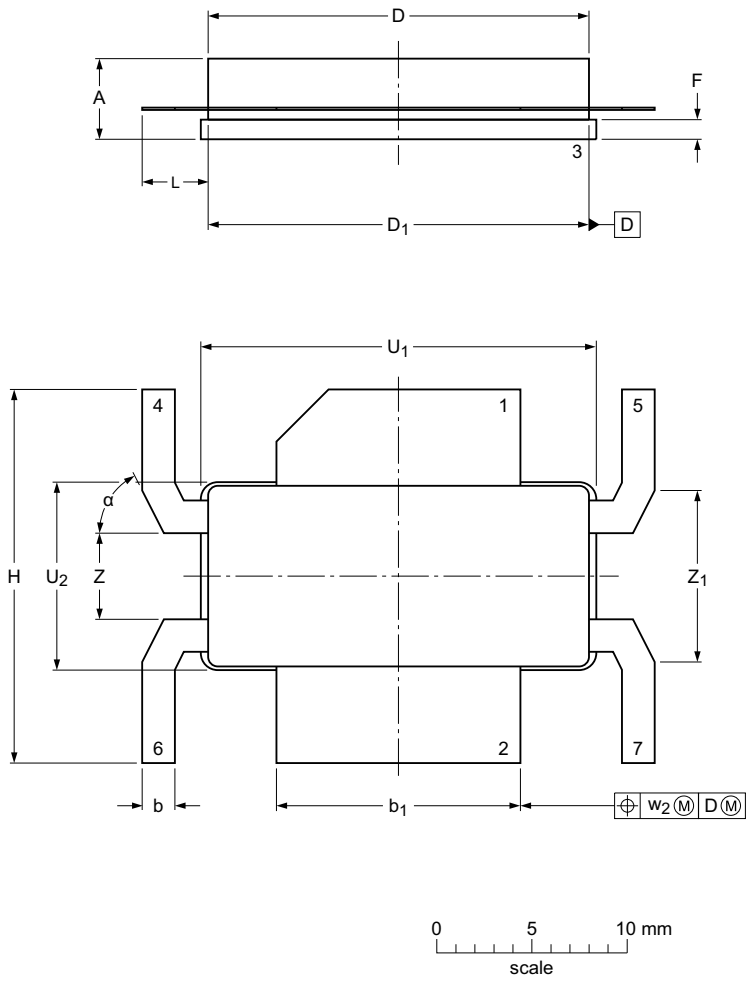
(1) IMD low

(2) IMD high

Fig 8. VBW capability in class-AB test circuit

8. Package outline

Earless flanged LDMOST ceramic package; 6 leads **SOT1239B**



Dimensions

Unit[1]		A	b	b_1	c	D	D_1	E	E_1	F	H	L	Q	U_1	U_2	w_2	Z	Z_1	α
mm	max	4.75	1.83	12.83	0.20	20.02	19.96	9.53	9.53	1.14	19.94	3.56	1.70	20.70	9.91	0.51	4.60	9.53	64°
	nom																		
	min	3.45	1.57	12.57	0.12	19.61	19.66	9.27	9.25	0.89	18.92	3.30	1.45	20.45	9.65		4.32	8.71	60°
inches	max	0.187	0.072	0.505	0.008	0.788	0.786	0.375	0.375	0.045	0.785	0.140	0.067	0.815	0.390	0.02	0.181	0.375	64°
	nom																		
	min	0.136	0.062	0.495	0.005	0.772	0.774	0.365	0.364	0.035	0.745	0.130	0.057	0.805	0.380		0.170	0.343	60°

Note
1. Millimeter dimensions are derived from the original inch dimensions.

sot1239b_po

Outline version	References				European projection	Issue date
	IEC	JEDEC	JEITA			
SOT1239B						~~13-07-09~~ 13-10-28

Fig 9. Package outline SOT1239B

9. Handling information

CAUTION	
	This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or equivalent standards.

10. Abbreviations

Table 10. Abbreviations

Acronym	Description
3GPP	3rd Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
MTF	Median Time to Failure
PAR	Peak-to-Average Ratio
SMD	Surface Mounted Device
VBW	Video BandWidth
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF8G20LS-230V#3	20150901	Product data sheet		BLF8G20LS-230V v.2
Modifications:	• The format of this document has been redesigned to comply with the new identity guidelines of Ampleon. • Legal texts have been adapted to the new company name where appropriate.			
BLF8G20LS-230V v.2	20140221	Product data sheet	-	BLF8G20LS-230V v.1
BLF8G20LS-230V v.1	20131107	Preliminary data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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