# mail

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LDMOS L-band radar power module Rev. 2 — 1 September 2015

#### **Product profile** 1.

### 1.1 General description

250 W LDMOS power module intended for L-band radar applications in the frequency range from 1.2 GHz to 1.4 GHz.

#### Table 1. **Test information**

Typical RF performance at  $T_{case} = 25 \ ^{\circ}C$ ;  $t_p = 1.8 \ ms; \delta = 30 \ \%; I_{Dq} = 200 \ mA; P_i = 26 \ dBm; in a$ class-AB production test circuit.

Test signal	f	V <sub>DS</sub>	PL	G <sub>p</sub>	η <sub>add</sub>	t <sub>r</sub>	t <sub>f</sub>
	(MHz)	(V)	(W)	(dB)	(%)	(ns)	(ns)
pulsed RF	1195 to 1405	45	190 to 290	27	48	15	5

## 1.2 Features and benefits

- Input/output 50 Ω matched
- High flexibility with respect to pulse formats
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Designed for broadband operation (1.2 GHz to 1.4 GHz)
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

### 1.3 Applications

L-band radar applications in the frequency range 1.2 GHz to 1.4 GHz

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## 2. Pinning information

## 2.1 Pinning



## 2.2 Pin description

### Table 2.Pin description

Symbol	Pin	Description
RF_IN	1	RF input
RF_OUT	2	RF output
V <sub>GS1</sub>	3	gate-source voltage 1
V <sub>DS1</sub>	4	drain-source voltage 1
V <sub>GS2</sub>	5	gate-source voltage 2
V <sub>DS2</sub>	6	drain-source voltage 2

## 3. Ordering information

### Table 3. Ordering information

Type number Package			
	Name	Description	Version
BLL6H1214P2S-250	-	pallet LDMOS; 6 mounting holes; 6 terminations	SOM039

## 4. Block diagram



## 5. Limiting values

### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	drain-source voltage		-	50	V
V <sub>GS</sub>	gate-source voltage		-0.5	+13	V
T <sub>amb</sub>	ambient temperature		5	60	°C
T <sub>mb</sub>	mounting base temperature		0	50	°C
T <sub>stg</sub>	storage temperature		-20	+70	°C
Tj	junction temperature	[1]	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the on-line MTF calculator.

## 6. Thermal characteristics

Table 5.	Thermal	characteristics
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Symbol	Parameter	Conditions	Тур	Unit
Z <sub>th(j-c)</sub>	transient thermal impedance from junction to case	$T_{case}$ = 50 °C; P <sub>i</sub> = 26 dBm; t <sub>p</sub> = 1.8 ms; $\delta$ = 30 %	0.39	K/W

## 7. Characteristics

### Table 6. RF characteristics

Test signal: pulsed RF;  $P_i = 26 \text{ dBm}$ ;  $t_p = 1.8 \text{ ms}$ ;  $\delta = 30 \%$ ; RF performance at  $V_{DS} = 45 \text{ V}$ ;  $I_{Dg} = 200 \text{ mA}$ ;  $T_{case} = 25 \degree C$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
f	frequency		1210	1300	1405	MHz
V <sub>DD</sub>	supply voltage		44.7	45	45.3	V
V <sub>GS</sub>	gate-source voltage		-	5	6.5	V
P <sub>L(sat)</sub>	saturated output power		52.8	53.0	54.3	dBm
FL	flatness of frequency response	[1]	-	-	1.2	dB
$\Delta P_L$	output power variation	$P_i$ = 26 dBm ± 0.4 dBm	-0.2	-	+0.2	
P <sub>droop(pulse)</sub>	pulse droop power		-	-	0.5	dB
G <sub>p</sub>	power gain	3 dB gain compression	-	27	-	dB
$\eta_{\text{add}}$	power added efficiency		45	48	-	%
t <sub>r</sub>	rise time		-	-	50	ns
t <sub>f</sub>	fall time		-	-	50	ns
$\alpha_{resp(sp)}$	spurious response		-	-	-60	dBc
$\alpha_{sup(H)}$	harmonic suppression		-	-	-40	dBc
MTTF	mean time to failure		1 × 10 <sup>6</sup>	-	-	h

[1] Power flatness; testing at fixed P<sub>i</sub>.

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### 7.1 Ruggedness in class-AB operation

The BLL6H1214P2S-250 is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS}$  = 45 V;  $I_{Dg}$  = 200 mA;  $P_i$  = 26 dBm;  $t_p$  = 1.8 ms;  $\delta$  = 30 %.

## 8. Test information

### 8.1 Graphical data



## AMPLEON

## BLL6H1214P2S-250

### LDMOS L-band radar power module



### LDMOS L-band radar power module

## 9. Package outline



### Fig 8. Package outline SOM039

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## **10. Handling information**

equivalent standards.

#### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices. Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or

## 11. Abbreviations

Table 7. Abbreviations				
Acronym	Description			
LDMOS	Laterally Diffused Metal-Oxide Semiconductor			
L-band	Long wave band			
MTF	Median Time to Failure			
VSWR	Voltage Standing-Wave Ratio			

## **12. Revision history**

### Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
BLL6H1214P2S-250#2	20150901	Product data sheet	-	BLL6H1214P2S-250#1	
Modifications:	<ul> <li>The format of the of Ampleon.</li> </ul>	• The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.			
	<ul> <li>Legal texts have</li> </ul>	e been adapted to the new co	ompany name where a	ppropriate.	
BLL6H1214P2S-250#1	20140812	Product data sheet	-	-	

## 13. Legal information

### 13.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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