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Contact us

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BLL9G1214L-600; BLL9G1214LS-600 LDMOS L-band radar power transistor Rev. 1 — 27 November 2017

AMPLEON

Product data sheet

Product profile

1.1 General description

600 W LDMOS power transistor for L-band radar applications in the frequency range from 1.2 GHz to 1.4 GHz.

Typical performance Table 1.

Typical RF performance at T_{case} = 25 °C; t_{p} = 300 μs ; δ = 10 %; I_{Dq} = 400 mA; in a class-AB demo test circuit.

Test signal	f	V _{DS}	P _{L(3dB)}	Gp	η _D
	(GHz)	(V)	(W)	(dB)	(%)
pulsed RF	1.2 to 1.4	32	600	19	60

1.2 Features and benefits

- High efficiency
- Excellent ruggedness
- Designed for L-band operation
- Excellent thermal stability
- Easy power control
- Integrated dual sided ESD protection enables excellent off-state isolation
- High flexibility with respect to pulse formats
- Internally matched for ease of use
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

■ L-band radar applications in the frequency range from 1.2 GHz to 1.4 GHz

2. Pinning information

Table 2. Pinning

Pin	Description		Simplified outline	Graphic symbol
BLL9G12	14L-600 (SOT502A)			
1	drain			,
2	gate		5 3	أ_ر.
3	source	[1]	2	2 — 3 3 sym112
BLL9G12	14LS-600 (SOT502B)			
1	drain			
2	gate		1 3	ئے
3	source	[1]	2	2 — 3 3 sym112

^[1] Connected to flange.

3. Ordering information

Table 3. Ordering information

Type number	Packag	nckage				
	Name	Description	Version			
BLL9G1214L-600	-	flanged ceramic package; 2 mounting holes; 2 leads	SOT502A			
BLL9G1214LS-600	-	earless flanged ceramic package; 2 leads	SOT502B			

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Min	Max	Unit
V_{DS}	drain-source voltage	-	65	V
V_{GS}	gate-source voltage	-6	+13	V
T _{stg}	storage temperature	-65	+150	°C
Tj	junction temperature [1]	-	225	°C

^[1] Continuous use at maximum temperature will affect the reliability. For details refer to the online MTF calculator.

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
$Z_{\text{th(j-mb)}}$	transient thermal impedance from junction	T _{case} = 85 °C; P _L = 600 W		
	to mounting base	t_p = 100 μ s; δ = 10 %	0.11	K/W
		t_p = 300 μ s; δ = 10 %	0.15	K/W
		t_p = 500 μ s; δ = 10 %	0.17	K/W
		t_p = 100 μ s; δ = 20 %	0.15	K/W

6. Characteristics

Table 6. DC characteristics

 $T_i = 25$ °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 4.5 \text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 10 \text{ V}; I_D = 450 \text{ mA}$	1.5	2	2.5	V
I _{DSS}	drain leakage current	$V_{GS} = 0 \text{ V}; V_{DS} = 32 \text{ V}$	-	-	5	μА
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	-	87	-	A
I _{GSS}	gate leakage current	V _{GS} = 11 V; V _{DS} = 0 V	-	-	400	nA
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 450 mA	-	4.2	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $I_D = 15.75 \text{ A}$	-	0.026	-	Ω

Table 7. RF characteristics

Test signal: pulsed RF; t_p = 300 μ s; δ = 10 %; RF performance at V_{DS} = 32 V; I_{Dq} = 400 mA; T_{case} = 25 °C; unless otherwise specified, in a class-AB production circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Gp	power gain	P _L = 600 W	16.8	19	-	dB
η_{D}	drain efficiency	P _L = 600 W	56	60	-	%
RLin	input return loss	P _L = 600 W	-	-7	-	dB
P _{droop(pulse)}	pulse droop power	P _L = 600 W	-	0.2	0.5	dB
t _r	rise time	P _L = 600 W	-	6	50	ns
t _f	fall time	P _L = 600 W	-	6	50	ns
P _{L(2dB)}	output power at 2 dB gain compression		-	575	-	W

7. Test information

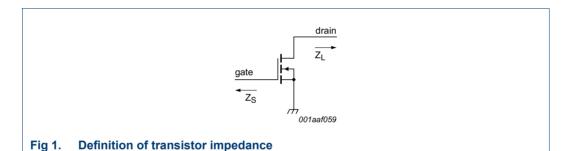
7.1 Ruggedness in class-AB operation

The BLL9G1214L-600 and BLL9G1214LS-600 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: V_{DS} = 32 V; I_{Dq} = 400 mA; P_L = 600 W; I_p = 300 μ s; δ = 10 %.

7.2 Impedance information

Table 8. Typical impedance

f	Z _S	Z_L
(GHz)	(Ω)	(Ω)
1.2	1.23 – j5.79	1.14 – j1.39
1.3	7.10 – j3.33	1.62 – j1.63
1.4	1.31 – j1.89	2.36 – j1.56



7.3 Test circuit

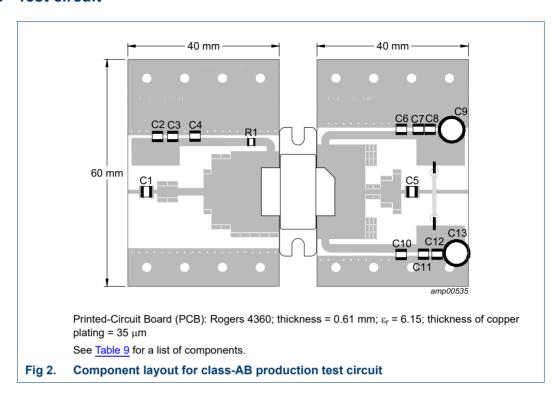
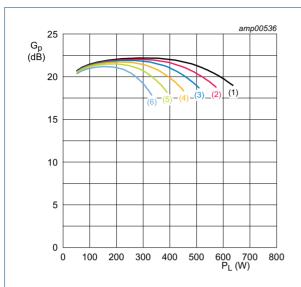


Table 9. List of components

For test circuit see Figure 2.

Component	Description	Value	Remarks
C1, C4, C5, C6, C10	multilayer ceramic chip capacitor	56 pF	ATC 100B
C2, C8, C12	multilayer ceramic chip capacitor	10 μF	Murata: GRM55DR61H106KA88L
C3, C7, C11	multilayer ceramic chip capacitor	910 pF	ATC 100B
C9, C13	electrolytic capacitor	100 μF, 63 V	
R1	resistor	5 Ω	SMD 0603

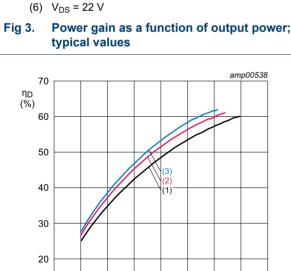
7.4 Graphical data



 I_{Dq} = 400 mA; t_p = 300 $\mu s;~\delta$ = 10 %; f = 1300 MHz.

- (1) $V_{DS} = 32 V$
- (2) $V_{DS} = 30 \text{ V}$
- (3) $V_{DS} = 28 \text{ V}$
- (4) $V_{DS} = 26 \text{ V}$
- (5) $V_{DS} = 24 \text{ V}$

typical values



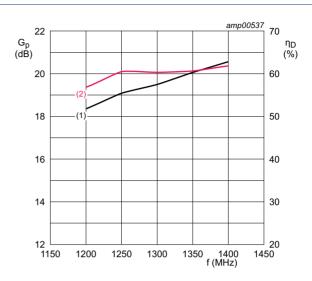
 V_{DS} = 32 V; I_{Dq} = 400 mA; t_p = 300 μ s; δ = 10 %.

(1) f = 1200 MHz

200 300 400

- (2) f = 1300 MHz
- (3) f = 1400 MHz

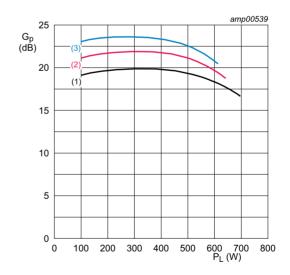
Drain efficiency as a function of output power; Fig 5. typical values



 V_{DS} = 32 V; I_{Dq} = 400 mA; t_p = 300 μ s; δ = 10 %; $P_1 = 60 \text{ W}.$

- (1) gain
- (2) efficiency





 V_{DS} = 32 V; I_{Dq} = 400 mA; t_p = 300 μ s; δ = 10 %.

- (1) f = 1200 MHz
- (2) f = 1300 MHz
- (3) f = 1400 MHz

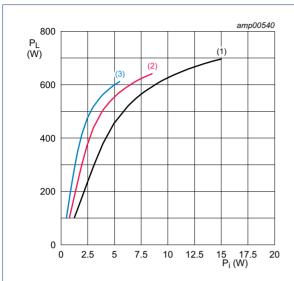
Fig 6. Power gain as a function of output power; typical values

BLL9G1214L-600_LS-600

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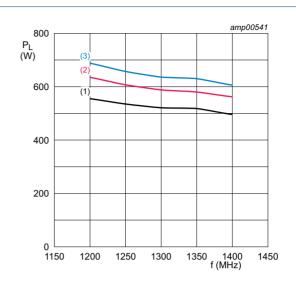
10



 V_{DS} = 32 V; I_{Dq} = 400 mA; t_p = 300 $\mu s;$ δ = 10 %.

- (1) f = 1200 MHz
- (2) f = 1300 MHz
- (3) f = 1400 MHz

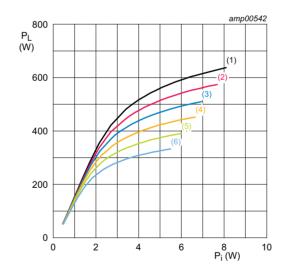
Fig 7. Output power as a function of input power; typical values



 V_{DS} = 32 V; I_{Dq} = 400 mA; t_p = 300 μ s; δ = 10 %.

- (1) P_{L(1dB)}
- (2) P_{L(2dB)}
- (3) P_{L(3dB)}

Fig 8. Output power as a function of frequency; typical values

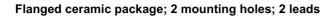


 I_{Dq} = 400 mA; t_p = 300 μ s; δ = 10 %; f = 1300 MHz.

- (1) $V_{DS} = 32 \text{ V}$
- (2) $V_{DS} = 30 \text{ V}$
- (3) $V_{DS} = 28 \text{ V}$
- (4) $V_{DS} = 26 \text{ V}$
- (5) $V_{DS} = 24 \text{ V}$
- (6) $V_{DS} = 22 V$

Fig 9. Output power as a function of input power; typical values

8. Package outline



SOT502A

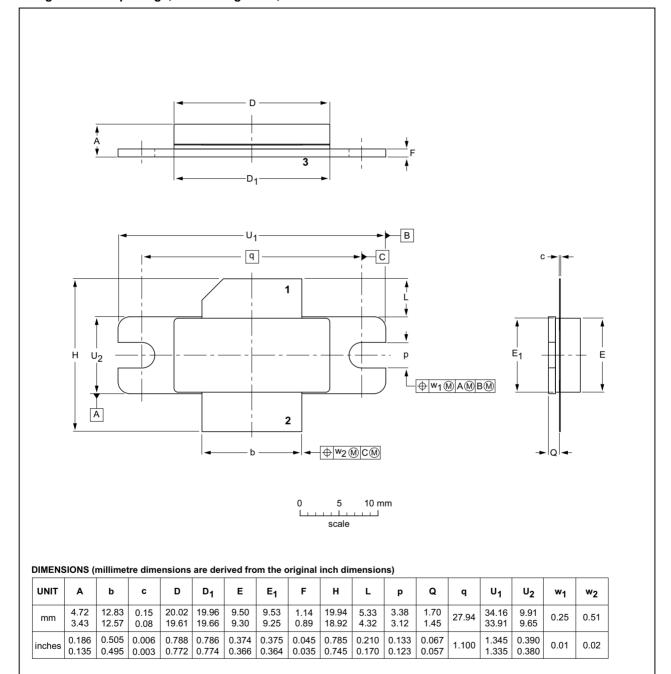


Fig 10. Package outline SOT502A

IEC

OUTLINE

VERSION

SOT502A

JEITA

REFERENCES

JEDEC

ISSUE DATE

03-01-10

12-05-02

EUROPEAN

PROJECTION

Earless flanged ceramic package; 2 leads

SOT502B

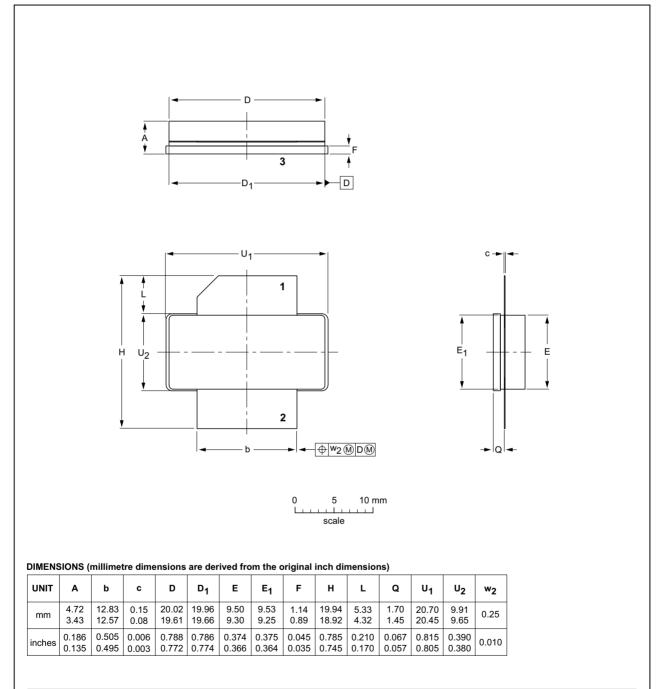


Fig 11. Package outline SOT502B

IEC

OUTLINE

VERSION

SOT502B

JEITA

REFERENCES

JEDEC

ISSUE DATE

07-05-09

12-05-02

EUROPEAN

PROJECTION

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

Table 10. ESD sensitivity

ESD model	Class
Charged Device Model (CDM); According to ANSI/ESDA/JEDEC standard JS-002	C2A [1]
Human Body Model (HBM); According to ANSI/ESDA/JEDEC standard JS-001	2 [2]

- [1] CDM classification C2A is granted to any part that passes after exposure to an ESD pulse of 500 V, but fails after exposure to an ESD pulse of 750 V.
- [2] HBM classification 2 is granted to any part that passes after exposure to an ESD pulse of 2000 V, but fails after exposure to an ESD pulse of 4000 V.

10. Abbreviations

Table 11. Abbreviations

Acronym	Description	
ESD	ElectroStatic Discharge	
L-band	ong wave Band	
LDMOS	aterally Diffused Metal-Oxide Semiconductor	
MTF	Median Time to Failure	
SMD	Surface Mounted Device	
VSWR	Voltage Standing-Wave Ratio	

11. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLL9G1214L-600_LS-600 v.1	20171127	Product data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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AMPLEON

BLL9G1214L(S)-600

LDMOS L-band radar power transistor

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