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BLP8G21S-160PV

Power LDMOS transistor

Rev. 5 — 28 September 2015

1. Product profile

1.1 General description

160 W LDMOS transistor for base station applications at frequencies from 1880 MHz to 2025 MHz.

Table 1. Typical performance

Typical RF performance per section at T_{case} = 25 °C in a common source class-AB production test circuit.

Test signal	f	I _{Dq}	V _{DS}	P _{L(AV)}	G _p	η_D	ACPR
	(MHz)	(mA)	(V)	(W)	(dB)	(%)	(dBc)
2-carrier W-CDMA	1880 to 1920	600	28	20	17.5	31	-30 <mark>[1]</mark>

 Test signal: 3GPP test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF; carrier spacing = 5 MHz.

1.2 Features and benefits

- Designed for broadband operation (1880 MHz to 2025 MHz)
- Decoupling leads to enable improved video bandwidth
- Excellent ruggedness
- High efficiency
- Excellent thermal stability
- Internally matched for ease of use
- High power gain
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

1.3 Applications

 RF power amplifiers for base station and multi-carrier applications in the 1880 MHz to 2025 MHz frequency range

2. Pinning information

Pin	Description	Simplified outline	Graphic symbol
1, 2	gate		
3, 6	decoupling lead		4
4, 5	drain	······································	
7	source		2 - 7 1 7 1 6 5 aaa-008888

[1] Connected to flange.

3. Ordering information

Table 3. Orderin	Table 3. Ordering information				
Type number Package					
	Name	Description	Version		
BLP8G21S-160PV	HSOP6F	plastic, heatsink small outline package; 6 leads (flat)	SOT1221-2		

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	drain-source voltage		-	65	V
V_{GS}	gate-source voltage		-0.5	+13	V
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature	[1]	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the on-line MTF calculator.

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Тур	Unit
R _{th(j-case)}	thermal resistance from junction to case	T _{case} = 80 °C; P _L = 80 W	0.356	K/W

6. Characteristics

Table 6.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	drain-source breakdown voltage	V _{GS} = 0 V; I _D = 1.14 mA	65	-	-	V
V _{GS(th)}	gate-source threshold voltage	V _{DS} = 10 V; I _D = 114 mA	1.5	1.9	2.3	V
V _{GSq}	gate-source quiescent voltage	V _{DS} = 28 V; I _D = 684 mA	1.7	2.1	2.5	V
I _{DSS}	drain leakage current	V _{GS} = 0 V; V _{DS} = 28 V	-	-	1.4	μA
I _{DSX}	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75 \text{ V};$ $V_{DS} = 10 \text{ V}$	-	20.4	-	A
I _{GSS}	gate leakage current	V _{GS} = 11 V; V _{DS} = 0 V	-	-	140	nA
9 _{fs}	forward transconductance	V _{DS} = 10 V; I _D = 114 mA	-	1.0	-	S
R _{DS(on)}	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75 V;$ I _D = 3.99 A	-	0.1	-	Ω

Table 7. RF characteristics

DC characteristics

Test signal: 2-carrier W-CDMA; PAR = 8.4 dB at 0.01 % probability on CCDF; carrier spacing = 5 MHz; 3GPP test model 1; 64 DPCH; f_1 = 1882.5 MHz; f_2 = 1887.5 MHz; f_3 = 1912.5 MHz; f_4 = 1917.5 MHz; RF performance per section at V_{DS} = 28 V; I_{Dq} = 600 mA; T_{case} = 25 °C; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _p	power gain	P _{L(AV)} = 20 W	16.3	17.5	-	dB
η_D	drain efficiency	P _{L(AV)} = 20 W	26	31	-	%
RL _{in}	input return loss	P _{L(AV)} = 20 W	-	-10	-6	dB
ACPR	adjacent channel power ratio	P _{L(AV)} = 20 W	-	-30	-25	dBc

7. Application information

7.1 Ruggedness in class-AB operation

The BLP8G21S-160PV is capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions: per section; V_{DS} = 28 V; I_{Dg} = 600 mA; P_L = 80 W (CW); f = 1880 MHz.

7.2 Impedance information

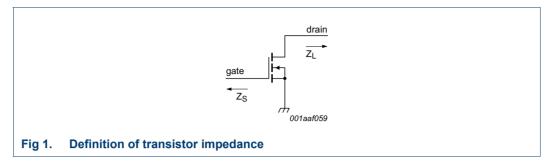
Table 8. Typical impedance

Measured per section load-pull data; I_{Dq} = 600 mA; V_{DS} = 28 V. Typical values unless otherwise specified.

f	Z _S [1]	Z _L [1]
(MHz)	(Ω)	(Ω)
1880	2.353 - j8.430	2.508 – j8.375
1920	3.032 - j9.435	2.407 – j8.091
2025	6.435 – j13.55	2.148 – j7.389

[1] Z_S and Z_L defined in Figure 1.

BLP8G21S-160PV



7.3 Test circuit

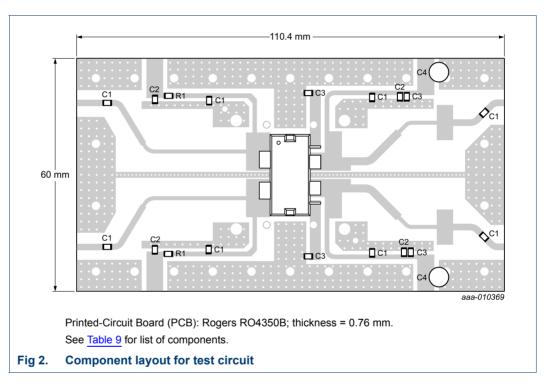


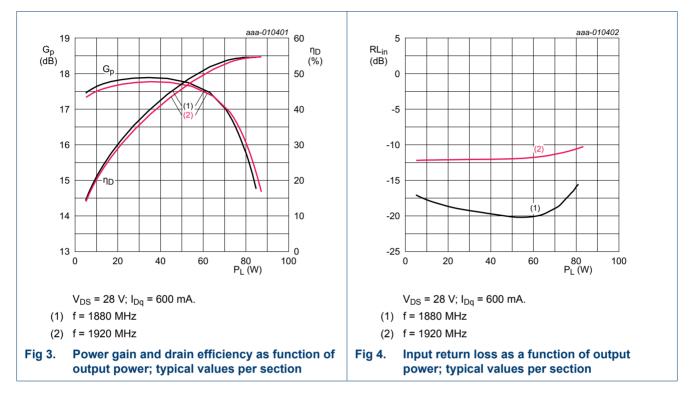
Table 9. List of components

For test circuit, see Figure 2.

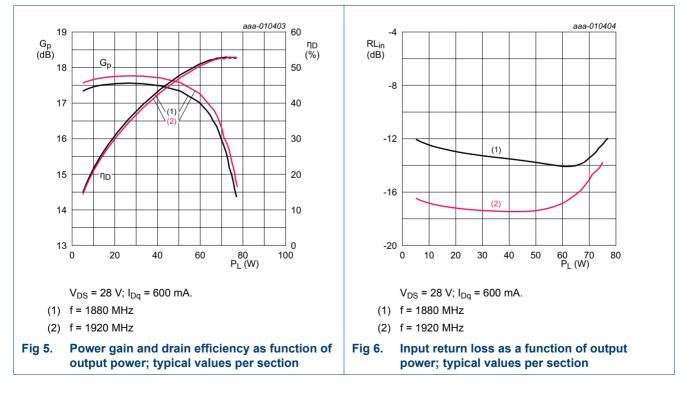
Component	Description	Value	Remarks
C1	multilayer ceramic chip capacitor	30 pF	ATC800B
C2	multilayer ceramic chip capacitor	2.2 μF	Murata
C3	multilayer ceramic chip capacitor	10 μF	Murata
C4	electrolytic capacitor	1000 μF, 63 V	
R1	chip resistor	5.1 Ω	Vishay Dale SMD 0805

7.4 Graphical data

7.4.1 Pulsed CW



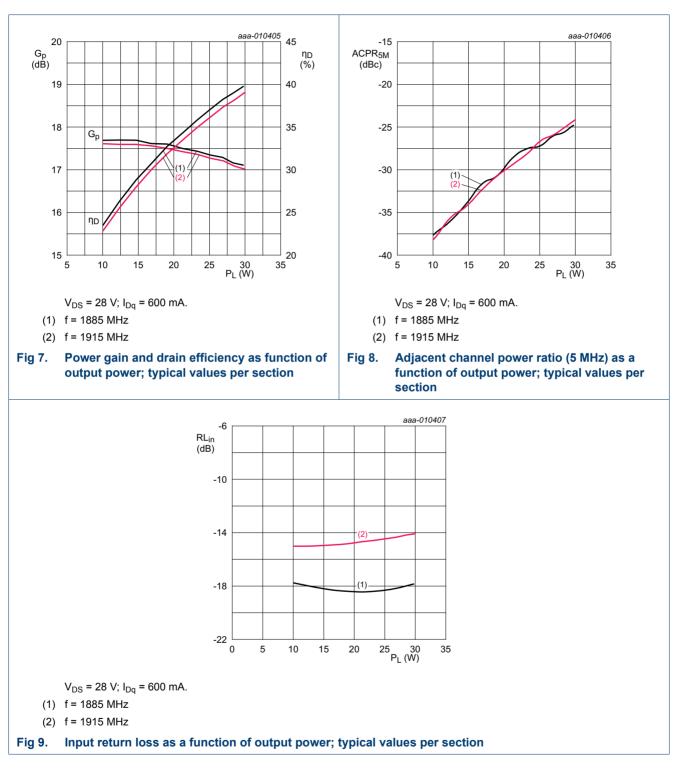




Product data sheet

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Power LDMOS transistor



7.4.3 2-Carrier W-CDMA

BLP8G21S-160PV

BLP8G21S-160PV

Power LDMOS transistor

8. Package outline

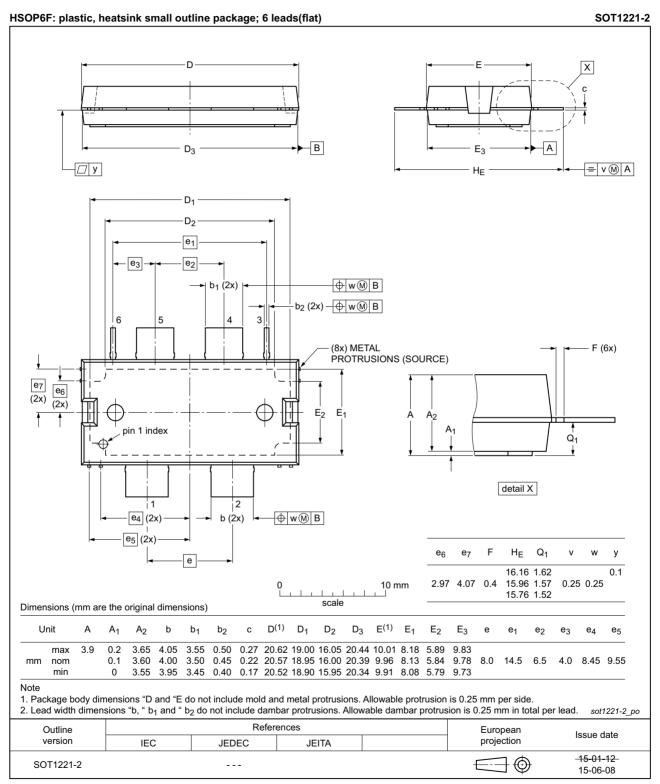


Fig 10. Package outline SOT1221-2 (HSOP6F)

9. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

10. Abbreviations

Table 10. Abbre	Table 10. Abbreviations			
Acronym	Description			
3GPP	3rd Generation Partnership Project			
CCDF	Complementary Cumulative Distribution Function			
CW	Continuous Wave			
DPCH	Dedicated Physical CHannel			
ESD	ElectroStatic Discharge			
LDMOS	Laterally Diffused Metal Oxide Semiconductor			
MTF	Median Time to Failure			
PAR	Peak-to-Average Ratio			
SMD	Surface Mounted Device			
VSWR	Voltage Standing Wave Ratio			
W-CDMA	Wideband Code Division Multiple Access			

11. Revision history

Table 11.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLP8G21S-160PV v.5	20150928	Product data sheet	-	BLP8G21S-160PV#4
Modifications	Table 3 on	page 2: package outline version	on changed from SOT1	221-1 to SOT1221-2
	Figure 10 c	n page 7: package outline ver	sion changed from SO	T1221-1 to SOT1221-2
BLP8G21S-160PV#4	20150901	Product data sheet	-	BLP8G21S-160PV v.3
Modifications:	The format of this document has been redesigned to comply with the new identity guidelines of Ampleon			
	 Legal texts 	have been adapted to the new	w company name wher	e appropriate
BLP8G21S-160PV v.3	20140701	Product data sheet	-	BLP8G21S-160PV v.2
BLP8G21S-160PV v.2	20131219	Objective data sheet	-	BLP8G21S-160PV v.1
BLP8G21S-160PV v.1	20130808	Objective data sheet	-	-

12. Legal information

12.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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