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PRELIMINARY



Features

- Low voltage operation
- Low current consumption
- Miniature SMD package size
- I²C communication protocol
- Established reliability
- Capacitive technology
- RoHS compliant*

Applications

Industrial:

- HVAC systems
- Process monitoring
- Packaging automation
- **Medical:
- Diagnostic equipment
- Analysis equipment

BPS230 Series - 2 mm Humidity Sensor

| Absolute Maximum Ratings | |
|--|--|
| Supply Voltage (V _{cc}) | -0.3 to 7.0 V |
| Input Voltage (V _I) | |
| CE | |
| SCL/SDA | |
| Output Voltage (VO) | 0.3 to V _{cc} + 0.3 V |
| fi-level Output Current (IOH) | |
| 1 Terminal | |
| All Terminals Total | 20 mA |
| ow-level Output Current (IOL) | |
| 1 Terminal | |
| All Terminals Total | 20 mA |
| Operating Temperature (T _a) | 40°C to +105°C (-40°F to +221°F) |
| torage Temperature (T _{stg}) | 50°C to +125°C (-58°F to +257°F) |
| Recommended Operating Conditions | |
| Power Supply Voltage (V _{CC}) | 1.62 to 5.5 VDC |
| Capacitance between V _{CC} and V _{SS} (C _p) | 0.1 μF typica |
| Pull Up Resistor Value on SDA ¹ (R1) | |
| full Up Resistor Value on SCL1 (R2) | 5 kΩ typica |
| Select the resistance value to meet AC characteristics. | |
| Electrical Characteristics | |
| Humidity Detection | |
| Measurement Range | 0 to 100 % R⊦ |
| Resolution (10-bit) | 0.1 % RH typica |
| Humidity Accuracy | |
| @ 25 °C (20 to 80 % RH) | |
| @ 5 °C to 45 °C (0 to 100 % RH) | |
| Hysteresis @ 5 °C to 45 °C (0 to 100 % RH) | ±1 % RH typica |
| Response Time | |
| Reach (τ 63 % @ 25 °C, wind velocity @ 1.0 m/s) | 8 seconds |
| Unless otherwise specified: V_{CC} = 1.62 to 5.5 V, V_{SS} = 0 V, T_a = -20 °C to 100 °C | |
| emperature Detection | |
| Measurement Range | 30 °C to +100 °C (-22 °F to +212 °F) |
| Resolution (11 bit) | |
| -10 °C to +70 °C | |
| All other temperatures | 0.4 °C (32.72 °F) |
| Temperature Accuracy | |
| @ 5 °C to 60 °C | |
| @ -20 °C to 85 °C | , |
| Reproducibility @ -30 °C to 100 °C | ± 0.1 °C (±32.18 °F) |
| Response Time Reach (τ 63 % (dependent on surrounding heat conduction) | 30 seconds |
| Inless otherwise specified: V_{CC} = 1.62 to 5.5 V, V_{SS} = 0 V, T_{A} = -30 °C to 100 °C | |
| | |
| Current Consumption | 10 mA tomical 400 mA marriage |
| Sleep Current (CE=0, Sleep Mode) | |
| Average Operating Current | 13 μ A typical, 35 μ A maximum |

Specifications are subject to change without notice.

Unless otherwise specified: V_{CC} = 1.62 to 5.5 V, V_{SS} = 0 V, T_a = 0 °C to 60 °C

^{*} RoHS Directive 2015/863, Mar 31, 2015 and Annex.

** Bourns® products have not been specifically designed and tested for FDA Class III applications and equivalent applications covered by other regulatory authority such as the European Counsel, and their use in such applications is neither recommended nor supported.

Users should verify actual device performance in their specific applications.

BOURNS

Input/Output Terminal Characteristics

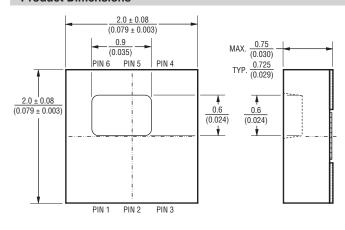
| High Level Input Voltage 1 (VIH1) [Target Terminal: SCL, SDA] | |
|---|--|
| High Level Input Voltage 2 (VIH2) [Target Terminal: CE] | |
| Low Level Input Voltage 1 (VIL1) [Target Terminal: SCL, SDA] | V _{SS} minimum, 0.3 V _{CC} maximum |
| Low Level Input Voltage 2 (VIL2) [Target Terminal: CE] | V _{SS} minimum, 0.2 V _{CC} maximum |
| Low Level Output Current (IOL) [VOL = 0.1 V _{CC} , Target Terminal: SCL, SDA] | |
| Terminal Leak Current 1 (IL1) [Terminal voltage = V _{CC} , Target Terminal: SCL, SDA] | ± 1 μA |
| Terminal Leak Current 2 (IL2) [Terminal voltage = 0 V, Target Terminal: SCL, SDA, CE] | ± 1 μA |
| Input Pull-Down Resistance (RPD) [Terminal voltage = V_{CC} , Target Terminal: CE]60 k Ω | minimum, 150 kΩ typical, 450 kΩ maximum |

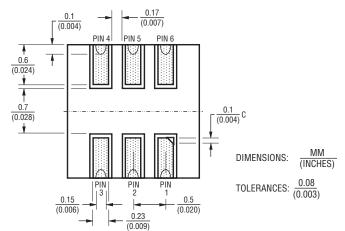
Unless otherwise specified: V_{CC} = 1.62 to 5.5 V, V_{SS} = 0 V, T_a = -30 °C to 100 °C

Product Characteristics

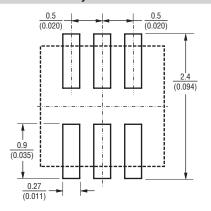
| Moisture Sensitivity Level | 1 |
|----------------------------|------|
| ESD Classification (HBM) | 1 kV |
| Marking | |
| Standard Packaging | |
| Weight | |

Product Dimensions





Recommended PCB Layout



Terminal Assignment

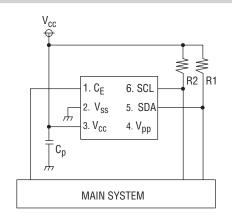
| No. | Terminal Name | Function |
|-----|------------------|-------------------------------|
| 1 | CE | Chip enable terminal |
| 2 | V_{ss} | Power supply terminal (-) |
| 3 | V_{cc} | Power supply terminal (+) |
| 4 | NC | No connection |
| 5 | SDA | I ² C serial data |
| 6 | SCL | I ² C serial clock |

Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

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Basic Circuit Schematic



 $C_p \dots 0.1 \mu F$ 5k Ω R2 $5k \Omega$

NOTE: R1 and R2 are reference values. Resistor values should be selected to meet the AC characteristics.

Operation Mode

| Operation | Terminal Setup | | Operation State of Each Functional Block | | | | | | |
|-----------|-------------------|-----------------|--|-------------|--------------------|--------------------------|----------------------|----------------------|--|
| Mode CE | CE | V _{pp} | Power Supply | Oscillation | Temp. Detection | Capacitance Detection | OTP Memory | I ² C-Bus | |
| Sleep *1 | 0 | NC | Stop | Stop | Stop | Stop | Stop | Stop | |
| Standby | 1 | NC | Operation | Operation | Stop | Stop | Read-out Possible | Operation | |

^{*1} In case of power control mode, there is no sleep operation. I²C slave address (SADR) is defined as "111 1111" (7Fh).

Control Register Map

| Address | Bit | Bit Name | Function | Value | ReadOut | Write-In | R/W | Init. |
|---------|-----------------------------------|-------------|-----------------------------|------------------------------|----------------------|--------------|-----|-------|
| | D7-1 | - | Reserved | - | | | R | 0 |
| 00h | D0 RE | RESET | Reset | 0 | Normal Operation | None | R/W | 0 |
| | | | | 1 | - | Reset Action | | |
| | D7-6 | MANMODE | Manual Detection Mode | ion 00 Normal Operation Mode | eration Mode | R/W | 0 | |
| | D5-3 HAVE[2:0] | LIAV/5[0.0] | value Avg. | 000 | No Averaging Process | | | |
| | | | | 001 | 2 Times Average Mode | | | |
| 01h | | HAVE[2.0] | | 01x | 4 Times Average Mode | | | |
| | | Mode | 1xx | 8 Times Average Mode | | | | |
| | | Te | Temperature | 0 | 8 Times Av | erage Mode | | |
| | D2 TAVE Detection Value Avg. Mode | 1 | 16 Times Av | verage Mode | R/W | 0 | | |

Control Register Map (Continued)

| Address | Bit | Bit Name | Function | Value | ReadOut | Write-In | R/W | Init. |
|---------|------|----------|---|-------|---------------------------------|---------------------------------|--------|-------|
| | D1 | - | Reserved | - | | | R | 0 |
| 01h | DO | MAN | Manual Detection | al | Standby State | Detection Operation Stop | R/W | 0 |
| | D0 | IVIAIN | Mode | 1 | Under Detection Operation | Detection Operation Start | 17/ VV | |
| | D7-1 | - | Reserved | - | | | R | 0 |
| 03h | Do | FDD | Manual | 0 | No Error | Nothing is Done | DAM | |
| | D0 | ERR | Detection Error Flag | 1 | Error Occurred | Error Flag Reset | R/W | 0 |
| 04h | D7-0 | HC[7:0] | Humidity Detection Result (After Correction Operation) | | 000h-3FFh | | R | Х |
| | D7-2 | - | Reserved | - | | | R | 0 |
| 05h | D1-0 | HC[9:8] | Humidity Detection Result (After Correction Operation) | | | | R | Х |
| 06h | D7-0 | TC[7:0] | Temperature Detection Result (After Correction Operation) | | 000h-7FFh | | R | Х |
| | D7-3 | - | Reserved | - | | | R | 0 |
| 07h | D2-0 | TC[10:8] | Temperature Detection Result (After Correction Operation) | | | | R | Х |
| 0Ah | D7-0 | K[7:0] | Capacity Detection Result (Before Correction Operation) | | 000h-FFFFh | | R | 0 |

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Control Register Map (Continued)

| Address | Bit | Bit Name | Function | Value | ReadOut | Write-In | R/W | Init. |
|---------|------------------|-------------------------|---|-------------------|---|--------------------|------|-------|
| 0Bh | D7-0 | K[15:8] | Capacity Detection Result (Before Correction Operation) | | | R | 0 | |
| | D7-5 | - | Reserved | - | - | - | R | 0 |
| | | | Standard | 0 | 0 Outside Capacity Cutting | | | |
| 2Ch | D4 SCR_ON_ | SCR_ON_R | Capacity Connection Control | 1 | Outside Capacity Connection | | R/W | 0 |
| | D3-0 | SCI_ ON_R[3:0] | Internal Capacity Connection Control | | 0h~Fh x 0.6 pF Example: At the time of 8 hours, access to internal capacity of 4.8 pF | | R/W | Х |
| | D7-1 | - | Reserved | - | | | R | 0 |
| 03h | D0 ERR Detection | FDD | Manual | 0 | No Error | Nothing is Done | D.44 | |
| | | Detection Error Flag | 1 | Error Occurred | Error Flag Reset | R/W | 0 | |

Conversion of Signal Output

Humidity Arithmetic Expression

RH =
$$\frac{100}{2^{10}}$$
 x RH_{IC} (0 ~ 100 % RH)

RH_{IC} : IC Humidity Output Data (10 bit)

Refer to Register Map:

 RH_{IC} = Data of the addresses 04H and 05H (000h ~ 3FFh) It changes into a decimal and is operation.

Temperature Arithmetic Expression

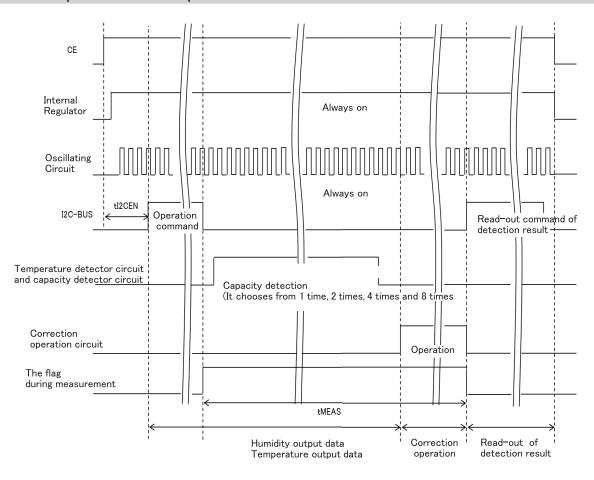
T =
$$[T_{IC} - (2^{10} - \frac{25}{0.1})] \times 0.1$$
 (-30 ~ 100 °C)

T_{IC}: IC Temperature Output Data (11 bit)

Refer to Register Map:

 T_{IC} = Data of the addresses 06H and 07H (000h ~ 7FFh) It changes into a decimal and is operation.

Capacitance/Temperature Detection Sequence



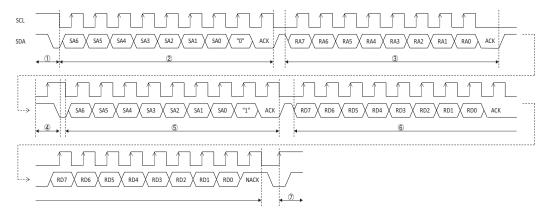
How To Order BPS230 - D 3P0 - S 10 E Model Series — Humidity-Temperature Sensor Output Type — D = Digital Accuracy (% RH) — 3P0 = ±3.0 Condensation Protection — Blank = No Seal S = Condensation Seal Resolution — 10 = 10-bit Packaging Designator

E = 3000 pcs. per 7-inch Reel

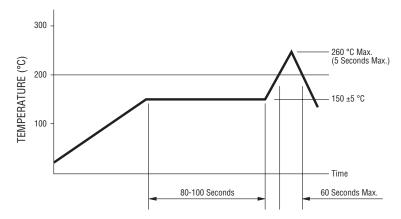
Output Type Waveform and Data Read/Write Procedure

I2C-BUS Data Read-out Procedure

- (1) I²C master device releases START condition.
- (2) I²C master device transmits slave address and WRITE mode selection.
- (3) I²C master device transmits register address of this IC.
- (4) I²C master device releases repeated START condition. (Release method is same as START condition.)
- (5) I²C master device again transmits slave address and READ mode selection. (Read mode can be selected by transmitting "1" in 8th bit.)
- 6 I²C master device reads-out data from register address designated at ③.
 It is possible to read-out data while register address increments one, by reading-out multiple data continuously. However, during continuous read-out, please return ACK to this IC as a reply of master.
- 7) After the completion of all read-out, I²C master device releases STOP condition.



Solder Profile

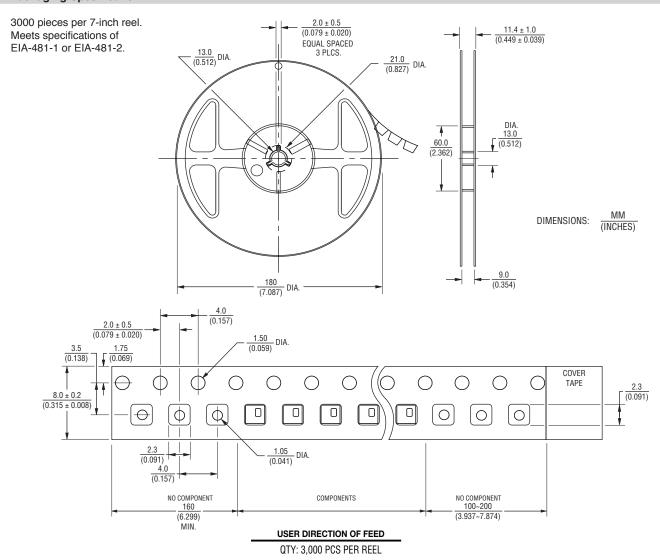


Processing Method: Reflow soldering with infrared heat or forced air convection (only once).

Notes:

- 1. No clean solder paste is recommended.
- 2. Aqueous wash is not recommended.
- Use of water soluble soldering flux should be avoided due to possible corrosion.
- 4. Multiple passes through the soldering process is not recommended.

Packaging Specification



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