# imall

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#### Application

- Motor drive
- · Inverter, Converter
- · Photovoltaics, wind power generation.
- · Induction heating equipment.

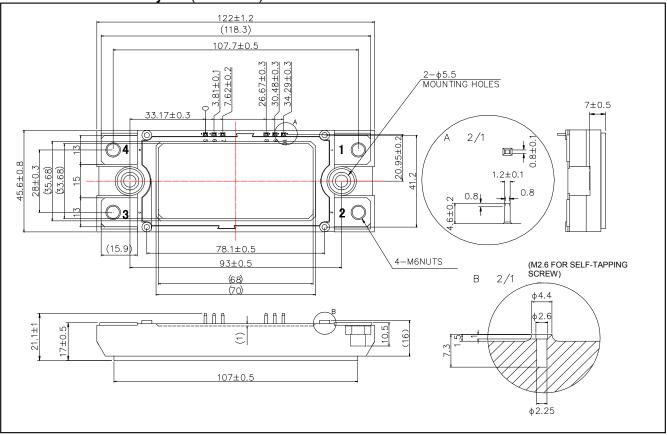
#### Features

- 1) Low surge, low switching loss.
- 2) High-speed switching possible.
- 3) Reduced temperature dependence.

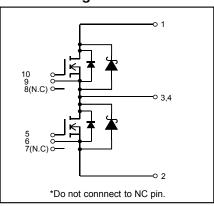
#### Construction

This product is a half bridge module consisting of SiC-DMOS and SiC SBD from ROHM.

#### •Dimensions & Pin layout (Unit : mm)



#### Circuit diagram



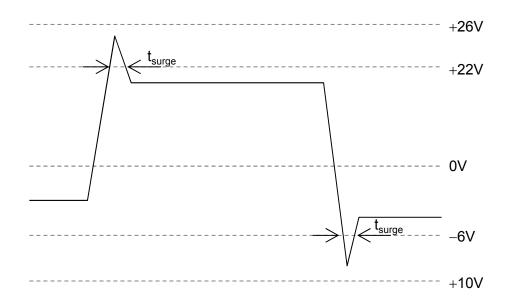
	<b>5</b> (1) - 2:	5.0)			
Parameter	Symbol	Conditions	Limit	Unit	
Drain-source voltage	V <sub>DSS</sub>	G-S short	1200		
Gate-source voltage(+)	V <sub>GSS</sub>	D-S short	22	V	
Gate-source voltage(-)			-6		
G - S voltage (t <sub>surge</sub> <300nsec)	V <sub>GSSsurge</sub>	D-S short	-10 to 26		
Drain current * <sup>1</sup>	I <sub>D</sub>	DC(T <sub>c</sub> =60°C)	80		
Drain current "	I <sub>DRM</sub>	Pulse (T <sub>c</sub> =60°C) 1ms * <sup>2</sup>	160	Α	
Source current *1	I <sub>s</sub>	$DC(T_{c} = 60^{\circ}C) V_{GS} = 18V$	80		
Source current	I <sub>SRM</sub>	Pulse ( $T_c$ =60°C) 1ms V <sub>GS</sub> =18V	160		
Total power disspation * <sup>3</sup>	Ptot	T <sub>c</sub> =25°C	600	W	
Max junction temperature	T <sub>jmax</sub>		175		
Junction temperature	T <sub>jop</sub>		-40 to150	°C	
Storage temperature	T <sub>stg</sub>		-40 to125		
Isolation voltage	Visol	Terminals to baseplate, f=60Hz AC 1min.	2500	Vrms	
Mounting torque		Main Terminals : M6 screw	4.5	N · m	
Mounting torque	_	Mounting to heat shink : M5 screw	3.5		

#### • Absolute maximum ratings (Tj = 25°C)

(\*1) Case temperature  $(T_c)$  is defined on the surface of base plate just under the chips.

(\*2) Repetition rate should be kept within the range where temperature rise if die should not exceed  $T_{jmax}$ . (\*3)  $T_j$  is less than 175°C

#### Example of acceptable VGS waveform



•Electrical characteristics (Tj=25°C)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
On state static			T <sub>i</sub> =25°C	_	2.8	3.5	V
On-state static	V <sub>DS(on)</sub>	I <sub>D</sub> =80A, V <sub>GS</sub> =18V	T <sub>i</sub> =125°C	_	4.2	_	
Drain-Source Voltage			T <sub>j</sub> =150°C	-	4.8	5.5	
Drain cutoff current	I <sub>DSS</sub>	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V		_	_	1.2	mA
		V <sub>GS</sub> =0V, I <sub>S</sub> =80A	T <sub>j</sub> =25°C	_	1.7	2.0	
			T <sub>j</sub> =125°C	_	2.1	_	
Source drain voltage	V <sub>SD</sub>		T <sub>j</sub> =150°C	1	2.3	3.3	V
Source-drain voltage	▼ SD		T <sub>j</sub> =25°C	Ι	1.4	_	
		V <sub>GS</sub> =18V, I <sub>S</sub> =80A	T <sub>j</sub> =125°C	1	1.7	_	
			T <sub>j</sub> =150°C	1	1.8	_	
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =13.2mA		1.6	-	4	V
Gate-source leak current	I <sub>GSS</sub>	V <sub>GS</sub> =22V, V <sub>DS</sub> =0V		1	1	0.5	μΑ
		V <sub>GS</sub> = -6V, V <sub>DS</sub> =0V		-0.5		_	
	t <sub>d(on)</sub>	V <sub>GS(on)</sub> =18V, V <sub>GS(off)</sub> =0V V <sub>DS</sub> =600V I <sub>D</sub> =80A		Ι	20	_	ns
	t <sub>r</sub>			Ι	30	_	
Switching characteristics	t <sub>rr</sub>			1	35	_	
	t <sub>d(off)</sub>	R <sub>G</sub> =0.82Ω		-	80	_	
	t <sub>f</sub>	inductive load		_	40	_	
Input capacitance	Ciss	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz		1	8	_	nF
Gate Registance	R <sub>Gint</sub>	T <sub>j</sub> =25°C			3.0	_	Ω
Stray Inductance	Ls				25	_	nH
Creanage distance	_	Terminal to heat sink			11.5	_	mm
Creepage distance		Terminal to terminal			19.0	_	mm
Clearance distance	_	Terminal to heat sink			9.5	_	mm
		Terminal to terminal			13.0	_	mm
Junction-to-case thermal	R <sub>th</sub> (j-c)	DMOSFET (1/2 module) * <sup>5</sup>		_	_	0.25	°C/W
resistance	(\thu-c)	SBD (1/2 module) *5		_	_	0.32	
Case-to-heat sink Thermal resistance	R <sub>th</sub> (c-f)	Case to heat sink, per 1 module, Thermal grease appied * <sup>6</sup>		_	0.035	_	

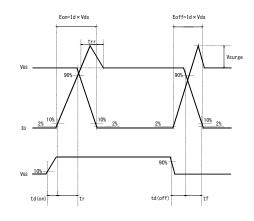
(\*4) In order to prevent self turn-on, it is recommended to apply negative gate bias.

(\*5) Measurement of Tc is to be done at the point just under the chip.

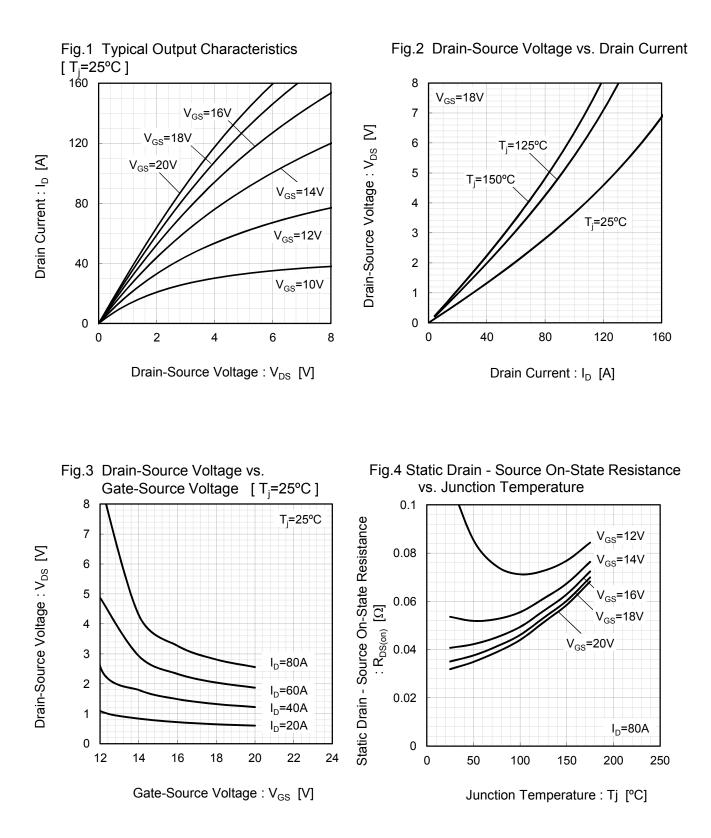
(\*6) Typical value is measured by using thermally conductive grease of  $\lambda$ =0.9W/(m · K).

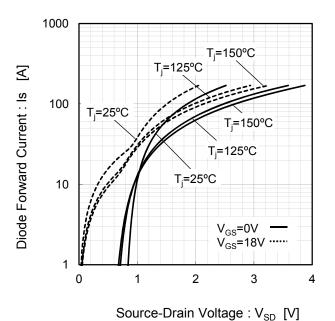
<Wavelength for Switching Test>

- (\*7) SiC devices have lower short cuicuit withstand capability due to high current density. Please be advised to pay careful attention to short cuicuit accident and try to adjust protection time to shutdown them as short as possible.
- (\*8) If the Product is used beyond absolute maximum ratings defined in the Specifications, as its internal structure may be dameged, please replace such Product with a new one.



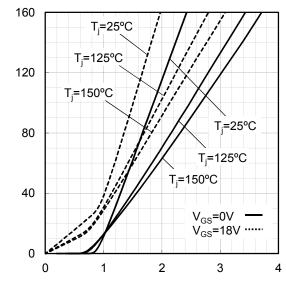
ROHM





#### Fig.5 Forward characteristic of Diode

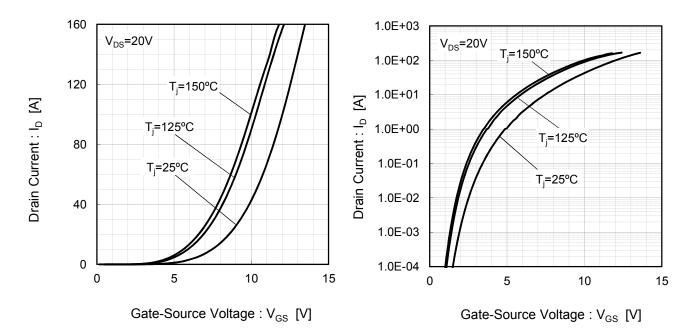
Fig.6 Forward characteristic of Diode



Source-Drain Voltage :  $V_{SD}$  [V]

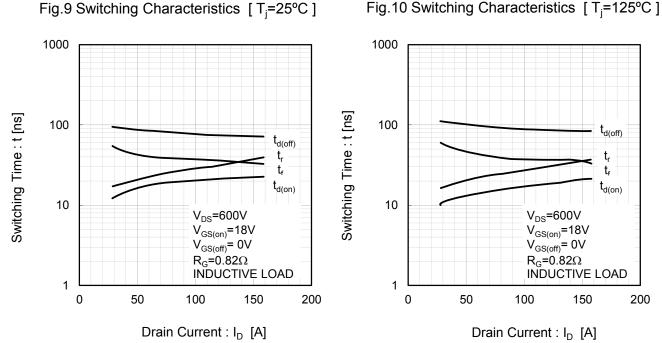
#### Fig.7 Drain Current vs. Gate-Source Voltage

Fig.8 Drain Current vs. Gate-Source Voltage



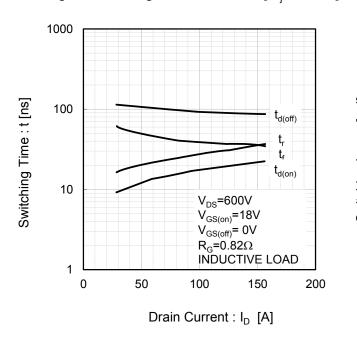
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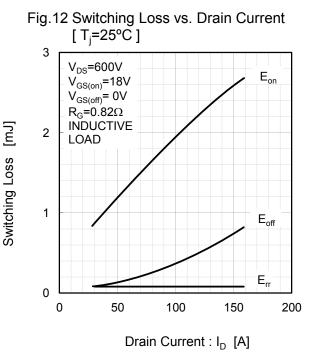
Diode Forward Current : Is



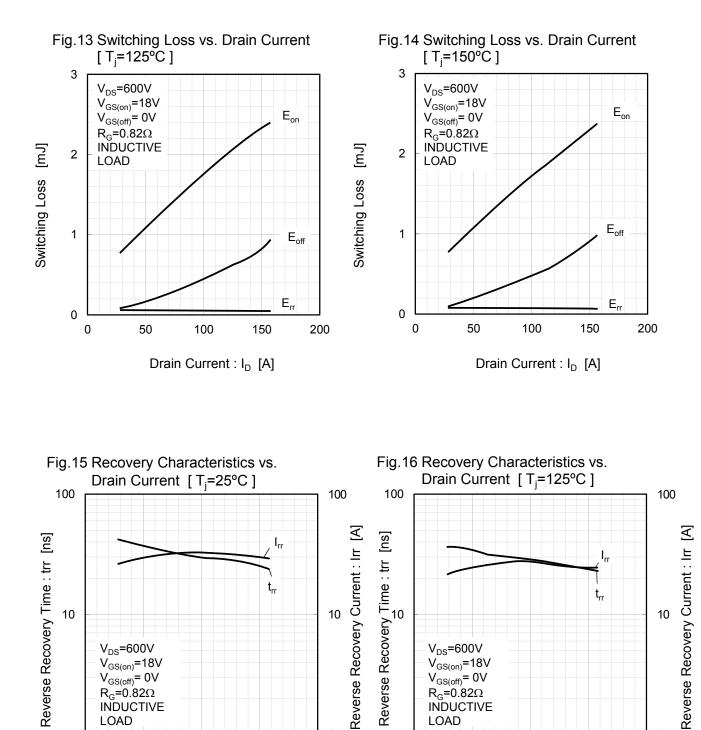
#### Fig.10 Switching Characteristics [T<sub>i</sub>=125°C]

## Fig.11 Switching Characteristics [T<sub>i</sub>=150°C]





2017.10 - Rev.B 



50

100

Drain Current : I<sub>D</sub> [A]

1

0

1

0

50

100

200

150

150

Drain Current : I<sub>D</sub> [A]

200

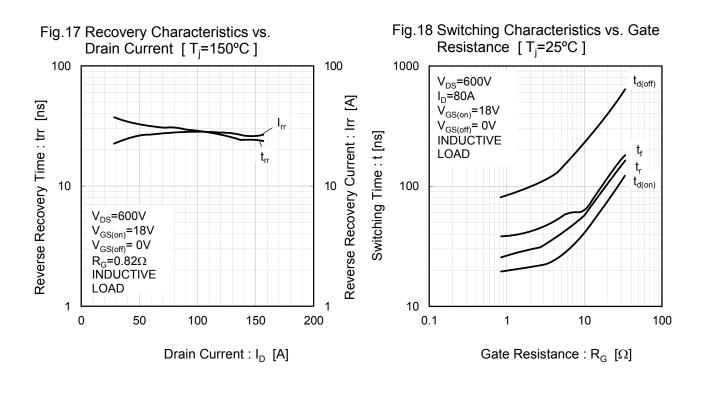


Fig. 19 Switching Characteristics vs. Gate					
-	Resistance [T <sub>i</sub> =125°C]				
1000	• •				

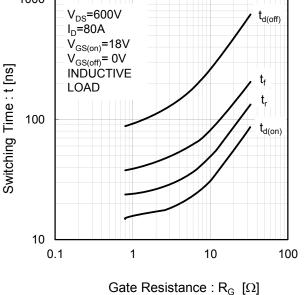
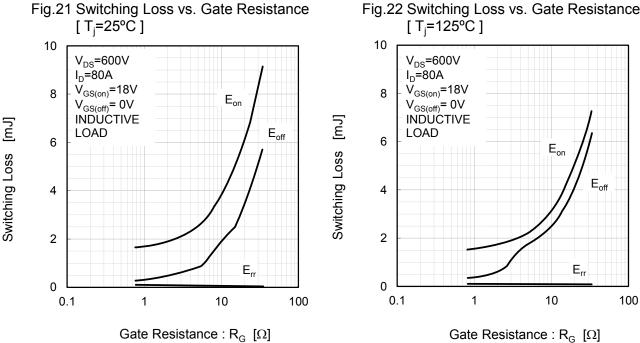
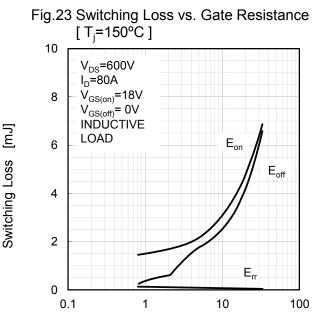


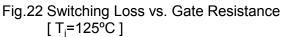
Fig.20 Switching Characteristics vs. Gate Resistance  $[T_j=150^{\circ}C]$ 

Gate Resistance :  $R_G$  [ $\Omega$ ]



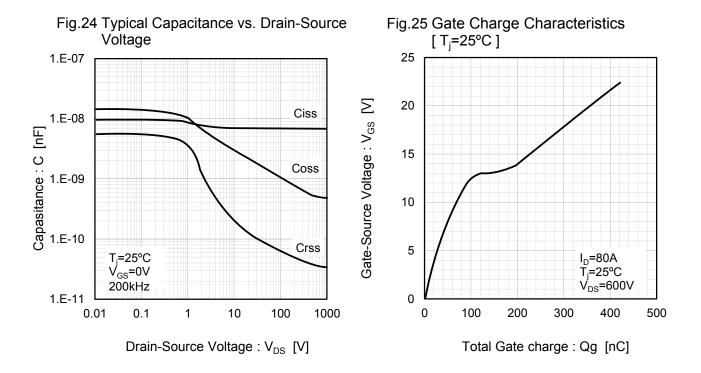


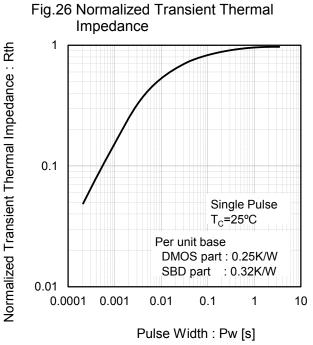
Gate Resistance :  $R_G$  [ $\Omega$ ]



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 SBD part
 : 0.32k

 0.01
 0.0001
 0.001
 0.1
 1

 Pulse Width : Pw [s]
 Pulse Width : Pw [s]
 Pulse Width : Pw [s]

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Part Number	BSM080D12P2C008
Package	С
Unit Quantity	12
Minimum Package Quantity	12
Packing Type	Tray
Constitution Materials List	inquiry
RoHS	Yes