



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



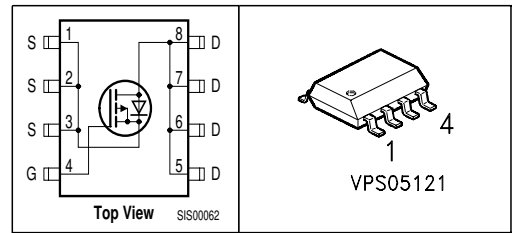
OptiMOS[®]-P Small-Signal-Transistor

Feature

- P-Channel
- Enhancement mode
- Logic Level
- 150°C operating temperature
- Avalanche rated
- dv/dt rated

Product Summary

V_{DS}	-30	V
$R_{DS(on)}$	21	mΩ
I_D	-8.9	A



Type	Package	Ordering Code
BSO303SP	SO 8	Q67042-S4129

Maximum Ratings, at $T_j = 25\text{ °C}$, unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current $T_A=25\text{ °C}$ $T_A=70\text{ °C}$	I_D	-8.9 -7.1	A
Pulsed drain current $T_A=25\text{ °C}$	$I_{D\text{ puls}}$	-35.6	
Avalanche energy, single pulse $I_D=-8.9\text{ A}$, $V_{DD}=-25\text{ V}$, $R_{GS}=25\text{ }\Omega$	E_{AS}	97	mJ
Reverse diode dv/dt $I_S=-8.9\text{ A}$, $V_{DS}=-24\text{ V}$, $di/dt=200\text{ A}/\mu\text{s}$, $T_{j\text{ max}}=150\text{ °C}$	dv/dt	-6	kV/ μs
Gate source voltage	V_{GS}	± 20	V
Power dissipation $T_A=25\text{ °C}$	P_{tot}	2.35	W
Operating and storage temperature	T_j , T_{stg}	-55... +150	°C
IEC climatic category; DIN IEC 68-1		55/150/56	

Thermal Characteristics

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Characteristics					
Thermal resistance, junction - soldering point	R_{thJS}	-	-	35	K/W
SMD version, device on PCB:	R_{thJA}	-	-	110	
@ min footprint, $t < 10s$ @ 6 cm ² cooling area ¹⁾		-	-	53	

Electrical Characteristics, at $T_j = 25\text{ }^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain-source breakdown voltage $V_{GS}=0, I_D=-250\mu A$	$V_{(BR)DSS}$	-30	-	-	V
Gate threshold voltage, $V_{GS} = V_{DS}$ $I_D=-100\mu A$	$V_{GS(th)}$	-1	-1.5	-2	
Zero gate voltage drain current $V_{DS}=-30V, V_{GS}=0, T_j=25^\circ C$ $V_{DS}=-30V, V_{GS}=0, T_j=150^\circ C$	I_{DSS}	-	-0.1 -10	-1 -100	μA
Gate-source leakage current $V_{GS}=-20V, V_{DS}=0$	I_{GSS}	-	-10	-100	
Drain-source on-state resistance $V_{GS}=-4.5V, I_D=-7.3A$	$R_{DS(on)}$	-	24	31	m Ω
Drain-source on-state resistance $V_{GS}=-10V, I_D=-8.9A$	$R_{DS(on)}$	-	15	21	

¹⁾ Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 μm thick) copper area for drain connection. PCB is vertical without blown air; $t \leq 10$ sec.

Electrical Characteristics, at $T_j = 25\text{ }^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	

Dynamic Characteristics

Transconductance	g_{fs}	$ V_{DS} \geq 2 * I_D * R_{DS(on)max}$ $I_D = -7.1\text{A}$	12	24	-	S
Input capacitance	C_{iss}	$V_{GS} = 0, V_{DS} = -25\text{V},$ $f = 1\text{MHz}$	-	1754	-	pF
Output capacitance	C_{oss}		-	465	-	
Reverse transfer capacitance	C_{rss}		-	389	-	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -15\text{V}, V_{GS} = -10\text{V},$ $I_D = -1\text{A}, R_G = 6\Omega$	-	10.3	15.5	ns
Rise time	t_r		-	12.5	19	
Turn-off delay time	$t_{d(off)}$		-	53	80	
Fall time	t_f		-	40.3	60.5	

Gate Charge Characteristics

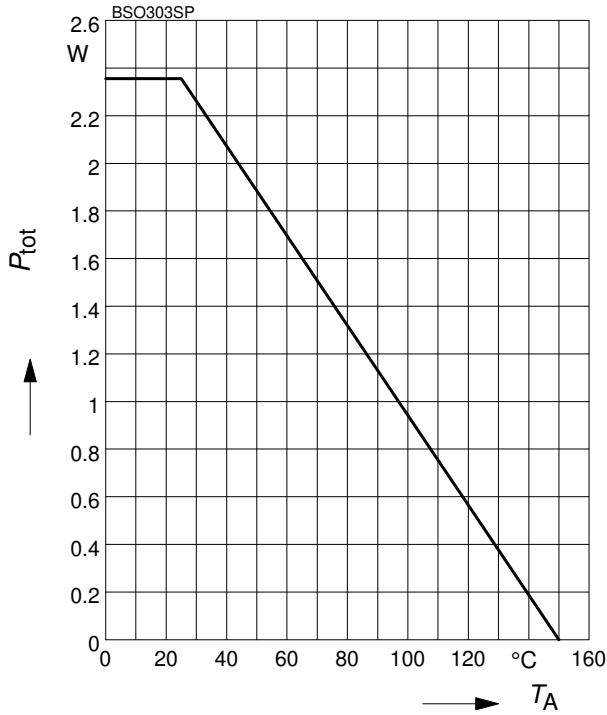
Gate to source charge	Q_{gs}	$V_{DD} = -24\text{V}, I_D = -8.9\text{A}$	-	-4.1	-6.2	nC
Gate to drain charge	Q_{gd}		-	-15.8	-26	
Gate charge total	Q_g	$V_{DD} = -24\text{V}, I_D = -8.9\text{A},$ $V_{GS} = 0 \text{ to } -10\text{V}$	-	-46	-69	
Gate plateau voltage	$V_{(plateau)}$	$V_{DD} = -24\text{V}, I_D = -8.9\text{A}$	-	-2.4	-	V

Reverse Diode

Inverse diode continuous forward current	I_S	$T_A = 25\text{ }^\circ\text{C}$	-	-	-3.5	A
Inverse diode direct current, pulsed	I_{SM}		-	-	-35.6	
Inverse diode forward voltage	V_{SD}	$V_{GS} = 0, I_F = I_D $	-	-0.87	-1.09	V
Reverse recovery time	t_{rr}	$V_R = -15\text{V}, I_F = I_D ,$ $di_F/dt = 100\text{A}/\mu\text{s}$	-	25	37	ns
Reverse recovery charge	Q_{rr}		-	11.7	17.6	nC

1 Power dissipation

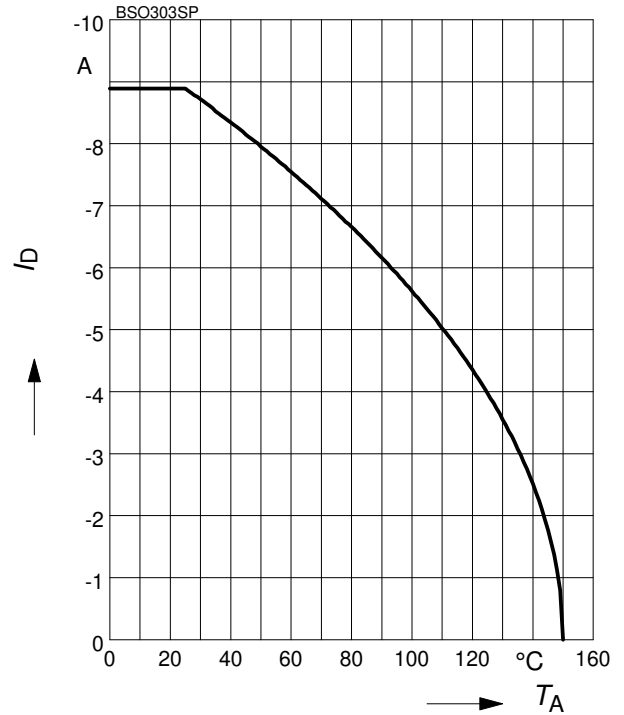
$$P_{tot} = f(T_A)$$



2 Drain current

$$I_D = f(T_A)$$

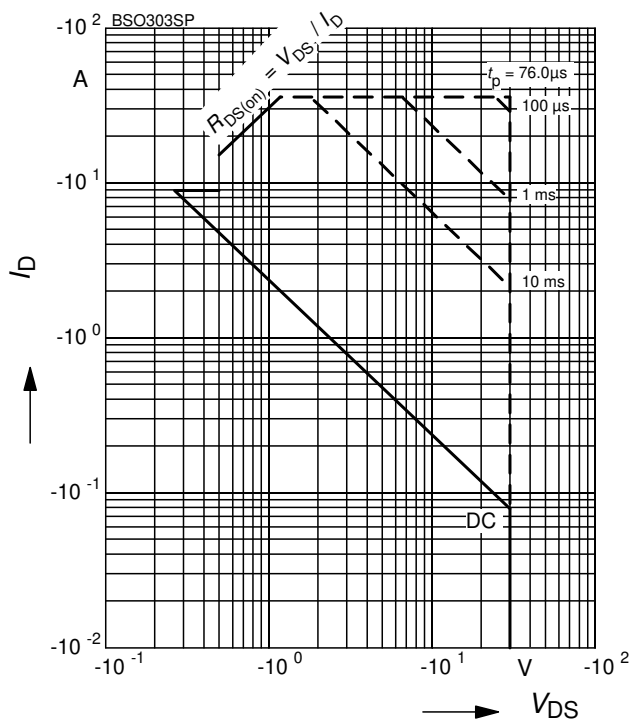
parameter: $|V_{GS}| \geq 10 \text{ V}$



3 Safe operating area

$$I_D = f(V_{DS})$$

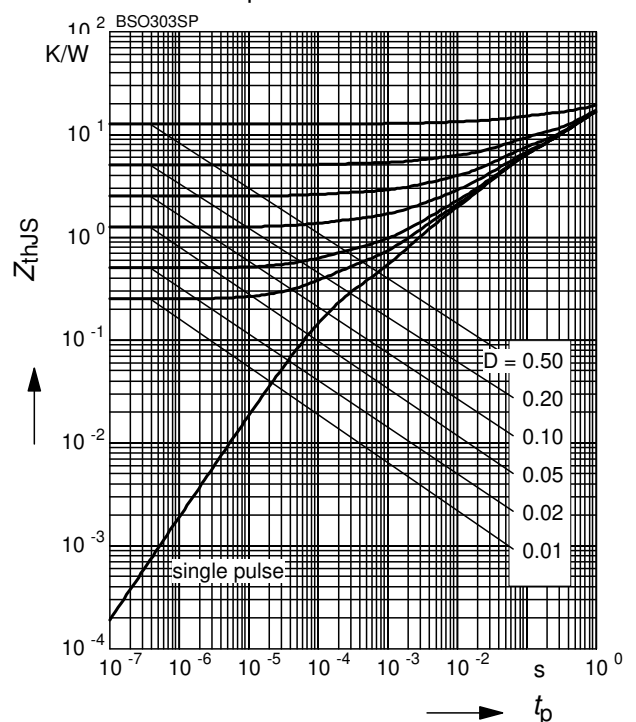
parameter: $D = 0$, $T_A = 25 \text{ °C}$



4 Transient thermal impedance

$$Z_{thJS} = f(t_p)$$

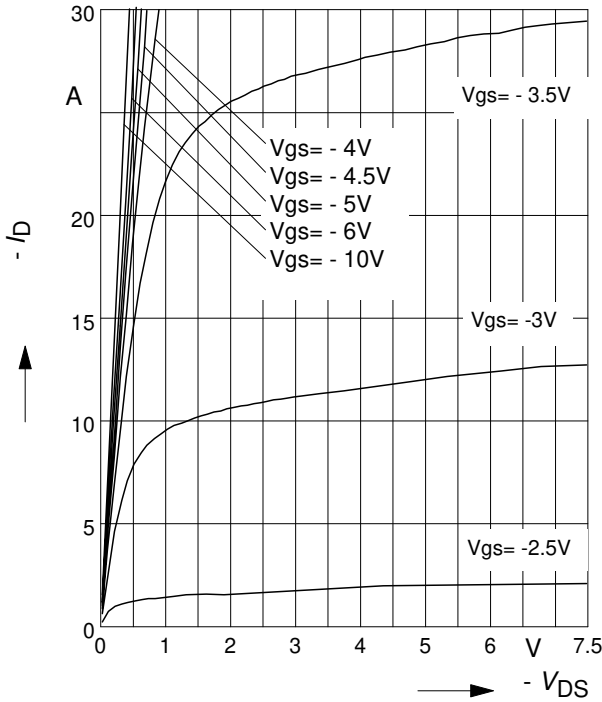
parameter: $D = t_p/T$



5 Typ. output characteristic

$I_D = f(V_{DS}); T_j=25^\circ\text{C}$

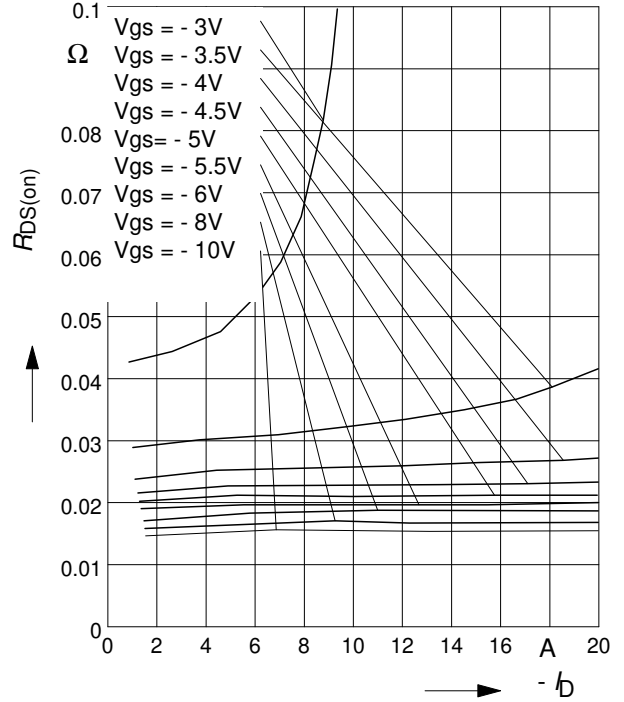
parameter: $t_p = 80 \mu\text{s}$



6 Typ. drain-source on resistance

$R_{DS(on)} = f(I_D)$

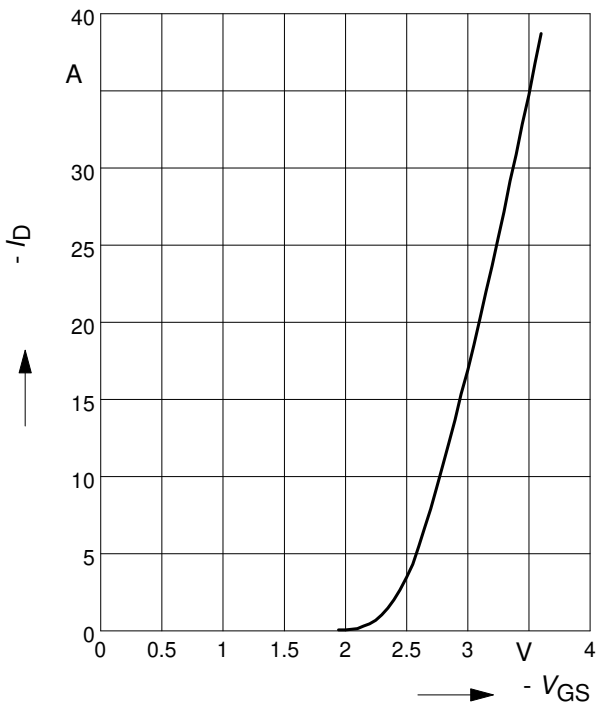
parameter: V_{GS}



7 Typ. transfer characteristics

$I_D = f(V_{GS}); |V_{DS}| \geq 2 \times |I_D| \times R_{DS(on)max}$

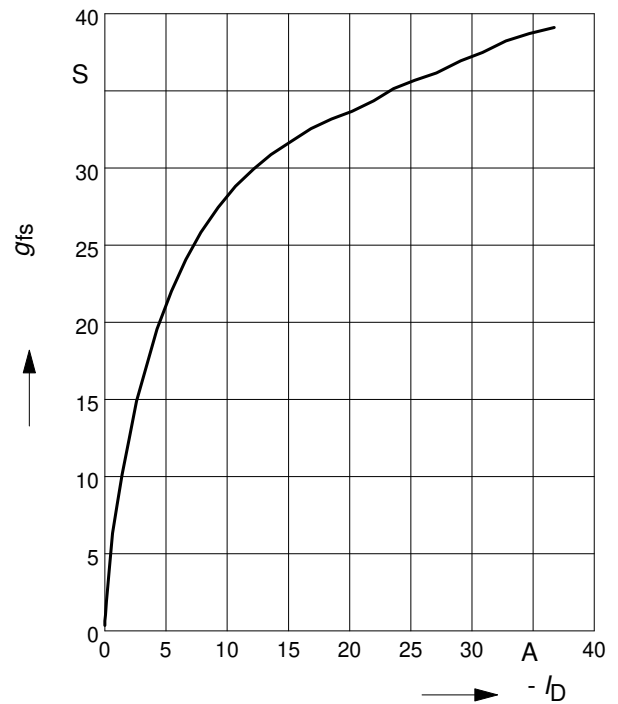
parameter: $t_p = 80 \mu\text{s}$



8 Typ. forward transconductance

$g_{fs} = f(I_D); T_j=25^\circ\text{C}$

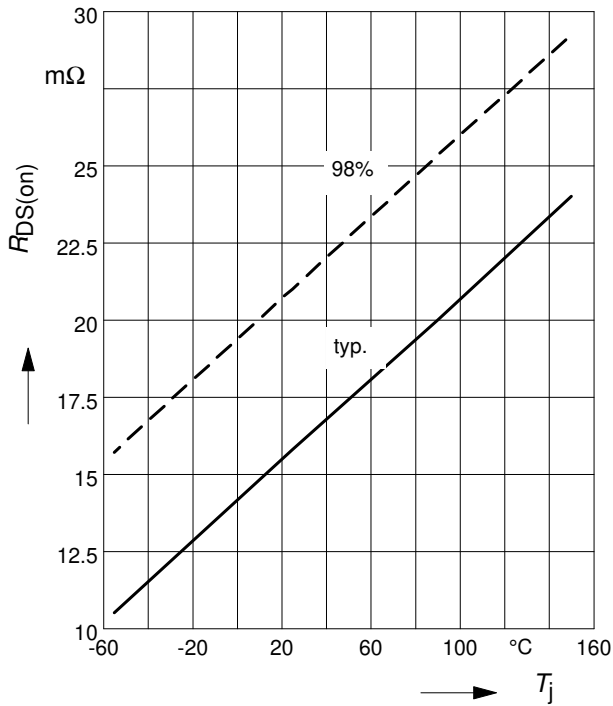
parameter: $t_p = 80 \mu\text{s}$



9 Drain-source on-resistance

$$R_{DS(on)} = f(T_j)$$

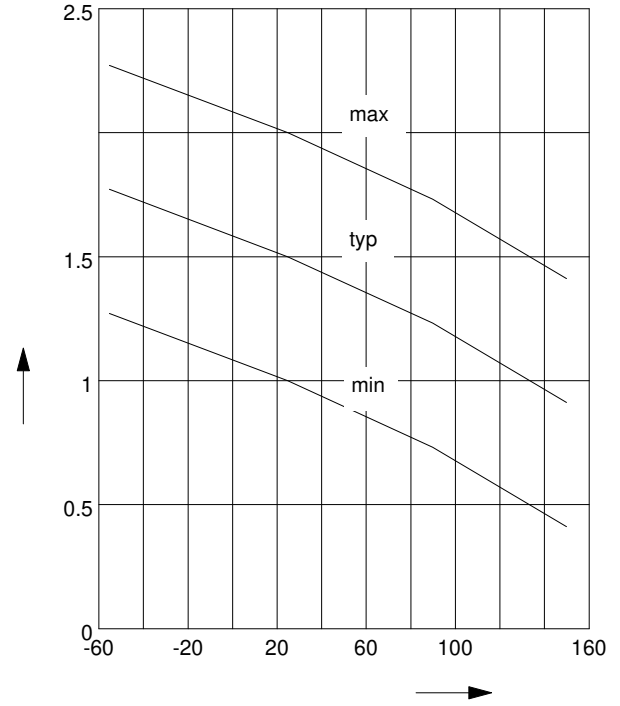
parameter: $I_D = -8.9 \text{ A}$, $V_{GS} = -10 \text{ V}$



10 Typ. gate threshold voltage

$$V_{GS(th)} = f(T_j)$$

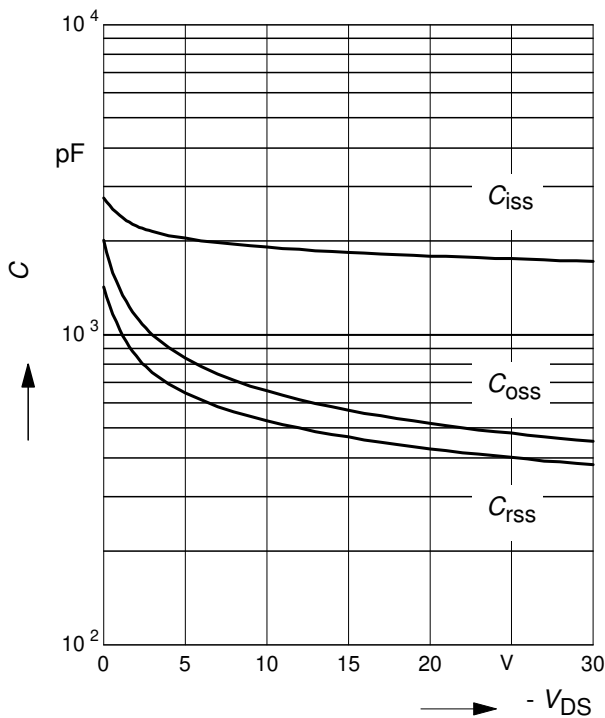
parameter: $V_{GS} = V_{DS}$



11 Typ. capacitances

$$C = f(V_{DS})$$

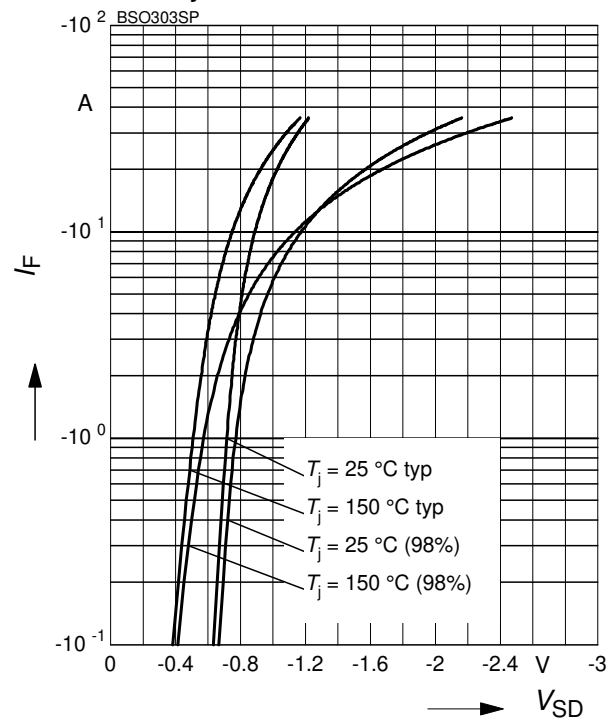
parameter: $V_{GS}=0$, $f=1 \text{ MHz}$



12 Forward character. of reverse diode

$$I_F = f(V_{SD})$$

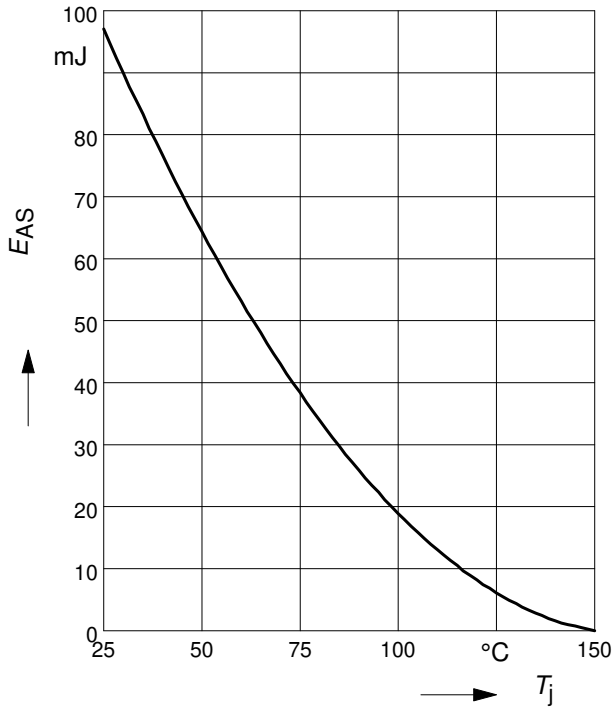
parameter: T_j , $t_p = 80 \mu\text{s}$



13 Typ. avalanche energy

$E_{AS} = f(T_j)$, par.: $I_D = -8.9\text{ A}$

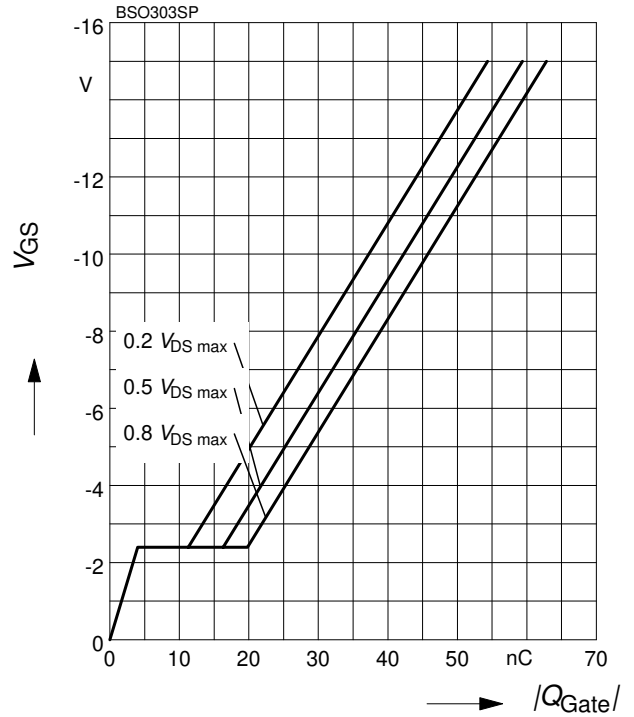
$V_{DD} = -25\text{ V}$, $R_{GS} = 25\ \Omega$



14 Typ. gate charge

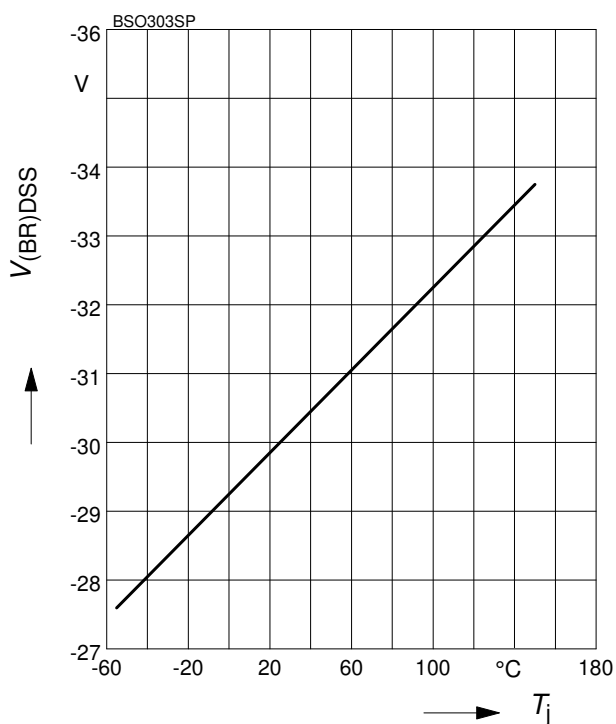
$V_{GS} = f(Q_{Gate})$

parameter: $I_D = -8.9\text{ A}$ pulsed



15 Drain-source breakdown voltage

$V_{(BR)DSS} = f(T_j)$



Published by
Infineon Technologies AG,
Bereichs Kommunikation
St.-Martin-Strasse 53,
D-81541 München
© Infineon Technologies AG 1999
All Rights Reserved.

Attention please!

The information herein is given to describe certain components and shall not be considered as warranted characteristics.

Terms of delivery and rights to technical change reserved.

We hereby disclaim any and all warranties, including but not limited to warranties of non-infringement, regarding circuits, descriptions and charts stated herein.

Infineon Technologies is an approved CECC manufacturer.

Information

For further information on technology, delivery terms and conditions and prices please contact your nearest Infineon Technologies Office in Germany or our Infineon Technologies Representatives worldwide (see address list).

Warnings

Due to technical requirements components may contain dangerous substances.

For information on the types in question please contact your nearest Infineon Technologies Office.

Infineon Technologies Components may only be used in life-support devices or systems with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support device or system, or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body, or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.