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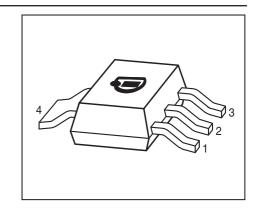


## **NPN Silicon Darlington Transistors**

- High collector current
- Low collector-emitter saturation voltage
- Complementary types: BSP60 BSP62 (PNP)
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101







Туре	Marking	Pin Configuration					Package	
BSP50	BSP50	1=B	2=C	3=E	4=C	-	-	SOT223
BSP51	BSP51	1=B	2=C	3=E	4=C	-	-	SOT223
BSP52	BSP52	1=B	2=C	3=E	4=C	-	-	SOT223

### **Maximum Ratings**

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V <sub>CEO</sub>		V
BSP50		45	
BSP51		60	
BSP52		80	
Collector-base voltage	V <sub>CBO</sub>		
BSP50		60	
BSP51		80	
BSP52		90	
Emitter-base voltage	V <sub>EBO</sub>	5	
Collector current	I <sub>C</sub>	1	Α
Peak collector current, $t_p \le 10 \text{ ms}$	I <sub>CM</sub>	2	
Base current	/ <sub>B</sub>	100	mA
Total power dissipation-	P <sub>tot</sub>	1.5	W
<i>T</i> <sub>S</sub> ≤ 124 °C			
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-65 150	

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### **Thermal Resistance**

Parameter	Symbol	Value	Unit
Junction - soldering point <sup>1)</sup>	R <sub>thJS</sub>	≤ 17	K/W

# **Electrical Characteristics** at $T_A = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics	·			•	
Collector-emitter breakdown voltage	$V_{(BR)CEO}$				V
$I_{\rm C}$ = 10 mA, $I_{\rm B}$ = 0 , BSP50		45	-	_	
$I_{\rm C}$ = 10 mA, $I_{\rm B}$ = 0 , BSP51		60	-	-	
$I_{\rm C}$ = 10 mA, $I_{\rm B}$ = 0 , BSP52		80	-	_	
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>				
$I_{\rm C}$ = 100 $\mu{\rm A},I_{\rm E}$ = 0 , BSP50		60	-	_	
$I_{\rm C}$ = 100 $\mu{\rm A},I_{\rm E}$ = 0 , BSP51		80	-	_	
$I_{\rm C}$ = 100 $\mu{\rm A},I_{\rm E}$ = 0 , BSP52		90	-	-	
Emitter-base breakdown voltage	V <sub>(BR)EBO</sub>	5	-	-	
$I_{\rm E}$ = 100 $\mu$ A, $I_{\rm C}$ = 0					
Collector-emitter cutoff current	I <sub>CES</sub>	-	-	10	μA
$V_{\text{CE}} = V_{\text{CE0max}}, V_{\text{BE}} = 0$					
Emitter-base cutoff current	I <sub>EBO</sub>	-	-	10	μA
$V_{EB} = 4 \text{ V}, I_{C} = 0$					
DC current gain <sup>2)</sup>	h <sub>FE</sub>				-
$I_{\rm C}$ = 150 mA, $V_{\rm CE}$ = 10 V		1000	-	-	
$I_{\rm C}$ = 500 mA, $V_{\rm CE}$ = 10 V		2000	-	-	
Collector-emitter saturation voltage <sup>2)</sup>	V <sub>CEsat</sub>				V
$I_{\rm C}$ = 500 mA, $I_{\rm B}$ = 0.5 mA		-	-	1.3	
$I_{\rm C}$ = 1 A, $I_{\rm B}$ = 1 mA				1.8	
Base emitter saturation voltage <sup>2)</sup>	V <sub>BEsat</sub>				
$I_{\rm C}$ = 500 mA, $I_{\rm B}$ = 0.5 mA		-	-	1.9	
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 1 A		-	-	2.2	

 $<sup>^{1}</sup>$ For calculation of  $R_{thJA}$  please refer to Application Note AN077 (Thermal Resistance Calculation)

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 $<sup>^{2}</sup>$ Pulse test: t < 300µs; D < 2%

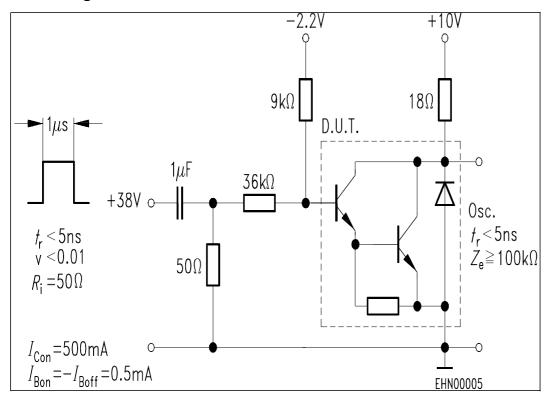


**Electrical Characteristics** at  $T_A = 25$ °C, unless otherwise specified

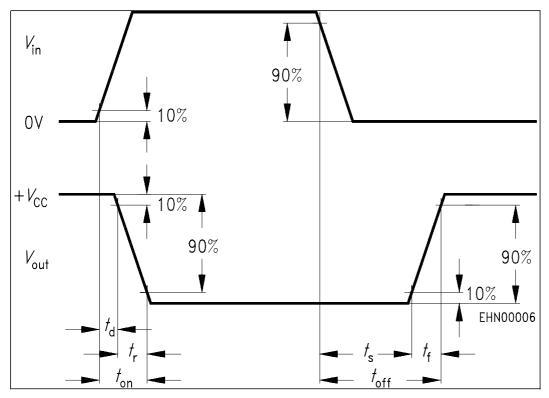
Parameter	Symbol	Values		Unit	
		min.	typ.	max.	
AC Characteristics					
Transition frequency	$f_{T}$	-	200	-	MHz
$I_{\rm C}$ = 100 mA, $V_{\rm CE}$ = 5 V, $f$ = 100 MHz					
Tum-on time	$t_{(on)}$	-	400	-	ns
$I_{\rm C}$ = 500 mA, $I_{\rm B1}$ = $I_{\rm B2}$ = 0.5 mA					
Tum-off time	t <sub>(Off)</sub>	-	1500	_	
$I_{\rm C}$ = 500 mA, $I_{\rm B1}$ = $I_{\rm B2}$ = 0.5 mA					



## Switching time test circuit



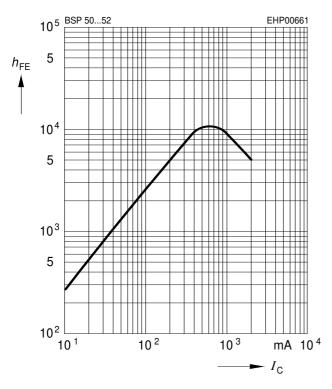
# Switching time waveform





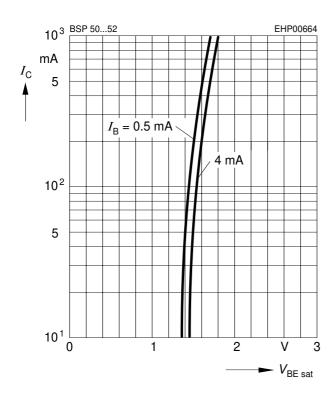
## **DC** current gain $h_{FE} = f(I_C)$

$$V_{CE}$$
 = 10 V



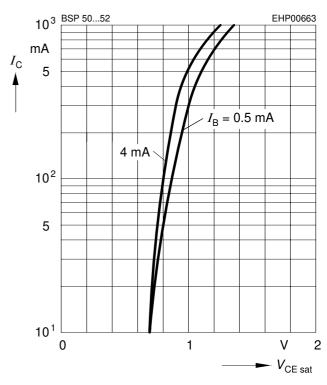
### **Base-emitter saturation voltage**

$$I_{\rm C} = f(V_{\rm BEsat}), I_{\rm B} = {\rm Parameter}$$



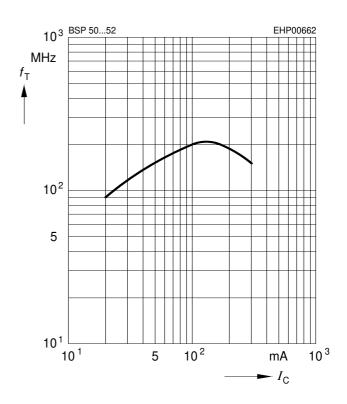
### Collector-emitter saturation voltage

 $I_{C} = f(V_{CEsat}), I_{B} = Parameter$ 



## Transition frequency $f_T = f(I_C)$

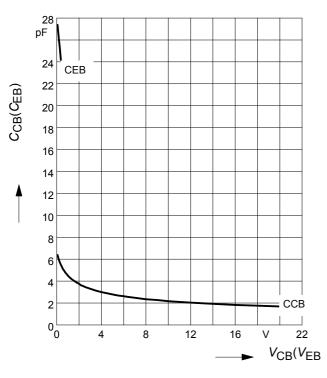
$$V_{CE} = 5 \text{ V}, f = 100 \text{ MHz}$$

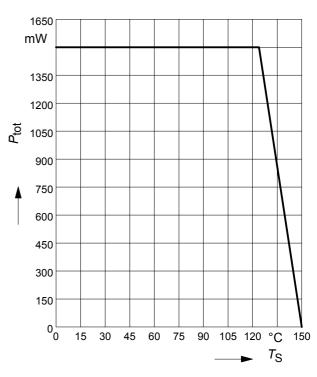




# Collector-base capacitance $C_{\text{cb}} = f(V_{\text{CB}})$ Emitter-base capacitance $C_{\text{eb}} = f(V_{\text{EB}})$

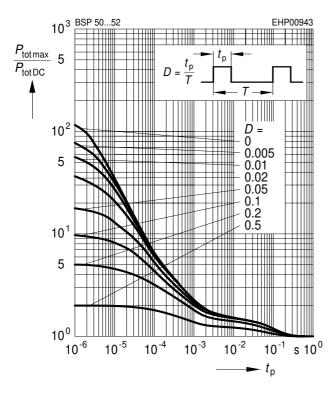
## Total power dissipation $P_{tot} = f(T_S)$





### **Permissible Pulse Load**

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$ 

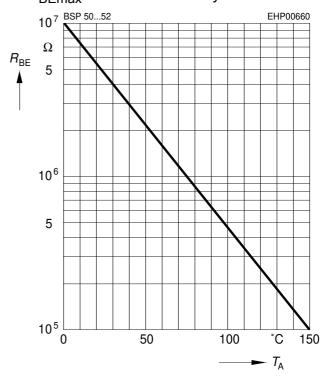


# External resistance $R_{BE} = f (T_A)^{**}$

 $V_{CB} = V_{CEmax}$ 

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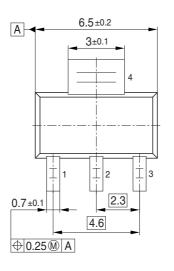
\*\* R<sub>BEmax</sub> for thermal stability

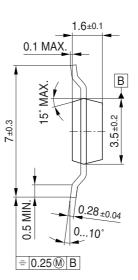




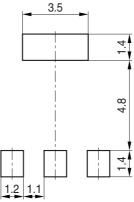
# Package Outline



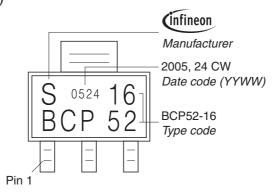




Foot Print

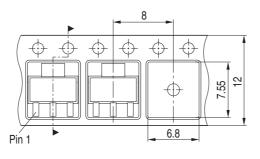


### Marking Layout (Example)



## Packing

Reel ø180 mm = 1.000 Pieces/Reel Reel ø330 mm = 4.000 Pieces/Reel







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