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BSS138DW

#### **DUAL N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR**

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C		
50V	3.5Ω @ V <sub>GS</sub> = 10V	200mA		

### **Description**

This MOSFET has been designed to minimize the on-state resistance  $(R_{DS(on)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## **Applications**

Load Switch

SOT-363



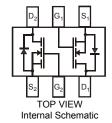
TOP VIEW

#### **Features**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Notes 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: SOT-363
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)



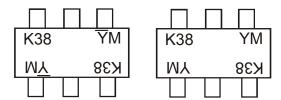
#### Ordering Information (Note 4)

Part Number	Case	Packaging
BSS138DW-7-F	SOT-363	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## Marking Information



K38 = Product Type Marking Code

 $\underline{Y}$ M = Date Code Marking for SAT (Shanghai Assembly/ Test site)  $\overline{Y}$ M = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{Y}$  = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Date Code Rey															
Year	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019
Code	S	Т	U	V	W	Х	Υ	Z	Α	В	С	D	Е	F	G
Month	Jan	Fe	b	Mar	Apr	May	Ju	n	Jul	Aug	Sep	Oc	t I	Nov	Dec
Code	1	2		3	4	5	6	;	7	8	9	0		N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristi	С	Symbol	BSS138DW	Units
Drain-Source Voltage		V <sub>DSS</sub>	50	V
Drain-Gate Voltage (Note 7)		$V_{DGR}$	50	V
Gate-Source Voltage	Continuous	V <sub>GSS</sub>	±20	V
Drain Current (Note 5)	Continuous	I <sub>D</sub>	200	mA

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

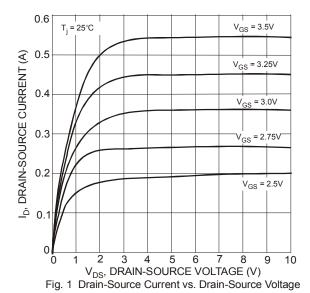
Characteristic	Symbol	BSS138DW	Units
Total Power Dissipation (Note 5)	$P_{D}$	200	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	625	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

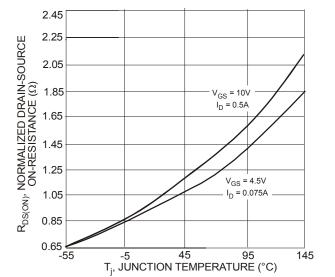
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)					•		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	50	75	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	0.5	μΑ	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V	
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	$V_{GS(th)}$	0.5	1.2	1.5	>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>		1.4	3.5	Ω	$V_{GS} = 10V, I_D = 0.22A$	
Forward Transconductance	g <sub>FS</sub>	100	_	_	mS	$V_{DS}$ =25V, $I_{D}$ = 0.2A, f = 1.0KHz	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C <sub>iss</sub>	_	_	50	pF		
Output Capacitance	C <sub>oss</sub>	_	_	25	pF	$V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	C <sub>rss</sub>			8.0	pF	7	
SWITCHING CHARACTERISTICS	<u> </u>						
Turn-On Delay Time	t <sub>D(ON)</sub>	_	_	20	ns	$V_{DD} = 30V, I_D = 0.2A,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	_	20	ns	$R_{GEN} = 50\Omega$	

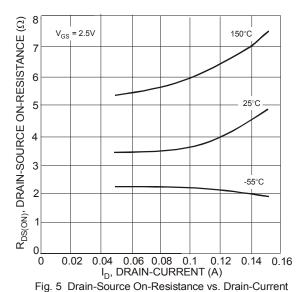
- Notes: 5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
  - 6. Short duration pulse test used to minimize self-heating effect.
  - $7. \quad R_{GS} \leq 20 K \Omega.$

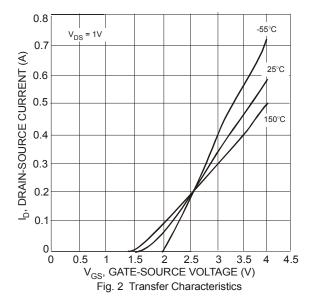












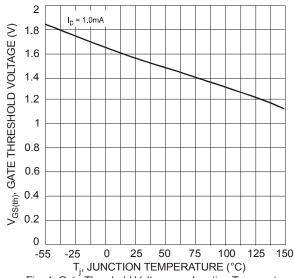


Fig. 4 Gate Threshold Voltage vs. Junction Temperature

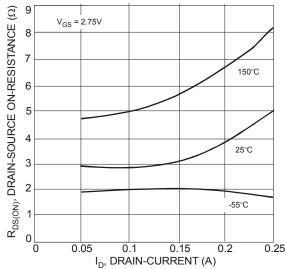
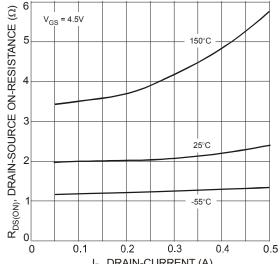
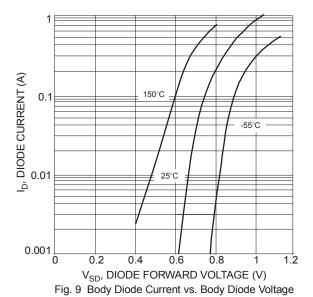


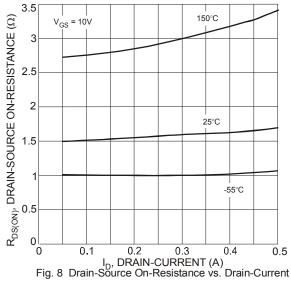
Fig. 6 Drain-Source On-Resistance vs. Drain-Current





I<sub>D</sub>, DRAIN-CURRENT (A) Fig. 7 Drain-Source On-Resistance vs. Drain-Current





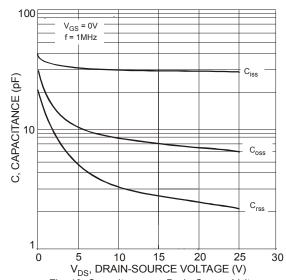
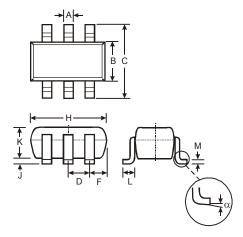


Fig. 10 Capacitance vs. Drain-Source Voltage



## **Package Outline Dimensions**

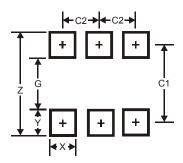
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT363							
Dim	Min	Max	Тур				
Α	0.10	0.30	0.25				
В	1.15	1.35	1.30				
С	2.00	2.20	2.10				
D		0.65 Ty	р				
F	0.40 0.45 0.425						
Н	1.80 2.20 2.15						
J	0	0.05					
K	0.90	1.00	1.00				
L	0.25	0.40	0.30				
М	0.10	0.22	0.11				
α	0°	8°	-				
All Dimensions in mm							

## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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