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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China











BSS84DW

DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D Τ _A = +25°C
-50V	10Ω @ V _{GS} = -5V	-130mA

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- General Purpose Interfacing Switch
- · Power Management Functions
- Analog Switch

SOT363



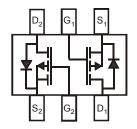
Top View

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Weight: 0.006 grams (approximate)



Top View Internal Schematic

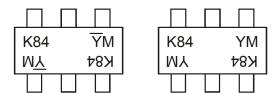
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
BSS84DW-7-F	Standard	SOT363	3,000/Tape & Reel
BSS84DWQ-13	Automotive	SOT363	10,000/Tape & Reel
BSS84DWQ-7	Automotive	SOT363	3.000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



K84 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)
YM = Date Code Marking for CAT (Chengdu Assembly/ Test site)

Y or \overline{Y} = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Date Code Key

Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	J	K	L	M	N	Р	R	S	Т	U	V	W	Х	Υ	Z
Month	Jan	Fe	b	Mar	Apr	May	Ju	n	Jul	Aug	Sep	Oc	t I	Nov	Dec
Code	1	2		3	4	5	6		7	8	9	0		N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-50	V
Drain-Gate Voltage (Note 5)		V_{DGR}	-50	V
Gate-Source Voltage	Continuous	V_{GSS}	±20	V
Drain Current (Note 6)	Continuous	I_{D}	-130	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	P_{D}	300	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	417	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

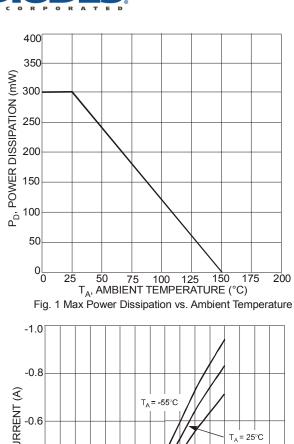
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)		l.				
Drain-Source Breakdown Voltage	BV _{DSS}	-50	-75	_	V	$V_{GS} = 0V, I_D = -250\mu A$
		_	_	-1	μA	V _{DS} = -50V, V _{GS} = 0V, T _J = +25°C
Zero Gate Voltage Drain Current	Inno	_	_	-2	μA	$V_{DS} = -50V$, $V_{GS} = 0V$, $T_{J} = +125$ °C
Zero Gate Voltage Brain Guirent	IDSS	_	_	-100	nA	$V_{DS} = -25V$, $V_{GS} = 0V$, $T_{J} = +25$ °C
Gate-Body Leakage	I _{GSS}	_	_	±10	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)	ē.					
Gate Threshold Voltage	V _{GS(th)}	-0.8	-1.6	-2.0	V	$V_{DS} = V_{GS}$, $I_D = -1mA$
Static Drain-Source On-Resistance	R _{DS (ON)}	_	6	10	Ω	$V_{GS} = -5V, I_D = -0.100A$
Forward Transconductance	g _{FS}	0.05	_	_	S	$V_{DS} = -25V, I_D = -0.1A$
DYNAMIC CHARACTERISTICS	ē.					
Input Capacitance	C _{iss}	_		45	рF	
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = -25V$, $V_{GS} = 0V$, $f = 1.0MHz$
Reverse Transfer Capacitance	C _{rss}	_	_	12	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{D(ON)}		10		ns	$V_{DD} = -30V$, $I_D = -0.27A$,
Turn-Off Delay Time	t _{D(OFF)}	_	18	_	ns	$R_{GEN} = 50\Omega$, $V_{GS} = -10V$

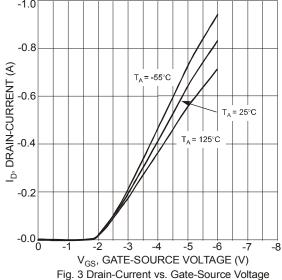
 $Notes: \hspace{1cm} 5. \hspace{1cm} R_{\text{GS}} \leq 20 K \Omega.$

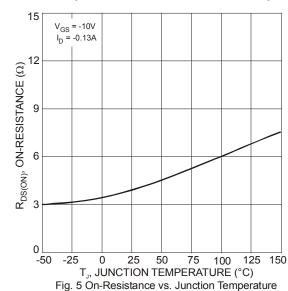
^{6.} Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com.

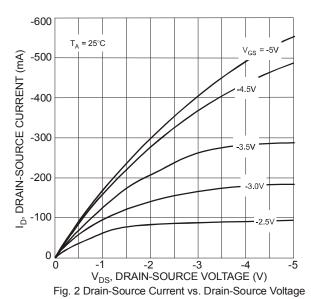
^{7.} Short duration pulse test used to minimize self-heating effect.

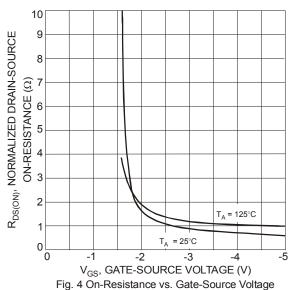


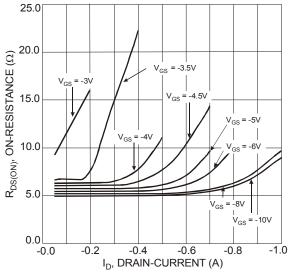








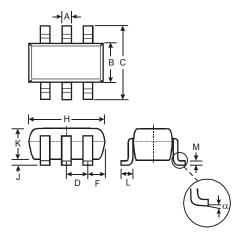






Package Outline Dimensions

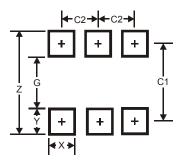
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT363								
Dim	Min Max Typ							
Α	0.10	0.30	0.25					
В	1.15	1.35	1.30					
C	2.00	2.20	2.10					
D		0.65 Ty	p					
F	0.40	0.45	0.425					
Н	1.80	2.20	2.15					
7	0	0.10	0.05					
K	0.90	1.00	1.00					
L	0.25	0.40	0.30					
M	0.10	0.22	0.11					
α	0°	8°	-					
All	All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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