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10 December 2015

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BT136X-600D

4Q Triac

30 April 2015

Product data sheet

1. General description

Planar passivated very sensitive gate four quadrant triac in a SOT186A "full pack" plastic package intended for use in general purpose bidirectional switching and phase control applications, where high sensitivity is required in all four quadrants. This very sensitive gate "series D" triac is intended to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits.

2. Features and benefits

- Direct triggering from low power drivers and logic ICs
- High blocking voltage capability
- Isolated package
- Low holding current for small load currents and lowest EMI at commutation
- Planar passivated for voltage ruggedness and reliability
- Triggering in all four quadrants
- Very sensitive gate

3. Applications

- General purpose motor control
- General purpose switching

4. Quick reference data

Table 1. Quick reference data

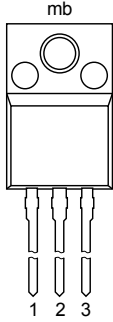

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-------------------------------|--------------------------------------|---|-----|-----|-----|------|
| V_{DRM} | repetitive peak off-state voltage | | - | - | 600 | V |
| I_{TSM} | non-repetitive peak on-state current | full sine wave; $T_{j(\text{init})} = 25\text{ }^{\circ}\text{C}$; $t_p = 20\text{ ms}$; Fig. 4 ; Fig. 5 | - | - | 25 | A |
| $I_{T(RMS)}$ | RMS on-state current | full sine wave; $T_h \leq 92\text{ }^{\circ}\text{C}$; Fig. 1 ; Fig. 2 ; Fig. 3 | - | - | 4 | A |
| Static characteristics | | | | | | |
| I_{GT} | gate trigger current | $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ }^{\circ}\text{C}$; Fig. 7 | - | 2 | 5 | mA |
| | | $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G-; $T_j = 25\text{ }^{\circ}\text{C}$; Fig. 7 | - | 2.5 | 5 | mA |



| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--------|-----------------|--|-----|-----|-----|------|
| | | $V_D = 12\text{ V}; I_T = 0.1\text{ A}; T2- G-;$ $T_j = 25\text{ }^\circ\text{C};$ Fig. 7 | - | 2.5 | 5 | mA |
| | | $V_D = 12\text{ V}; I_T = 0.1\text{ A}; T2- G+;$ $T_j = 25\text{ }^\circ\text{C};$ Fig. 7 | - | 5 | 10 | mA |
| I_H | holding current | $V_D = 12\text{ V}; T_j = 25\text{ }^\circ\text{C};$ Fig. 9 | - | 1.2 | 10 | mA |

5. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------------------|---|---|
| 1 | T1 | main terminal 1 |  <p>TO-220F (SOT186A)</p> |  |
| 2 | T2 | main terminal 2 | | |
| 3 | G | gate | | |
| mb | n.c. | mounting base; isolated | | |

6. Ordering information

Table 3. Ordering information

| Type number | Package | | |
|-------------|---------|---|---------|
| | Name | Description | Version |
| BT136X-600D | TO-220F | plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack" | SOT186A |

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | Min | Max | Unit |
|--------------|--------------------------------------|---|-----|-----|-------------|
| V_{DRM} | repetitive peak off-state voltage | | - | 600 | V |
| $I_{T(RMS)}$ | RMS on-state current | full sine wave; $T_h \leq 92\text{ °C}$; Fig. 1 ; Fig. 2 ; Fig. 3 | - | 4 | A |
| I_{TSM} | non-repetitive peak on-state current | full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 20\text{ ms}$; Fig. 4 ; Fig. 5 | - | 25 | A |
| | | full sine wave; $T_{j(init)} = 25\text{ °C}$; $t_p = 16.7\text{ ms}$ | - | 27 | A |
| I^2t | I^2t for fusing | $t_p = 10\text{ ms}$; SIN | - | 3.1 | A^2s |
| dl_T/dt | rate of rise of on-state current | $I_G = 10\text{ mA}$; T2+ G+ | - | 50 | $A/\mu s$ |
| | | $I_G = 10\text{ mA}$; T2+ G- | - | 50 | $A/\mu s$ |
| | | $I_G = 20\text{ mA}$; T2- G+ | - | 10 | $A/\mu s$ |
| | | $I_G = 10\text{ mA}$; T2- G- | - | 50 | $A/\mu s$ |
| I_{GM} | peak gate current | | - | 2 | A |
| P_{GM} | peak gate power | | - | 5 | W |
| $P_{G(AV)}$ | average gate power | over any 20 ms period | - | 0.5 | W |
| T_{stg} | storage temperature | | -40 | 150 | $^{\circ}C$ |
| T_j | junction temperature | | - | 125 | $^{\circ}C$ |

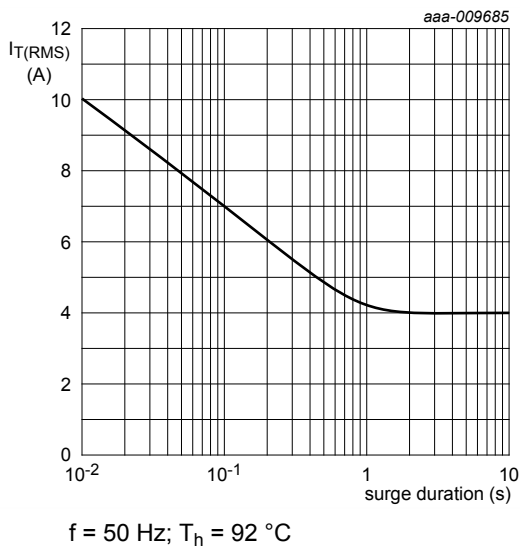


Fig. 1. RMS on-state current as a function of surge duration; maximum values

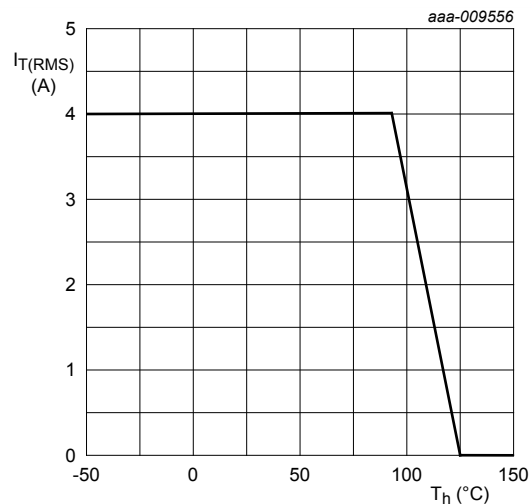


Fig. 2. RMS on-state current as a function of heatsink temperature; maximum values

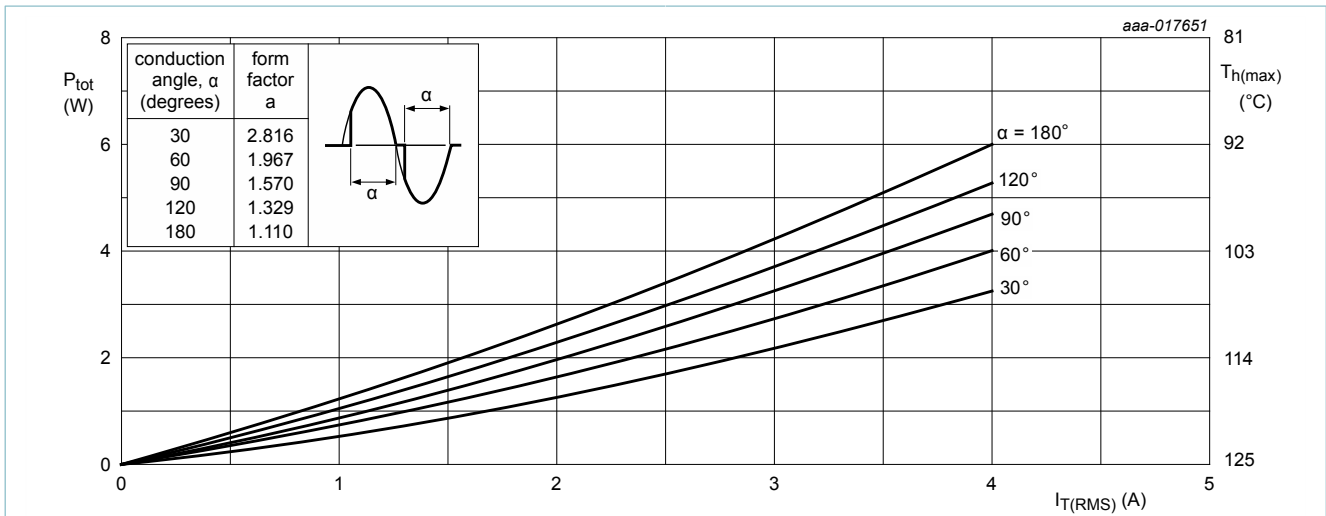


Fig. 3. Total power dissipation as a function of RMS on-state current; maximum values

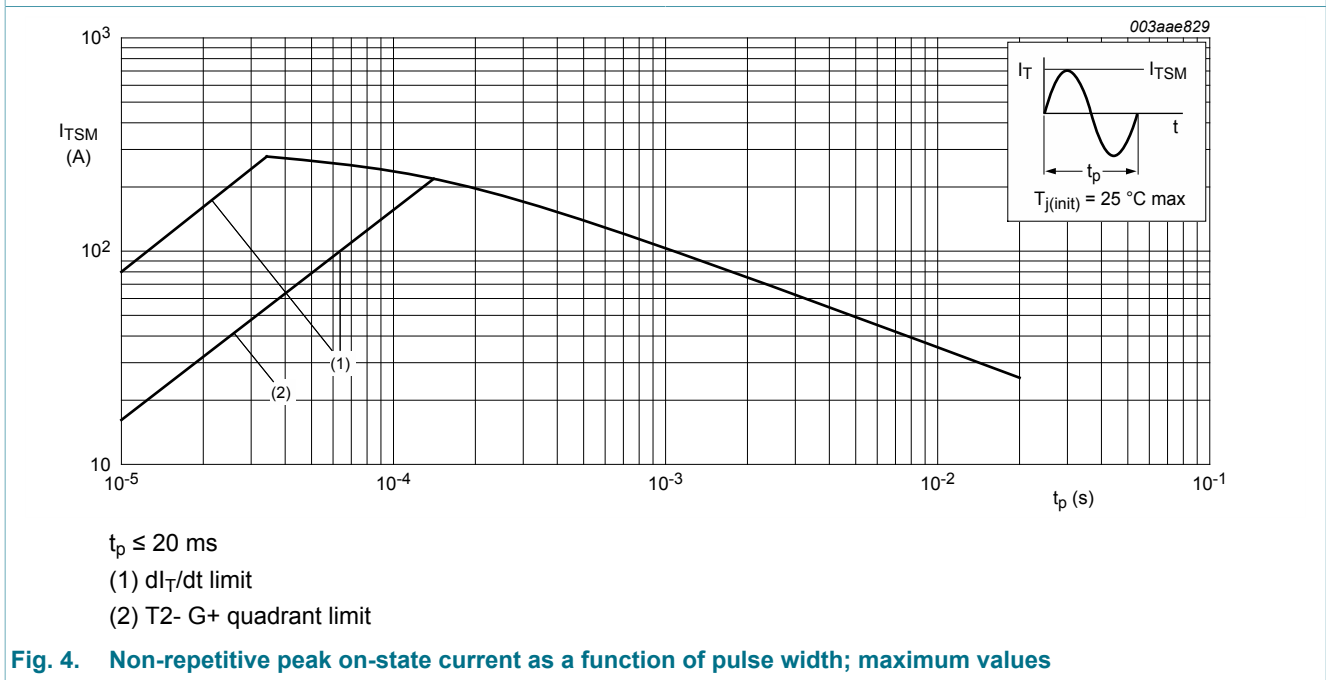
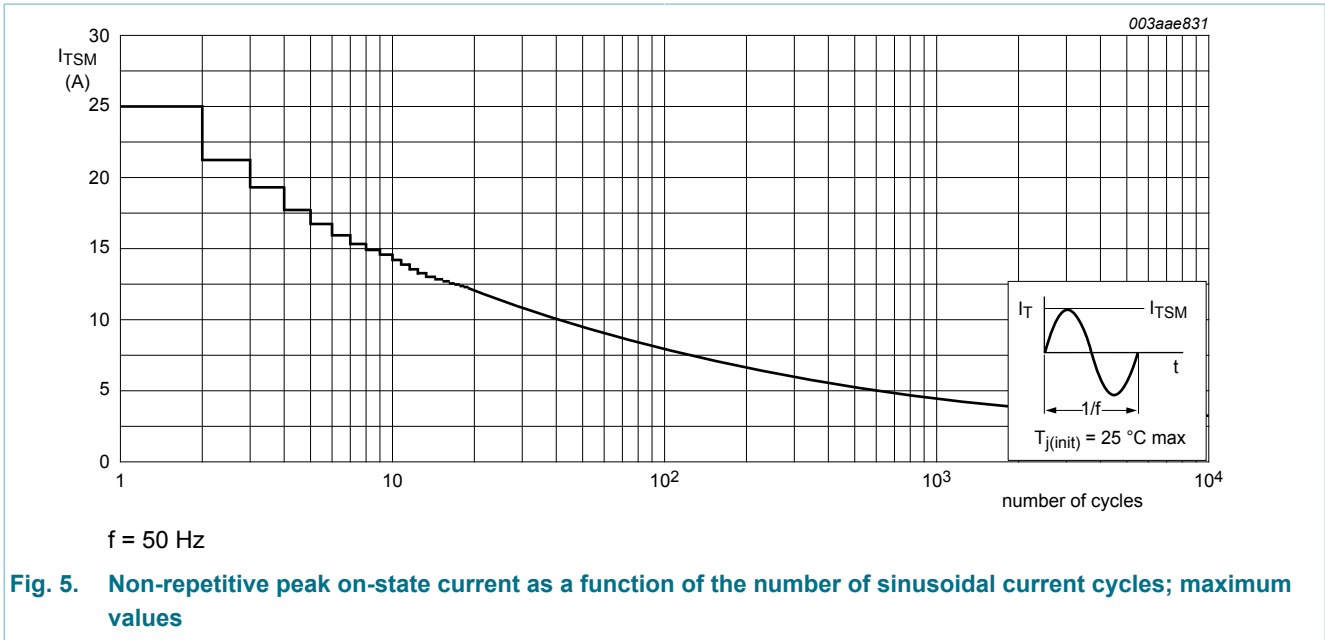


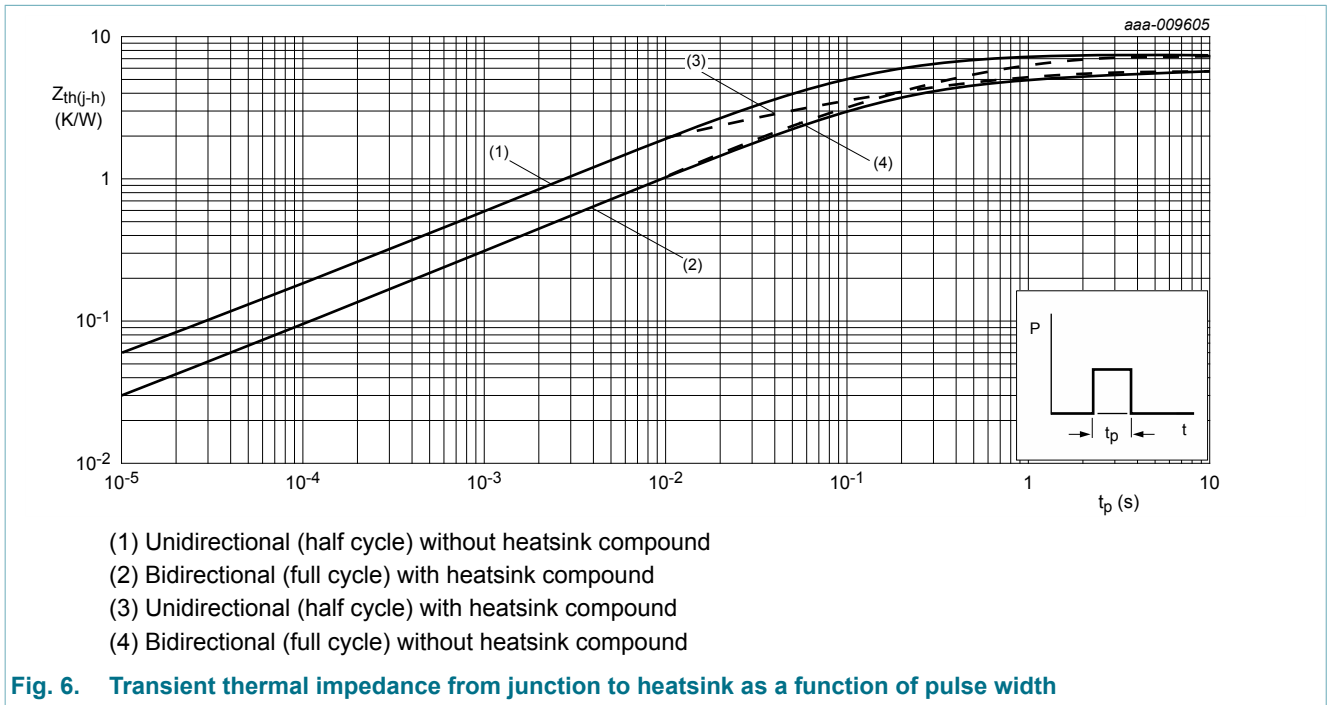
Fig. 4. Non-repetitive peak on-state current as a function of pulse width; maximum values



8. Thermal characteristics

Table 5. Thermal characteristics

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---------------|--|---|-----|-----|-----|------|
| $R_{th(j-h)}$ | thermal resistance from junction to heatsink | full or half cycle; with heatsink compound; Fig. 6 | - | - | 5.5 | K/W |
| | | full or half cycle; without heatsink compound; Fig. 6 | - | - | 7.2 | K/W |
| $R_{th(j-a)}$ | thermal resistance from junction to ambient | in free air | - | 55 | - | K/W |



9. Isolation characteristics

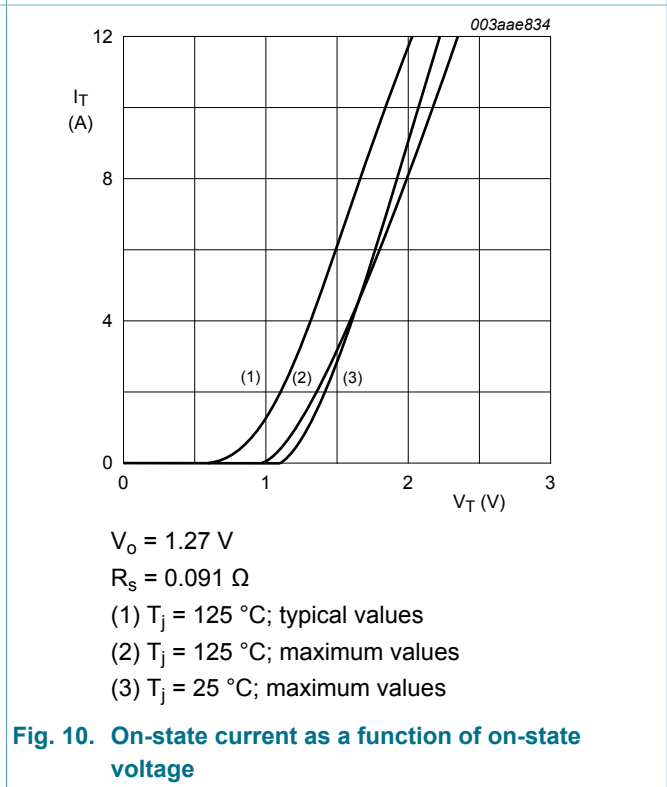
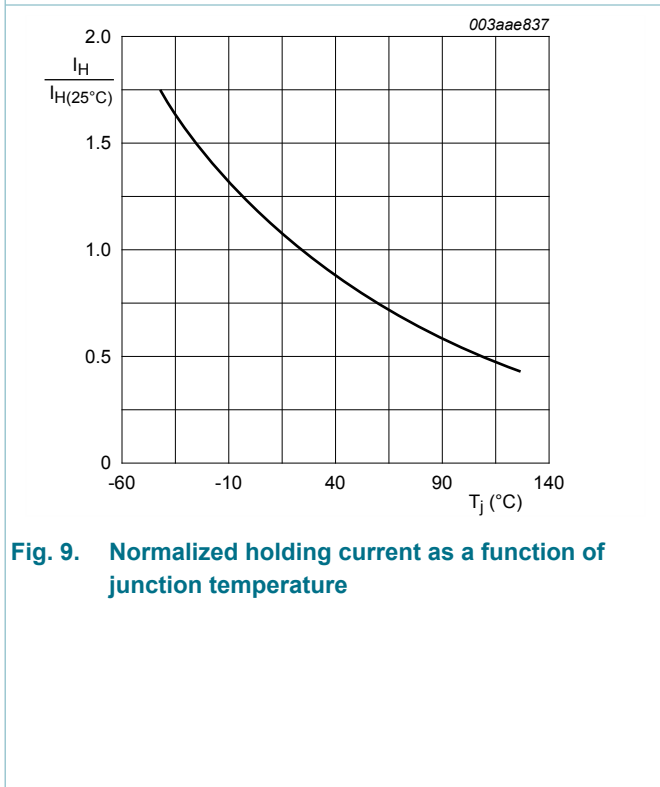
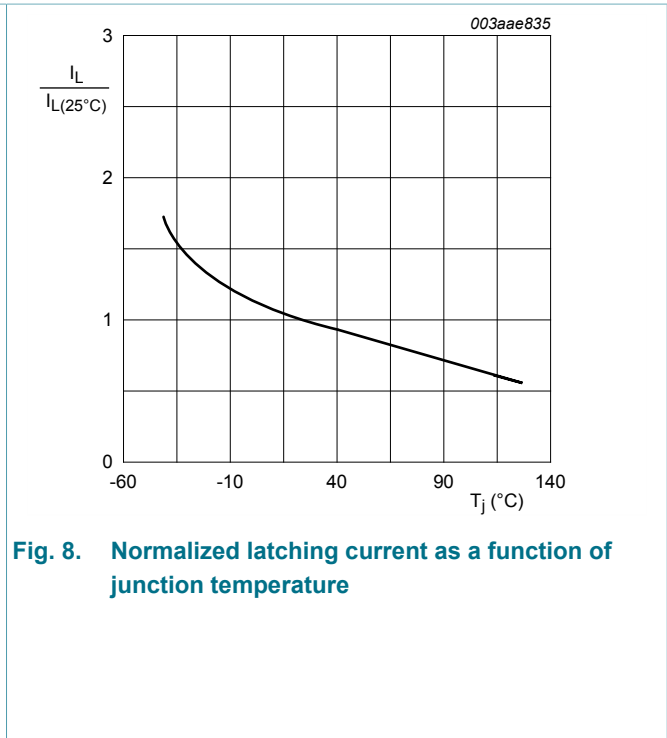
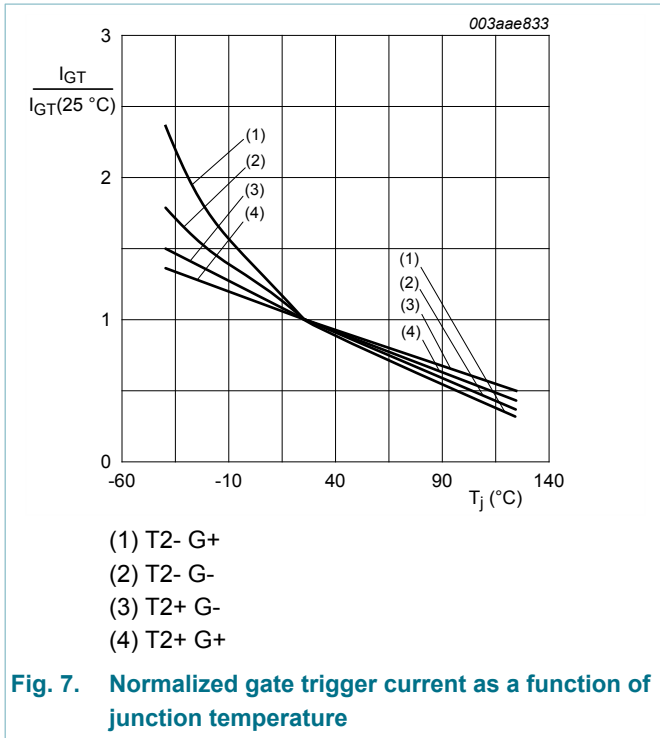
Table 6. Isolation characteristics

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|-----------------|-----------------------|--|-----|-----|------|------|
| $V_{isol(RMS)}$ | RMS isolation voltage | from all terminals to external heatsink; sinusoidal waveform; clean and dust free; $50\text{ Hz} \leq f \leq 60\text{ Hz}$; $RH \leq 65\%$; $T_h = 25\text{ }^\circ\text{C}$ | - | - | 2500 | V |
| C_{isol} | isolation capacitance | from main terminal 2 to external heatsink; $f = 1\text{ MHz}$; $T_h = 25\text{ }^\circ\text{C}$ | - | 10 | - | pF |

10. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|--------------------------------|-----------------------------------|--|------|-----|-----|------------|
| Static characteristics | | | | | | |
| I_{GT} | gate trigger current | $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ °C}$; Fig. 7 | - | 2 | 5 | mA |
| | | $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2+ G-; $T_j = 25\text{ °C}$; Fig. 7 | - | 2.5 | 5 | mA |
| | | $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G-; $T_j = 25\text{ °C}$; Fig. 7 | - | 2.5 | 5 | mA |
| | | $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; T2- G+; $T_j = 25\text{ °C}$; Fig. 7 | - | 5 | 10 | mA |
| I_L | latching current | $V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2+ G+; $T_j = 25\text{ °C}$; Fig. 8 | - | 1.6 | 10 | mA |
| | | $V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2+ G-; $T_j = 25\text{ °C}$; Fig. 8 | - | 1.2 | 15 | mA |
| | | $V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2- G-; $T_j = 25\text{ °C}$; Fig. 8 | - | 2.2 | 10 | mA |
| | | $V_D = 12\text{ V}$; $I_G = 0.1\text{ A}$; T2- G+; $T_j = 25\text{ °C}$; Fig. 8 | - | 1.2 | 15 | mA |
| I_H | holding current | $V_D = 12\text{ V}$; $T_j = 25\text{ °C}$; Fig. 9 | - | 1.2 | 10 | mA |
| V_T | on-state voltage | $I_T = 5\text{ A}$; $T_j = 25\text{ °C}$; Fig. 10 | - | 1.4 | 1.7 | V |
| V_{GT} | gate trigger voltage | $V_D = 12\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 25\text{ °C}$; Fig. 11 | - | 0.7 | 1 | V |
| | | $V_D = 400\text{ V}$; $I_T = 0.1\text{ A}$; $T_j = 125\text{ °C}$; Fig. 11 | 0.25 | 0.4 | - | V |
| I_D | off-state current | $V_D = 600\text{ V}$; $T_j = 125\text{ °C}$ | - | 0.1 | 0.5 | mA |
| Dynamic characteristics | | | | | | |
| dV_D/dt | rate of rise of off-state voltage | $V_{DM} = 402\text{ V}$; $T_j = 125\text{ °C}$; $R_{GK} = 1\text{ k}\Omega$; ($V_{DM} = 67\%$ of V_{DRM}); exponential waveform; gate open circuit | - | 5 | - | V/ μ s |
| t_{gt} | gate-controlled turn-on time | $I_{TM} = 6\text{ A}$; $V_D = 600\text{ V}$; $I_G = 0.1\text{ A}$; $dI_G/dt = 5\text{ A}/\mu\text{s}$ | - | 2 | - | μ s |



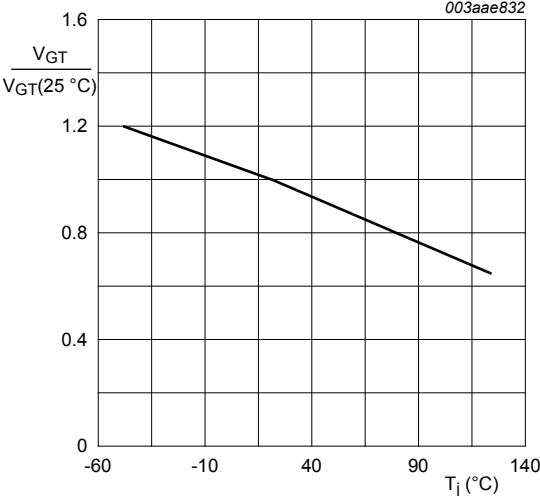


Fig. 11. Normalized gate trigger voltage as a function of junction temperature

11. Package outline

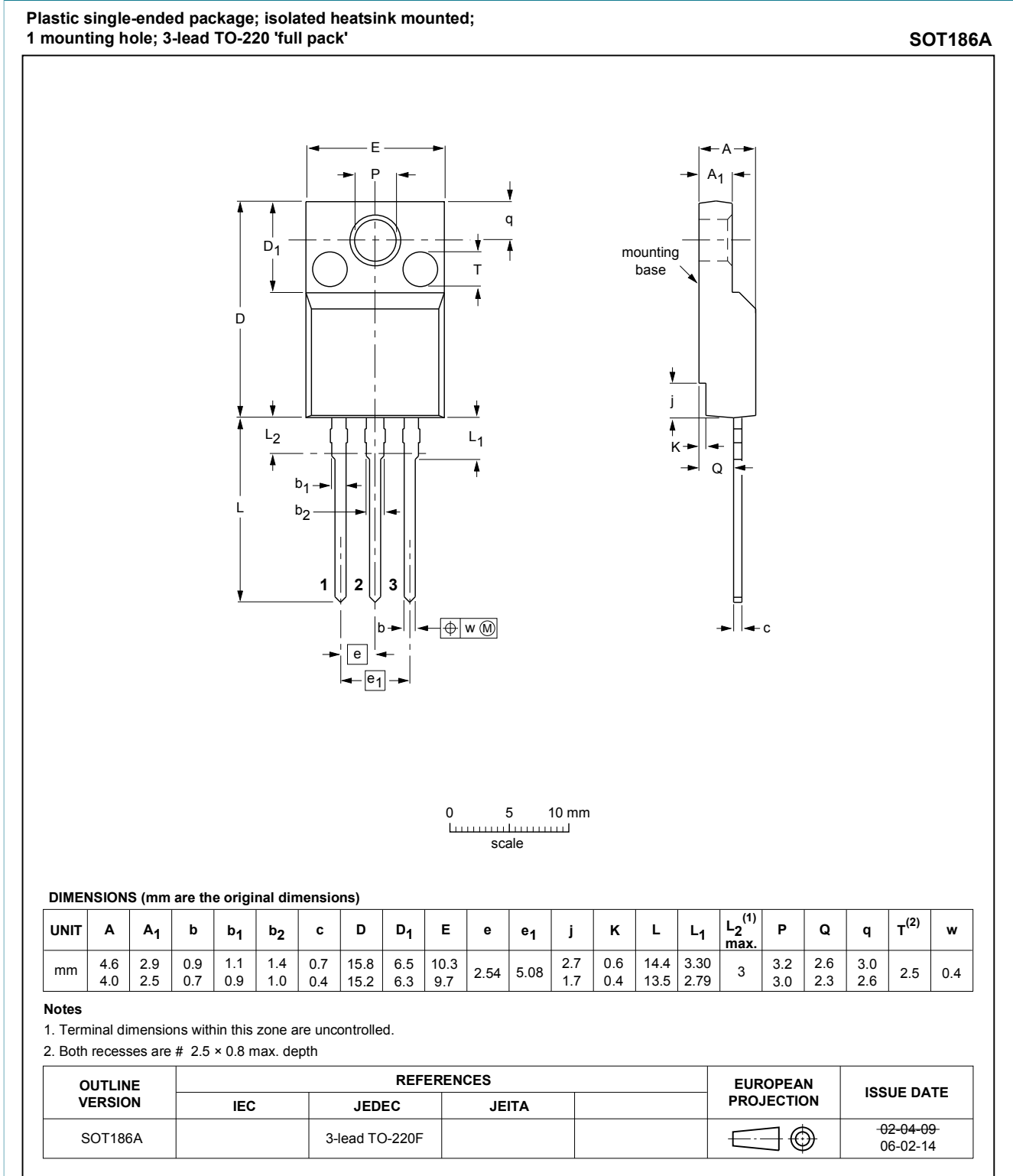


Fig. 12. Package outline TO-220F (SOT186A)

12. Legal information

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| Document status [1][2] | Product status [3] | Definition |
|--------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
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- [2] The term 'short data sheet' is explained in section "Definitions".
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