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1. Global joint venture starts operations as WeEn Semiconductors

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Thank you for your cooperation and understanding,

WeEn Semiconductors



SCR, 12 A, 15mA, 650 V, SOT78

Rev. 05 — 27 February 2009

Product data sheet

1. Product profile

1.1 General description

Planar passivated SCR (Silicon Controlled Rectifier) in a SOT78 plastic package.

1.2 Features and benefits

High reliability

High thermal cycling performance

Protection Circuits

Static switching

High surge current capability

1.3 Applications

- Ignition circuits
- Motor control

1.4 Quick reference data

Table 1.	Quick reference					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	-	650	V
I _{T(AV)}	average on-state current	half sine wave; T _{mb} ≤ 109 °C; see <u>Figure 3</u>	-	-	7.5	A
I _{T(RMS)}	RMS on-state current	half sine wave; T _{mb} ≤ 109 °C; see <u>Figure 1</u> ; see <u>Figure 2</u>	-	-	12	A
Static ch	aracteristics					
I _{GT}	gate trigger current	V _D = 12 V; T _j = 25 °C; I _T = 100 mA; see <u>Figure 8</u>	-	2	15	mA



2. Pinning information

Table 2.	Pinning	information	
Pin	Symbol	Description	Simplified outline Graphic symbol
1	К	cathode	
2	А	anode	
3	G	gate	G sym037
mb	mb	anode	
			SOT78

(TO-220AB;SC-46)

3. Ordering information

Table 3. Ordering information

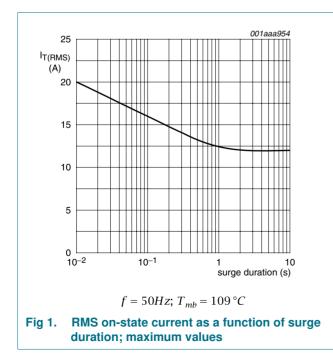
Type number	Package				
	Name	Description	Version		
BT151-650R	TO-220AB; SC-46	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78		

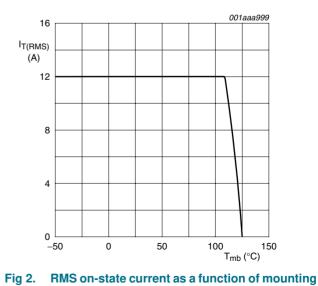
4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

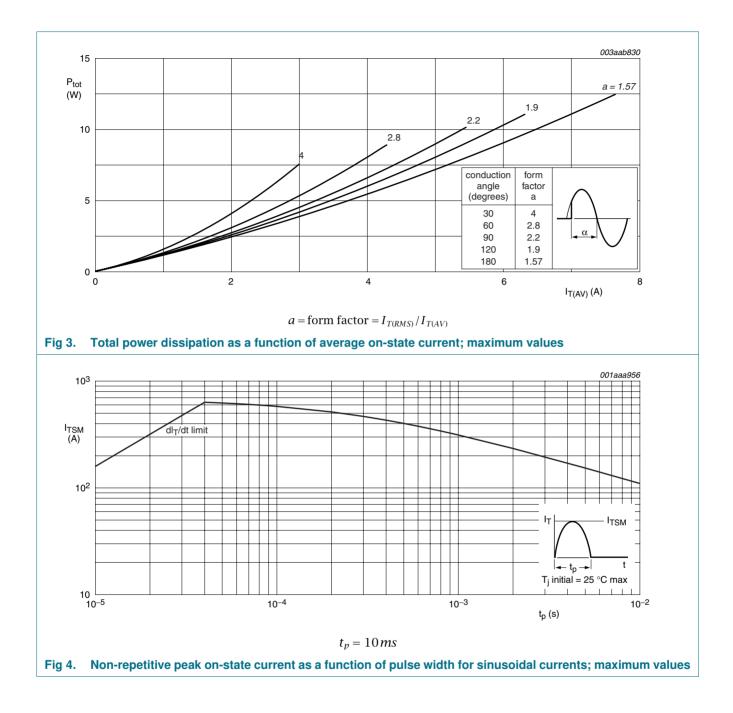
Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	650	V
V _{RRM}	repetitive peak reverse voltage		-	650	V
I _{T(AV)}	average on-state current	half sine wave; $T_{mb} \le 109 \text{ °C}$; see Figure 3	-	7.5	Α
I _{T(RMS)}	RMS on-state current	half sine wave; T _{mb} ≤ 109 °C; see <u>Figure 1</u> ; see <u>Figure 2</u>	-	12	Α
dl _T /dt	rate of rise of on-state current	I_T = 20 A; I_G = 50 mA; dI_G/dt = 50 mA/µs	-	50	A/µs
I _{GM}	peak gate current		-	2	А
P _{GM}	peak gate power		-	5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C
I _{TSM}	non-repetitive peak	half sine wave; $t_p = 8.3 \text{ ms}$; $T_{j(init)} = 25 \text{ °C}$	-	132	А
	on-state current	half sine wave; $t_p = 10 \text{ ms}$; $T_{j(init)} = 25 \text{ °C}$; see Figure 4; see Figure 5	-	120	A
l ² t	I2t for fusing	t _p = 10 ms; sine-wave pulse	-	72	A ² s
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
V _{RGM}	peak reverse gate voltage		-	5	V



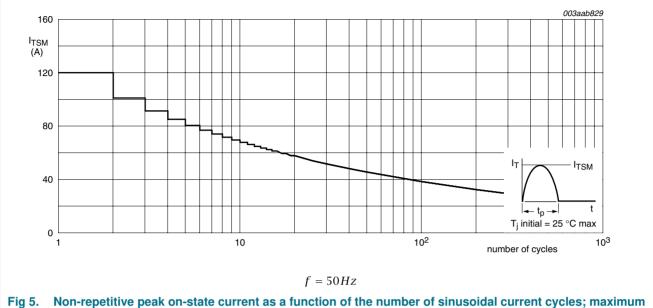


base temperature; maximum values

SCR, 12 A, 15mA, 650 V, SOT78



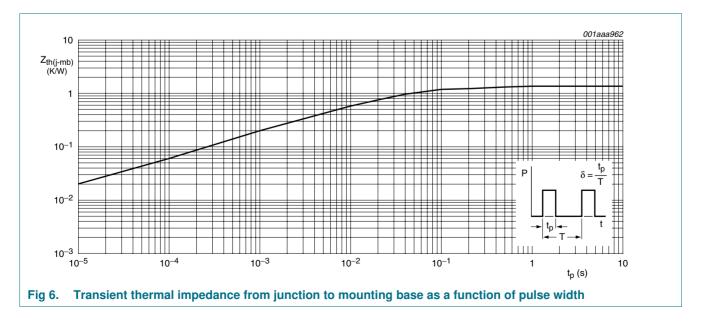
SCR, 12 A, 15mA, 650 V, SOT78



values

5. Thermal characteristics

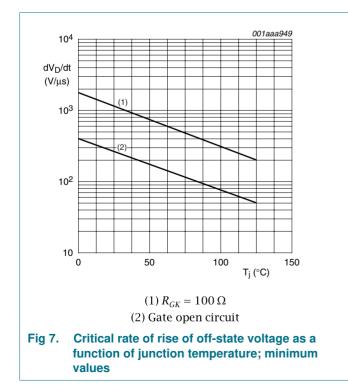
Table 5.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance from junction to mounting base	see <u>Figure 6</u>	-	-	1.3	K/W
R _{th(j-a)}	thermal resistance from junction to ambient free air		-	60	-	K/W



BT151-650R_5

6. Characteristics

Table 6.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	aracteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; \text{ T}_j = 25 \text{ °C}; \text{ I}_T = 100 \text{ mA}; \text{ see} $ Figure 8	-	2	15	mA
IL	latching current	$V_D = 12 \text{ V}; \text{ T}_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 9}}{1000 \text{ Figure 9}}$	-	10	40	mA
I _H	holding current	$V_D = 12 \text{ V}; \text{ T}_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure } 10}{10}$	-	7	20	mA
V _T	on-state voltage	I _T = 23 A; T _j = 25 °C; see <u>Figure 11</u>	-	1.4	1.75	V
V _{GT}	gate trigger voltage	I_T = 100 mA; V_D = 12 V; T_j = 25 °C; see <u>Figure 12</u>	-	0.6	1.5	V
		$I_T = 100 \text{ mA}; V_D = 650 \text{ V}; T_j = 125 \text{ °C}$	0.25	0.4	-	V
I _D	off-state current	$V_D = 650 \text{ V}; \text{ T}_j = 125 \text{ °C}$	-	0.1	0.5	mA
I _R	reverse current	$V_R = 650 \text{ V}; \text{ T}_j = 125 \text{ °C}$	-	0.1	0.5	mA
Dynamic	characteristics					
dV_D/dt	rate of rise of off-state voltage	V_{DM} = 435 V; T _j = 125 °C; exponential waveform; gate open circuit	50	130	-	V/µs
		V_{DM} = 435 V; T _j = 125 °C; R _{GK} = 100 Ω; exponential waveform; see Figure 7	200	1000	-	V/µs
t _{gt}	gate-controlled turn-on time	$\label{eq:ITM} \begin{array}{l} I_{TM} = 40 \text{ A}; V_D = 650 \text{ V}; I_G = 100 \text{ mA}; \\ \text{d} I_G/\text{d} t = 5 A/\mu\text{s}; T_j = 25 ^\circ\text{C} \end{array}$	-	2	-	μs
t _q	commutated turn-off time	$ \begin{split} &V_{DM} = 435 \text{ V}; \ T_{j} = 125 \ ^{\circ}\text{C}; \ I_{TM} = 20 \text{ A}; \\ &V_{R} = 25 \text{ V}; \ (dI_{T}/dt)_{M} = 30 \text{ A}/\mu s; \\ &dV_{D}/dt = 50 \text{ V}/\mu s; \ R_{GK} = 100 \ \Omega \end{split} $	-	70	-	μs



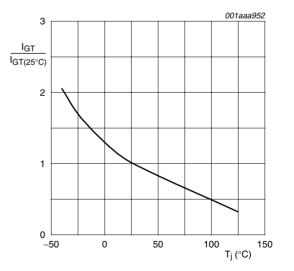
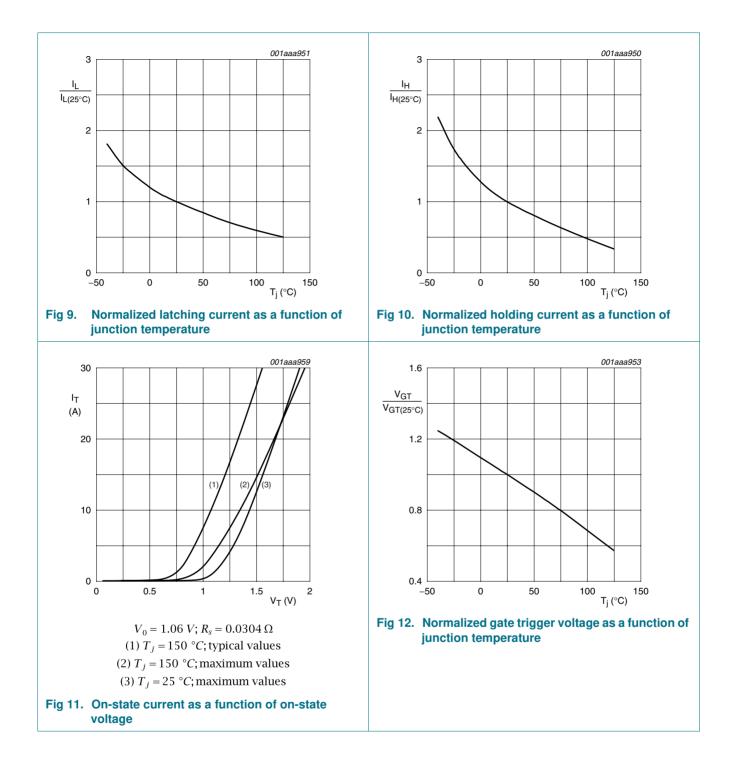


Fig 8. Normalized gate trigger current as a function of junction temperature

SCR, 12 A, 15mA, 650 V, SOT78



SCR, 12 A, 15mA, 650 V, SOT78

7. Package outline

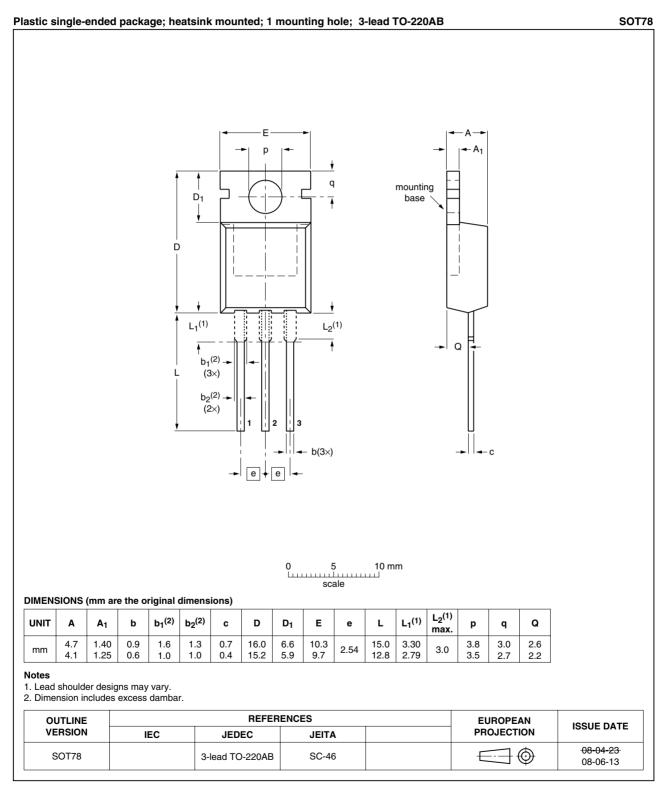


Fig 13. Package outline SOT78 (TO-220AB)

8. Revision history

ory			
Release date	Data sheet status	Change notice	Supersedes
20090227	Product data sheet	-	BT151_SER_L_R_4
0	•	from data sheet BT151_S	SERIB4
	•	-	BT151 SERIES 3
20040607	Product specification	-	BT151_SERIES_2
19990601	Product specification	-	BT151_SERIES_1
19970901	Product specification	-	-
	Release date 20090227 • Package ou • Type number 20061023 20040607 19990601	Release dateData sheet status20090227Product data sheet• Package outline updated.• Type number BT151-650R separated20061023Product data sheet20040607Product specification19990601Product specification	Release date Data sheet status Change notice 20090227 Product data sheet - • Package outline updated. - - • Type number BT151-650R separated from data sheet BT151_S - 20061023 Product data sheet - 20040607 Product specification - 19990601 Product specification -

9. Legal information

9.1 Data sheet status

Document status [1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions"

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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