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1. Global joint venture starts operations as WeEn Semiconductors

Dear customer.

As from November 9th, 2015 NXP Semiconductors N.V. and Beijing JianGuang Asset Management Co. Ltd established Bipolar Power joint venture (JV), **WeEn Semiconductors**, which will be used in future Bipolar Power documents together with new contact details.

In this document where the previous NXP references remain, please use the new links as shown below.

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Thank you for your cooperation and understanding,

WeEn Semiconductors



DISCRETE SEMICONDUCTORS

DATA SHEET

BT151 series C Thyristors

Product specification

April 2004



NXP Semiconductors Product specification

Thyristors BT151 series C

GENERAL DESCRIPTION

Passivated thyristors in a plastic envelope, intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

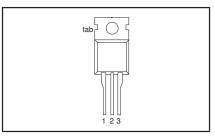
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V _{DRM} , V _{RRM} I _{T(AV)} I _{T(RMS)} I _{TSM}	BT151- Repetitive peak off-state voltages Average on-state current RMS on-state current Non-repetitive peak on-state current	500C 500 7.5 12 100	650C 650 7.5 12 100	800C 800 7.5 12 100	V A A

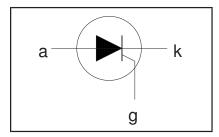
PINNING - TO220AB

PIN	DESCRIPTION			
1	cathode			
2	anode			
3	gate			
tab anode				

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 60134).

SYMBO L	PARAMETER	CONDITIONS	MIN.	MAX.		UNIT	
V _{DRM} ,	Repetitive peak off-state voltages		-	-500C 500 ¹	-650C 650 ¹	-800C 800	V
I _{T(AV)}	Average on-state	half sine wave; T _{mb} ≤ 109 °C	-	7.5			Α
I _{T(RMS)} I _{TSM}	current RMS on-state current Non-repetitive peak on-state current	all conduction angles half sine wave; T _j = 25 °C prior to surge	-	12			А
	on-state current	t = 10 ms t = 8.3 ms	-	100 110		A A	
l²t dl _⊤ /dt	I ² t for fusing Repetitive rate of rise of on-state current after triggering	t = 10 ms	-	50 50		A ² s A/μs	
I _{GM} V _{GM} V _{RGM}	Peak gate current Peak gate voltage Peak reverse gate		- - -	2 5 5		A V V	
$\begin{array}{c} P_{GM} \\ P_{G(AV)} \\ T_{stg} \\ T_j \end{array}$	voltage Peak gate power Average gate power Storage temperature Operating junction temperature	over any 20 ms period	- -40 -	5 0.5 150 125		°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°°	

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15 $A/\mu s$.

NXP Semiconductors Product specification

Thyristors BT151 series C

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-mb}	Thermal resistance		-	-	1.3	K/W
R _{th j-a}	junction to mounting base Thermal resistance junction to ambient	in free air	-	60	-	K/W

STATIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$	-	2	15	mA
l I	Latching current	$V_{\rm D}^{\rm S} = 12 \text{ V}; I_{\rm GT}^{\rm T} = 0.1 \text{ A}$	-	10	40	mA
l _H	Holding current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$	-	7	20	mA
Ϋ́Τ	On-state voltage	$I_{T} = 23 \text{ A}$	-	1.44	1.75	V
V _{GT}	Gate trigger voltage	$\dot{V}_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$	-	0.6	1.5	V
		$V_D = V_{DRM(max)}$; $I_T = 0.1 A$; $T_j = 125 °C$	0.25	0.4	-	V
I _D , I _R	Off-state leakage current	$V_D = V_{DRM(max)}^{Station (max)}; V_R = V_{RRM(max)}; T_i = 125 °C$	-	0.1	0.5	mΑ

DYNAMIC CHARACTERISTICS

 $T_i = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% \ V_{DRM(max)}; \ T_j = 125 \ ^{\circ}C;$ exponential waveform; Gate open circuit $R_{GK} = 100 \ \Omega$	50 200	130 1000		V/μs V/μs
t _q	Gate controlled turn-on time Circuit commutated turn-off time	$\begin{array}{l} I_{TM} = 40 \text{ A; } V_D = V_{DRM(max)}; \ I_G = \widetilde{0}.1 \text{ A; } \\ dI_G/dt = 5 \text{ A/}\mu\text{s} \\ V_D = 67\% \ V_{DRM(max)}; \ T_j = 125 \ ^{\circ}\text{C; } \\ I_{TM} = 20 \text{ A; } V_R = 25 \text{ V; } dI_{TM}/dt = 30 \text{ A/}\mu\text{s; } \\ dV_D/dt = 50 \text{ V/}\mu\text{s; } R_{GK} = 100 \ \Omega \end{array}$	-	2 70	-	μs μs

NXP Semiconductors Product specification

Thyristors BT151 series C

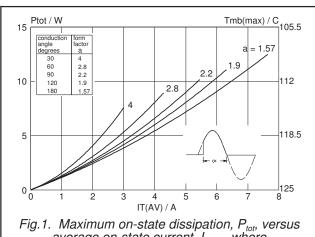


Fig.1. Maximum on-state dissipation, P_{tot} , versus average on-state current, $I_{T(AV)}$, where $a = form \ factor = I_{T(RMS)} / I_{T(AV)}$.

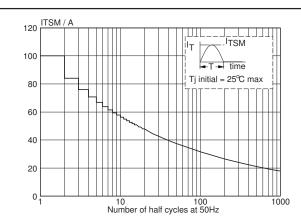


Fig.4. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, f = 50 Hz.

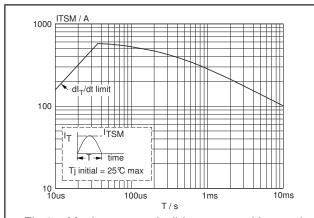


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_n \le 10$ ms.

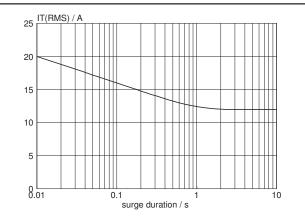


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50 Hz; $T_{mb} \le 109^{\circ}\text{C}$.

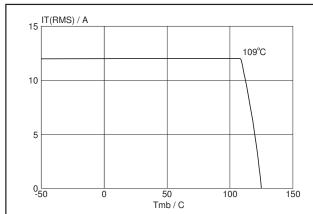
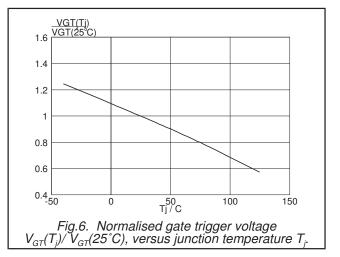
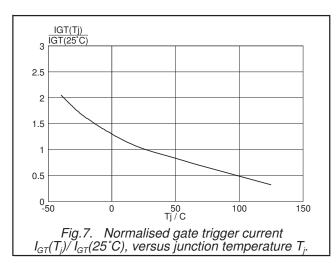


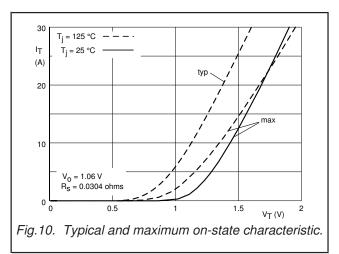
Fig.3. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

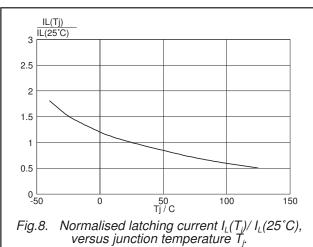


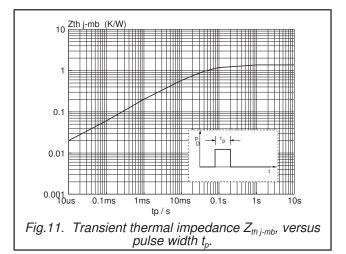
NXP Semiconductors Product specification

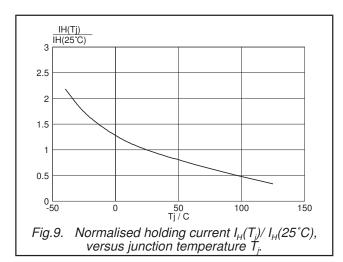
Thyristors BT151 series C

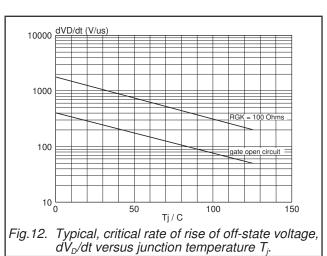








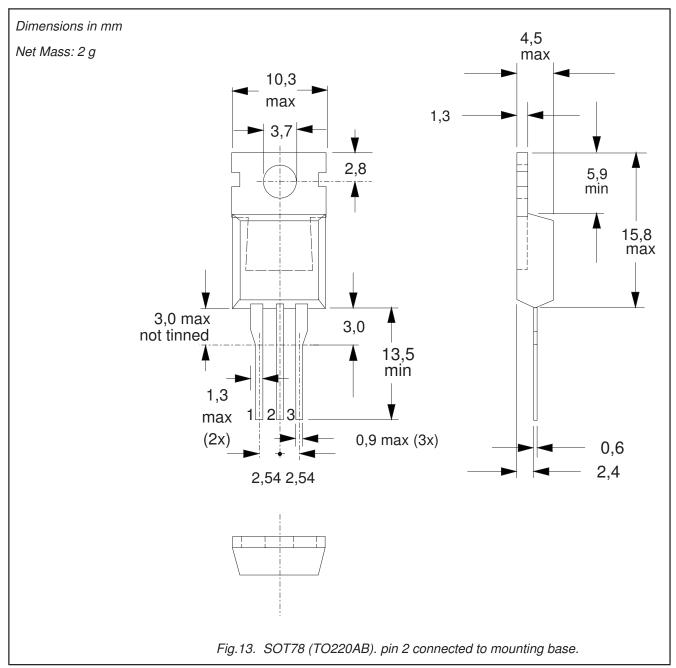




NXP Semiconductors Product specification

Thyristors BT151 series C

MECHANICAL DATA



- Refer to mounting instructions for SOT78 (TO220) envelopes.
 Epoxy meets UL94 V0 at 1/8".

Legal information

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet Production This document contains the product specification.		This document contains the product specification.

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Contact information

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