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1. Global joint venture starts operations as WeEn Semiconductors

Dear customer,

As from November 9th, 2015 NXP Semiconductors N.V. and Beijing JianGuang Asset Management Co. Ltd established Bipolar Power joint venture (JV), **WeEn Semiconductors**, which will be used in future Bipolar Power documents together with new contact details.

In this document where the previous NXP references remain, please use the new links as shown below.

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Thank you for your cooperation and understanding,

WeEn Semiconductors





1. General description

Planar passivated high commutation three quadrant triac in a SOT78 (TO-220AB) plastic package intended for use in circuits where high static and dynamic dV/dt and high dl/dt can occur. This "series C" triac will commutate the full RMS current at the maximum rated junction temperature without the aid of a snubber.

2. Features and benefits

- 3Q technology for improved noise immunity
- High commutation capability with maximum false trigger immunity
- High voltage capability
- Less sensitive gate for high noise immunity
- Planar passivated for voltage ruggedness and reliability
- Triggering in three quadrants only
- Very high immunity to false turn-on by dV/dt

3. Applications

- Electronic thermostats (heating and cooling)
- High power motor controls e.g. washing machines and vacuum cleaners
- Rectifier-fed DC inductive loads e.g. DC motors and solenoids

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{DRM}	repetitive peak off- state voltage		-	-	800	V
I _{TSM}	non-repetitive peak on- state current	full sine wave; T _{j(init)} = 25 °C; t _p = 20 ms; <u>Fig. 4; Fig. 5</u>	-	-	100	A
I _{T(RMS)}	RMS on-state current	full sine wave; T _{mb} ≤ 100 °C; <u>Fig. 1;</u> <u>Fig. 2; Fig. 3</u>	-	-	12	A
Static chara	acteristics					
I _{GT}	gate trigger current	V _D = 12 V; I _T = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 7</u>	2	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 7</u>	2	-	35	mA





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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _i = 25 °C; <u>Fig. 7</u>	2	-	35	mA
		$r_j = 20^{\circ} \text{ O}, \frac{r_{19}}{2}$				

5. Pinning information

Table 2.	Pinning	information	<u>.</u>	
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	T1	main terminal 1	mb	T2-T1
2	T2	main terminal 2		G sym051
3	G	gate		
mb	T2	mounting base; main terminal 2		
			TO-220AB (SOT78)	

6. Ordering information

Table 3.Ordering information

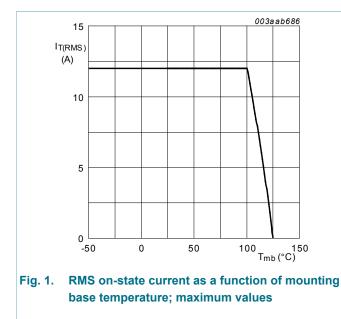
Type number	Package	ckage					
	Name	Description	Version				
BTA312-800C	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78				
BTA312-800C/DG	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78				

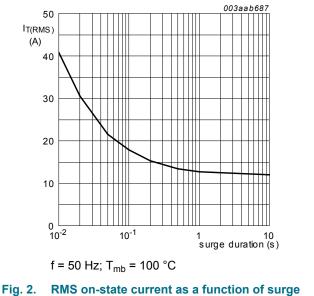
7. Limiting values

Table 4.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

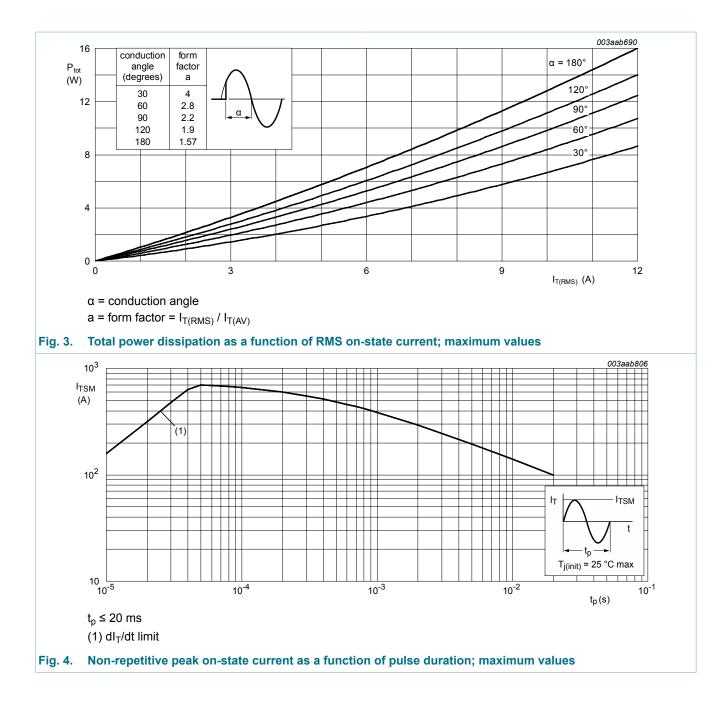
Symbol	Parameter	Conditions	Min	Max	Unit
V _{DRM}	repetitive peak off-state voltage		-	800	V
I _{T(RMS)}	RMS on-state current	full sine wave; $T_{mb} \le 100 \text{ °C}$; Fig. 1; Fig. 2; Fig. 3	-	12	A
I _{TSM}	non-repetitive peak on-state current	full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 20 \text{ ms}; \text{ Fig. 4}; \text{ Fig. 5}$	-	100	A
		full sine wave; $T_{j(init)} = 25 \text{ °C};$ $t_p = 16.7 \text{ ms}$	-	110	A
l ² t	I ² t for fusing	t _p = 10 ms; SIN	-	50	A ² s
dI _T /dt	rate of rise of on-state current	I_{T} = 20 A; I_{G} = 0.2 A; dI_{G}/dt = 0.2 A/µs	-	100	A/µs
I _{GM}	peak gate current		-	2	А
P _{GM}	peak gate power		-	5	W
P _{G(AV)}	average gate power	over any 20 ms period	-	0.5	W
T _{stg}	storage temperature		-40	150	°C
Tj	junction temperature		-	125	°C





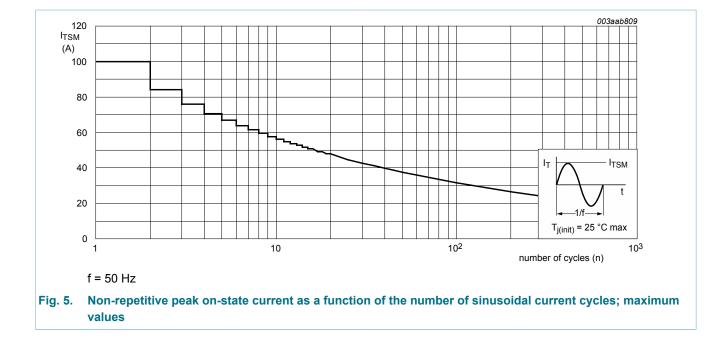
duration; maximum values

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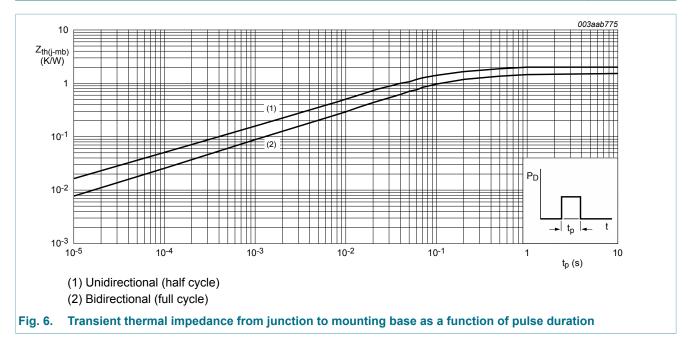
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8. Thermal characteristics

Table 5. The	rmal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-mb)}	thermal resistance	full cycle; <u>Fig. 6</u>	-	-	1.5	K/W
	from junction to mounting base	half cycle; <u>Fig. 6</u>	-	-	2	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	-	60	-	K/W



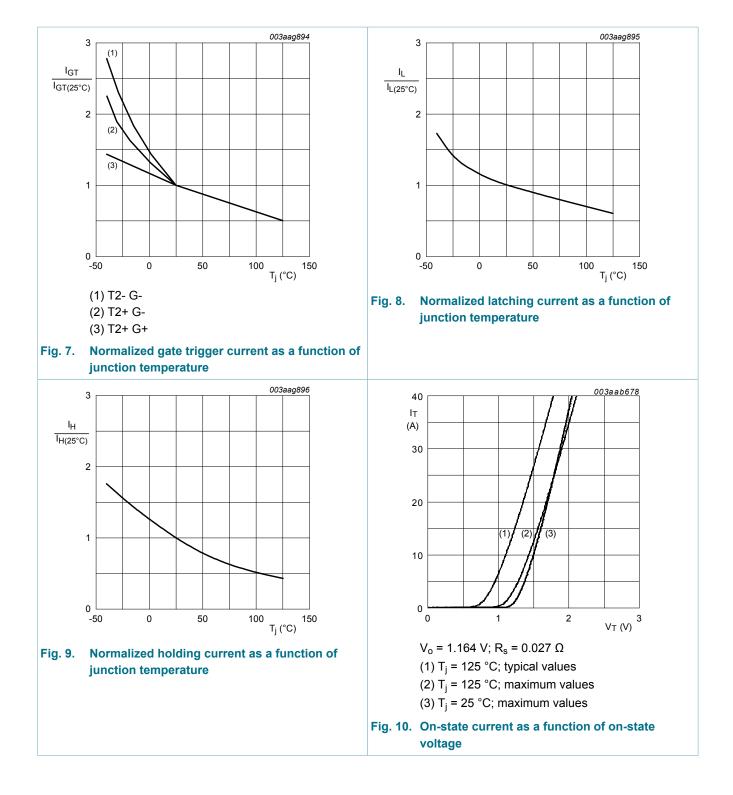
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9. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static char	acteristics					
I _{GT}	gate trigger current	$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T2+ G+};$ $T_j = 25 \text{ °C}; \text{ Fig. 7}$	2	-	35	mA
		$V_D = 12 \text{ V}; \text{ I}_T = 0.1 \text{ A}; \text{ T2+ G-};$ $T_j = 25 ^\circ\text{C}; \text{ Fig. 7}$	2	-	35	mA
		V _D = 12 V; I _T = 0.1 A; T2- G-; T _j = 25 °C; <u>Fig. 7</u>	2	-	35	mA
IL	latching current	V _D = 12 V; I _G = 0.1 A; T2+ G+; T _j = 25 °C; <u>Fig. 8</u>	-	-	50	mA
		V _D = 12 V; I _G = 0.1 A; T2+ G-; T _j = 25 °C; <u>Fig. 8</u>	-	-	60	mA
		$V_D = 12 \text{ V}; \text{ I}_G = 0.1 \text{ A}; \text{ T2- G-};$ T _j = 25 °C; Fig. 8	-	-	50	mA
I _H	holding current	V _D = 12 V; T _j = 25 °C; <u>Fig. 9</u>	-	-	35	mA
V _T	on-state voltage	I _T = 15 A; T _j = 25 °C; <u>Fig. 10</u>	-	1.3	1.6	V
V _{GT}	gate trigger voltage	V _D = 12 V; I _T = 0.1 A; T _j = 25 °C; Fig. 11	-	0.8	1	V
		V _D = 400 V; I _T = 0.1 A; T _j = 125 °C; <u>Fig. 11</u>	0.25	0.4	-	V
I _D	off-state current	V _D = 800 V; T _j = 125 °C	-	0.1	0.5	mA
Dynamic cl	naracteristics	· · · · ·				
dV _D /dt	rate of rise of off-state voltage	V_{DM} = 536 V; T _j = 125 °C; (V _{DM} = 67% of V _{DRM}); exponential waveform; gate open circuit	500	-	-	V/µs
dl _{com} /dt	rate of change of commutating current	V_D = 400 V; T_j = 125 °C; $I_{T(RMS)}$ = 12 A; dV _{com} /dt = 20 V/µs; (snubberless condition); gate open circuit	20	-	-	A/ms

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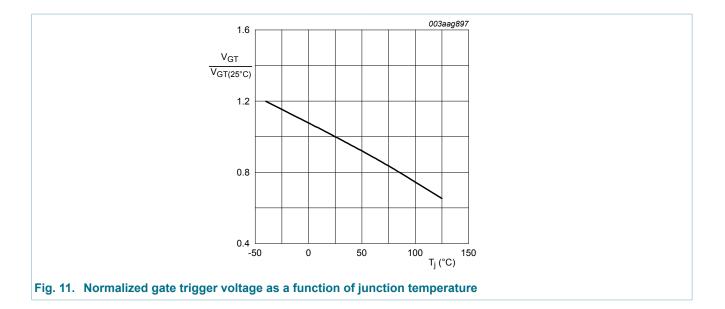
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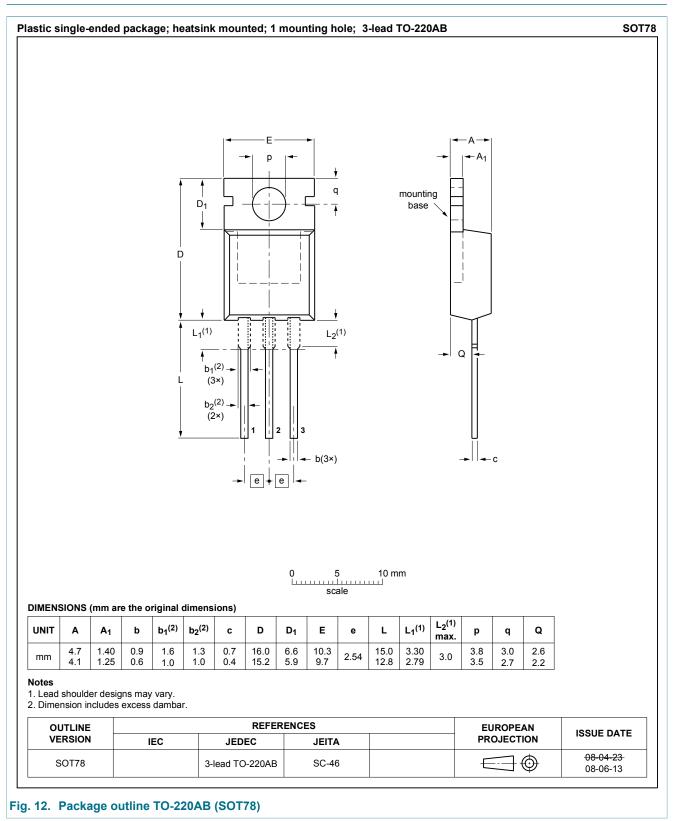
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10. Package outline



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11.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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