# mail

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# CMOS LDO Regulator Series for Portable Equipments Versatile Package FULL CMOS LDO Regulator



Datasheet

### BUxxTD2WNVX series

#### General Description

ROHM

BUxxTD2WNVX series is high-performance FULL CMOS regulator with 200-mA output, which is mounted on versatile package SSON004X1010 (1.00mm  $\times$  1.00 mm  $\times$  0.60mm). It has excellent noise characteristics and load responsiveness characteristics despite its low circuit current consumption of 35µA. It is most appropriate for various applications such as power supplies for logic IC, RF, and camera modules.ROHM's.

#### Features

- High accuracy detection
- Iow current consumption
- Compatible with small ceramic capacitor(Cin=Co=0.47uF)
- With built-in output discharge circuit
- High ripple rejection
- ON/OFF control of output voltage
- With built-in over current protection circuit and thermal shutdown circuit
- Low dropout voltage

### •Key Specifications

Output voltage:	1.0V to 3.4V
Accuracy output voltage:	±1.0% (±25mV)
Low current consumption:	35µA

■ Operating temperature range: -40°C to +85°C

#### Applications

Battery-powered portable equipment, etc.

#### Package

SSON004X1010 :

1.00mm x 1.00mm x 0.60mm



#### Typical Application Circuit

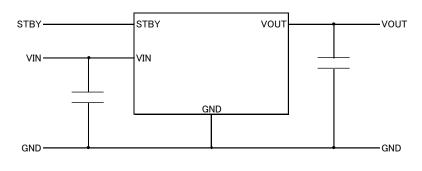
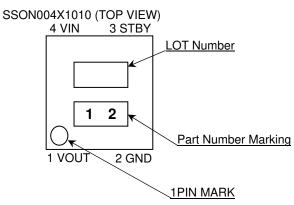


Fig.1 Application Circuit

OProduct structure:Silicon monolithic integrated circuit OThis product is not designed protection against radioactive rays.

#### Connection Diagram



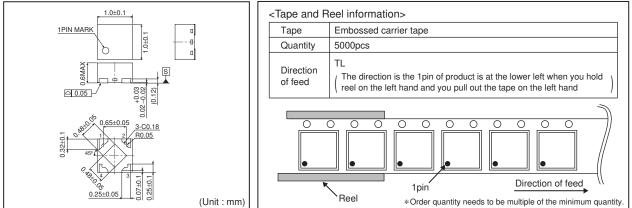
#### Pin Descriptions

	SSON004X1010											
PIN No.	Symbol	Function										
1	VOUT	Output Voltage										
2	GND	Grouding										
3	STBY	ON/OFF control of output voltage (High: ON, Low: OFF)										
4	VIN	Power Supply Voltage										

#### Ordering Information

В	U	х	Х	Т	D	2	W	Ν	V	Х	-	х	Т	L
Part Number		Output Volt 10 : 1.0V ↓ 34 : 3.4V	age	Low Dropo Maximum ( 200mA			with output discharge	Package NVX : SSO	1 DN004X101	D			Packageing and form Embossed tape and TL: The pin numbe	reel

#### SSON004X1010



#### ●Lineup

Marking	А	6	2	В	3	С	D	E	F
Output Voltage	1.0V	1.05V	1.15V	1.2V	1.25V	1.5V	1.8V	1.85V	1.9V
Part Number	BU10	BU1A	BU1B	BU12	BU1C	BU15	BU18	BU1J	BU19
	•								

G	r	0	1	Н	J	K	а	L
2.0V	2.05V	2.1V	2.3V	2.5V	2.6V	2.7V	2.75V	2.8V
BU20	BU2A	BU21	BU23	BU25	BU26	BU27	BU2H	BU28
М	Ν	Р	Q	R	U	Y		
2.85V	2.9V	3.0V	3.1V	3.2V	3.3V	3.4V		

**BU32** 

**BU33** 

**BU34** 

#### ● Absolute Maximum Ratings (Ta=25°C)

BU2J

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VMAX	-0.3 ~ +6.5	V
Power Dissipation	Pd	560(*1)	mW
Maximum junction temperature	TjMAX	+125	°C
Operating Temperature Range	Topr	-40 ~ +85	°C
Storage Temperature Range	Tstg	-55 ~ +125	°C

**BU31** 

**BU30** 

(\*1)Pd deleted at 5.6mW/°C at temperatures above Ta=25°C, mounted on 70×70×1.6 mm glass-epoxy PCB.

#### • RECOMMENDED OPERATING RANGE (not to exceed Pd)

**BU29** 

PARAMETER	Symbol	Limit	Unit
Power Supply Voltage	VIN	1.7~6.0	V
Maximum Output Current	IMAX	200	mA

#### **OPERATING CONDITIONS**

PARAMETER	Symbol	MIN.	TYP.	MAX.	Unit	CONDITION
Input Capacitor	Cin	0. 22 (*2)	0.47	-	μF	Ceramic capacitor recommended
Output Capacitor	Со	0. 22 (*2)	0.47	-	μF	

(\*2) Make sure that the output capacitor value is not kept lower than this specified level across a variety of temperature, DC bias, changing as time progresses characteristic.

#### •Electrical Characteristics

(Ta=25°C, VIN=VOUT+1.0V (\*3), STBY=VIN, Cin=0.47  $\mu$  F, Co=0.47  $\mu$  F, unless otherwise noted.)

DADAMETED				Limit			
PARAMETER		Symbol	MIN.	TYP.	MAX.	Unit	Conditions
Overall Device							
Output Voltage		VOUT	VOUT × 0.99	VOUT	VOUT × 1.01	v	IOUT=10 $\mu$ A, VOUT $\geq$ 2.5V
Output Voltage		0001	VOUT-25mV	001	VOUT+25mV	v	IOUT=10 μ A, VOUT<2.5V
Operating Current		IIN	_	35	60	μA	IOUT=0mA
Operating Current (STB)	()	ISTBY	-	-	1.0	μA	STBY=0V
Ripple Rejection Ratio		RR	45	70	-	dB	VRR=-20dBv, fRR=1kHz, IOUT=10mA
			-	280	540	mV	2.5V≦VOUT≦2.6V (VIN=0.98*VOUT, IOUT=200mA)
Dropout Voltage		VSAT	-	260	500	mV	2.7V≦VOUT≦2.85V (VIN=0.98*VOUT, IOUT=200mA)
Dropout voltage			-	240	460	mV	2.9V≦VOUT≦3.1V (VIN=0.98*VOUT, IOUT=200mA)
			-	220	420	mV	3.2V≦VOUT≦3.4V (VIN=0.98*VOUT, IOUT=200mA)
Line Regulation		VDL	- 2		20	mV	VIN=VOUT+1.0V to 5.5V (*4), IOUT=10 $\mu$ A
Load Regulation		VDLO	_	10	80	mV	IOUT=0.01mA to 100mA
Over Current Prote	ction (	OCP)					
Limit Current		ILMAX	220	400	700	mA	Vo=VOUT*0.95
Short Current		ISHORT	20	70	150	mA	Vo=0V
Standby Block							
Discharge Resistor		RDSC	20	50	80	Ω	VIN=4.0V, STBY=0V, VOUT=4.0V
STBY Pin Pull-down Cu	rrent	ISTB	0.1	0.6	2.0	μA	STBY=1.5V
STBY Control Voltage	ON	VSTBH	1.2	-	5.5	V	
STET Control voltage	OFF	VSTBL	-0.3	-	0.3	V	

This product is not designed for protection against radioactive rays.

(\*3) VIN=2.5V for VOUT  $\leq 1.5V$ 

(\*4) VIN=2.5V to 3.6V for VOUT  $\leq\!1.5V$ 

#### ●ELECTRICAL CHARACTERISTICS of each Output Voltage

(Ta=25°C, STBY=VIN, Cin=0.47  $\mu$  F, Co=0.47  $\mu$  F, unless otherwise noted.)

Output Voltage	PARAMETER	MIN.	TYP.	MAX.	Unit	Conditions
1. 0V, 1. 15V, 1. 2V, 1. 25V		80	160	-		VIN=1.7V
1.00, 1.130, 1.20	Maximum	200	-	-		VIN=2.1V
1. 5V		60	120	-	mA	VIN=1.8V
1.5V	output current	200	-	-		VIN=2.2V
1. 8V, 1. 85V, 1. 9V, 2. 0V, 2. 05V, 2. 1V, 2. 3V		200	-	_		VIN=VOUT+0.6V

#### Block Diagrams

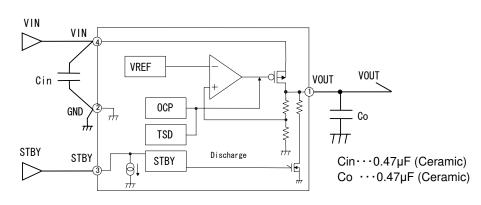
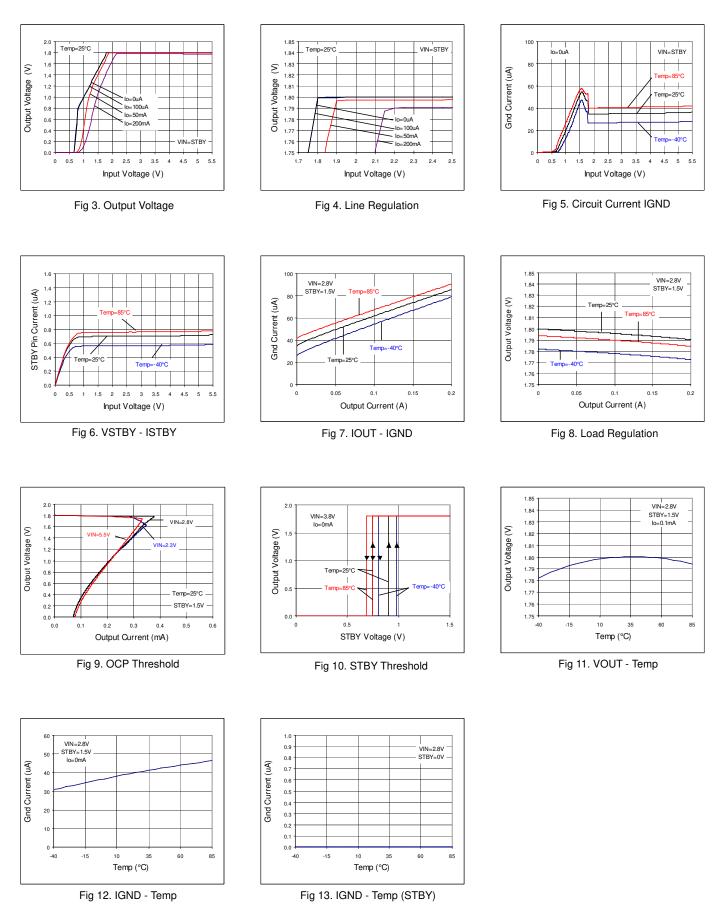
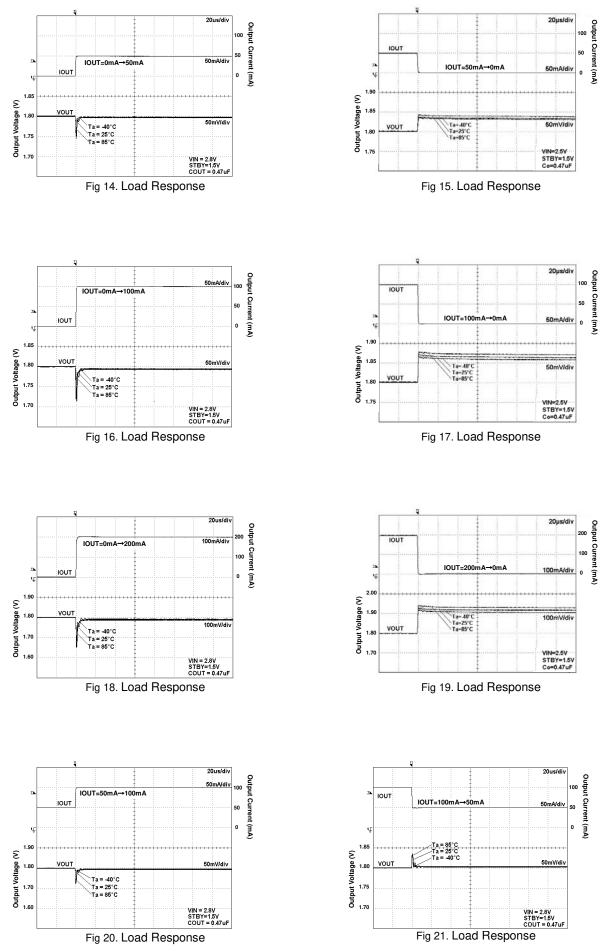


Fig. 2 Block Diagrams

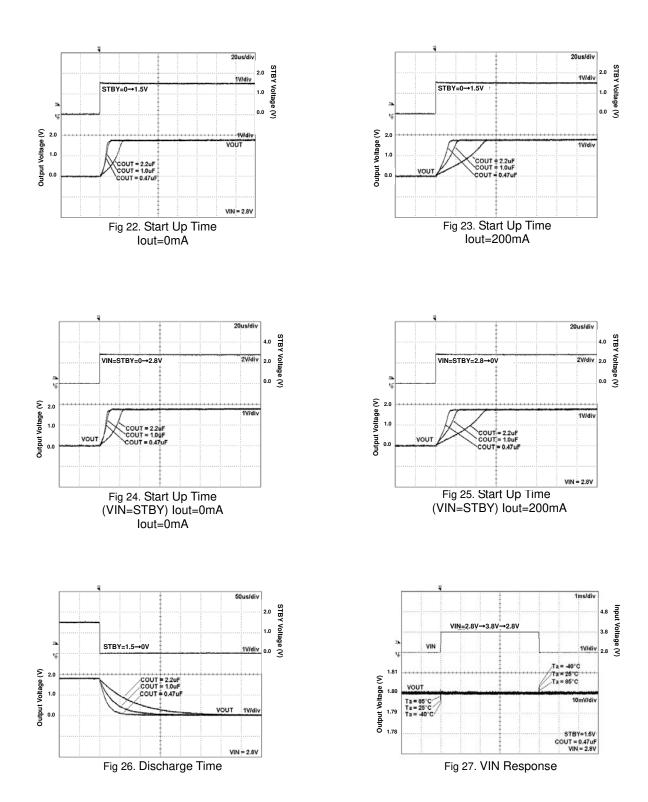
### •Reference data BU18TD2WNVX (Ta=25°C unless otherwise specified.)



# •Reference data BU18TD2WNVX (Ta=25°C unless otherwise specified.)



## ●Reference data BU18TD2WNVX (Ta=25°C unless otherwise specified.)



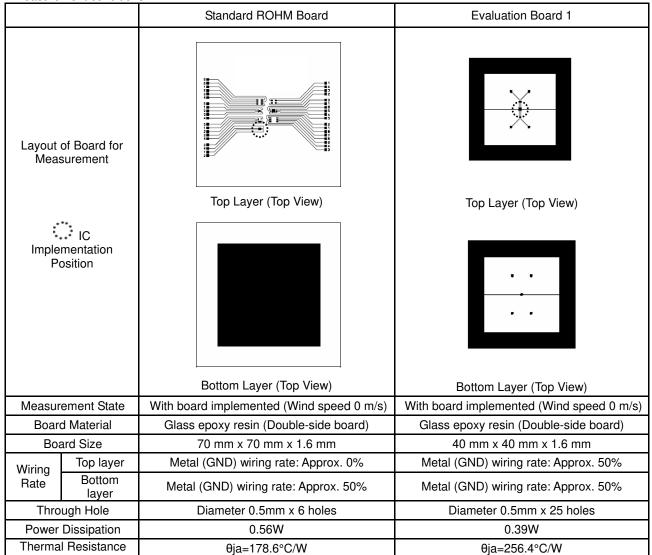
#### About power dissipation (Pd)

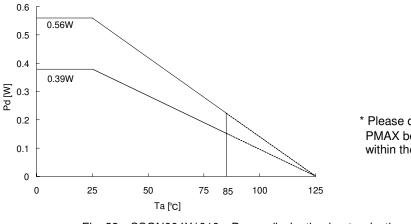
As for power dissipation, an approximate estimate of the heat reduction characteristics and internal power consumption of IC are shown, so please use these for reference. Since power dissipation changes substantially depending on the implementation conditions (board size, board thickness, metal wiring rate, number of layers and through holes, etc.), it is recommended to measure Pd on a set board. Exceeding the power dissipation of IC may lead to deterioration of the original IC performance, such as causing operation of the thermal shutdown circuit or reduction in current capability. Therefore, be sure to prepare sufficient margin within power dissipation for usage.

Calculation of the maximum internal power consumption of IC (PMAX)

PMAX=(VIN-VOUT)×IOUT(MAX.) (VIN: Input voltage VOUT: Output voltage IOUT(MAX): Maximum output current)

• Measurement conditions





\* Please design the margin so that PMAX becomes is than Pd (PMAX<Pd) within the usage temperature range

Fig. 28 SSON004X1010 Power dissipation heat reduction characteristics (Reference)

#### Operation Notes

#### 1.) Absolute maximum ratings

Use of the IC in excess of absolute maximum ratings (such as the input voltage or operating temperature range) may result in damage to the IC. Assumptions should not be made regarding the state of the IC (e.g., short mode or open mode) when such damage is suffered. If operational values are expected to exceed the maximum ratings for the device, consider adding protective circuitry (such as fuses) to eliminate the risk of damaging the IC.

#### 2.) GND potential

The potential of the GND pin must be the minimum potential in the system in all operating conditions.

Never connect a potential lower than GND to any pin, even if only transiently.

#### 3.) Thermal design

Use a thermal design that allows for a sufficient margin for that package power dissipation rating (Pd) under actual operating conditions

#### 4.) Inter-pin shorts and mounting errors

Use caution when orienting and positioning the IC for mounting on printed circuit boards. Improper mounting or shorts between pins may result in damage to the IC.

#### 5.) Operation in strong electromagnetic fields

Strong electromagnetic fields may cause the IC to malfunction. Caution should be exercised in applications where strong electromagnetic fields may be present.

#### 6.) Common impedance

Wiring traces should be as short and wide as possible to minimize common impedance. Bypass capacitors should be use to keep ripple to a minimum.

#### 7.) Voltage of STBY pin

To enable standby mode for all channels, set the STBY pin to 0.3 V or less, and for normal operation, to 1.2 V or more. Setting STBY to a voltage between 0.3 and 1.2 V may cause malfunction and should be avoided. Keep transition time between high and low (or vice versa) to a minimum.

Additionally, if STBY is shorted to VIN, the IC will switch to standby mode and disable the output discharge circuit, causing a temporary voltage to remain on the output pin. If the IC is switched on again while this voltage is present, overshoot may occur on the output. Therefore, in applications where these pins are shorted, the output should always be completely discharged before turning the IC on.

#### 8.) Over-current protection circuit (OCP)

This IC features an integrated over-current and short-protection circuitry on the output to prevent destruction of the IC when the output is shorted. The OCP circuitry is designed only to protect the IC from irregular conditions (such as motor output shorts) and is not designed to be used as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

#### 9.) Thermal shutdown circuit (TSD)

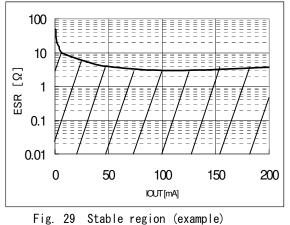
This IC also features a thermal shutdown circuit that is designed to turn the output off when the junction temperature of the IC exceeds about 150°C. This feature is intended to protect the IC only in the event of thermal overload and is not designed to guarantee operation or act as an active security device for the application. Therefore, applications should not be designed under the assumption that this circuitry will engage.

#### 10.) Input/output capacitor

Capacitors must be connected between the input/output pins and GND for stable operation, and should be physically mounted as close to the IC pins as possible. The input capacitor helps to counteract increases in power supply impedance, and increases stability in applications with long or winding power supply traces. The output capacitance value is directly related to the overall stability and transient response of the regulator, and should be set to the largest pUnstable regionor the application to increase these characteristics. During design, keep in mind that in general, ceramic capacitors have a wide range of tolerances, temperature coefficients and DC bias characteristics, and that their capacitance values tend to decrease over time. Confirm these details before choosing appropriate capacitors for your application. (Please refer the technical note, regarding ceramic capacitor of recommendation) Cout=0.47  $\mu$  F, Cin=0.47  $\mu$  F, Temp=+25°C

#### 11.) About the equivalent series resistance (ESR) of a ceramic capacitor

Capacitors generally have ESR (equivalent series resistance) and it operates stably in the ESR-IOUT area shown on the right. Since ceramic capacitors, tantalum capacitors, electrolytic capacitors, etc. generally have different ESR, please check the ESR of the capacitor to be used and use it within the stability area range shown in the right graph for evaluation of the actual application.



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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4) The Products are not subject to radiation-proof design.
- 5) Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6) In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse) is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7) De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8) Confirm that operation temperature is within the specified range described in the product specification.
- 9) ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1) When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2) In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1) Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
    - [b] the temperature or humidity exceeds those recommended by ROHM
    - [c] the Products are exposed to direct sunshine or condensation
    - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3) Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4) Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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