imall

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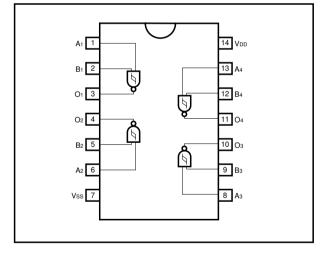
Quad 2-input NAND Schmitt trigger BU4093B / BU4093BF / BU4093BFV

The BU4093B, BU4093BF, and BU4093BFV are 4-circuit, 2-input NAND gates whose input pins all have a Schmitt trigger function.

As the circuit threshold voltages are different when the input waveform rises and when it falls (V_{IH} , V_{IL}), they can be used for line receivers, waveform rectification, multivibrators, and other purposes in addition to the customary usage as a NAND gate. They may be used in place of the BU4011B which uses the same pin connection.

- Features
- 1) Low power dissipation.
- 2) Wide range of operating power supply voltages.
- 3) High input impedance.

- 4) High fan-out.
- 5) Direct drive of 2 L-TTL inputs and 1 LS-TTL input.



Truth table

INF	OUTPUT	
А	В	OUTFOT
L	L	Н
L	Н	н
Н	L	н
Н	Н	L

Block diagram



• Absolute maximum ratings (Vss = 0V, Ta = 25°C)

Parameter	Symbol	Limits	Unit
Power supply voltage	Vdd	- 0.3 ~ + 18	V
Power dissipation	Pd	1000 (DIP), 450 (SOP), 350 (SSOP)	mW
Operating temperature	Topr	- 40 ~ + 85	°C
Storage temperature	Tstg	- 55 ~ + 150	°C
Input voltage	VIN	$-0.3 \sim V_{DD} + 0.3$	V

• Electrical characteristics

DC characteristics (unless otherwise noted, Vss = 0V, Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		Measurement
						Vdd (V)		circuit
Input high level voltage	Vін	3.5	_		V	5	4	Fig.1
		7.0	—			10	—	
		11.0	—	—		15		
		_	_	1.5	V	5	_	Fig.1
Input low level voltage	VIL	—	—	3.0		10		
			_	4.0	-	15		
Input high level current	Ін	_	_	0.3	μA	15	V⊪ = 15V	Fig.1
Input low level current	lı∟	_	_	- 0.3	μA	15	VIL = 0V	Fig.1
		4.95	_	_		5	lo = 0mA	
Output high level voltage	Vон	9.95	_	_	v	10		Fig.1
		14.95	_	_		15		
Output low level voltage	Vol	_	_	0.05	v	5	lo = 0mA	Fig.1
		_	_	0.05		10		
		_	_	0.05		15		
Output high level current	Іон	- 0.44	_	_	mA	5	Vон = 4.6V	Fig.1
		- 1.1	_	_		10	Vон = 9.5V	
		- 3.0	_	_		15	Vон = 13.5V	
	lol	0.44	_	_	mA	5	Vol = 0.4V	Fig.1
Output low level current		1.1	_	_		10	Vol = 0.5V	
		3.0	_	_		15	Vol = 1.5V	
Static current dissipation	lod	_	_	1	μA	5	VI = VDD or GND	Fig.1
		_	_	2		10		
		_	_	4		15		
Hysteresis voltage	Vн	0.17	_	0.39	V	5		Fig.3
		0.25	_	0.60		10		
		0.33	_	0.90	1	15		



								Measurement
Parameter	Symbol	Min.	Тур.	Max.	Unit.	Vdd (V)	Conditions	circuit
Output rise time		_	100	_		5		
	tт∟н	_	50	_	ns	10		Fig.2
			40	_		15		
Output fall time	tтн∟	_	100	_	ns	5	_	Fig.2
		_	50	_		10		
			40	_		15		
Propagation delay time, "L" to "H"	tрін	_	125	_	ns	5		Fig.2
		_	50	_		10		
		_	40	_		15		
Propagation delay time, "H" to "L"	tрнг		125	_	ns	5		Fig.2
			50	_		10		
		_	40	_		15		
Input capacitance	CIN	_	5	_	pF	—	—	_

Switching characteristics (unless otherwise noted, $V_{SS} = 0V$, $Ta = 25^{\circ}C$, $C_{L} = 50pF$)

Measurement circuits

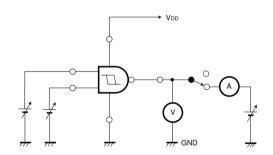
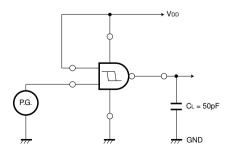
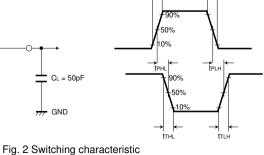


Fig. 1 DC characteristics





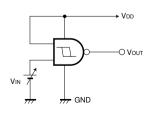
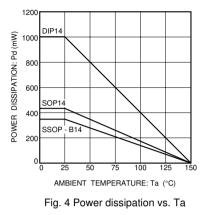


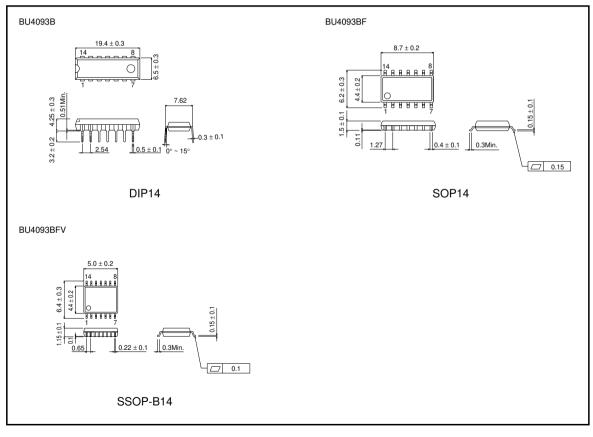
Fig. 3 Hysteresis voltage



Electrical characteristic curve



• External dimensions (Units: mm)



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