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BUL128

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- STMicroelectronics PREFERRED SALESTYPE
- NPN TRANSISTOR
- HIGH VOLTAGE CAPABILITY
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED

APPLICATIONS:

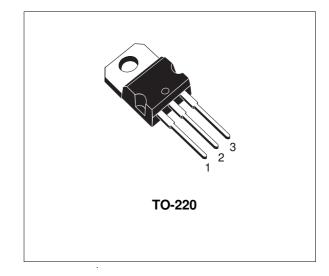
 ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING

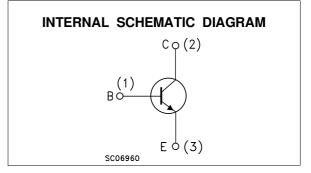
DESCRIPTION

The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and medium voltage capability.

It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CES}	Collector-Emitter Voltage (V _{BE} = 0)	700	V
VCEO	Collector-Emitter Voltage (I _B = 0)	400	V
V_{EBO}	Emitter-Base Voltage (I _C = 0)	9	V
Ic	Collector Current	4	Α
I _{CM}	Collector Peak Current (t _p < 5 ms)	8	Α
I _B	Base Current	2	Α
I _{BM}	Base Peak Current (t _p < 5 ms)	4	А
P _{tot}	Total Dissipation at $T_c = 25$ °C	70	W
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-Case	Max	1.78	°C/W
R _{thj-amb}	Thermal Resistance Junction-Ambient	Max	62.5	°C/W

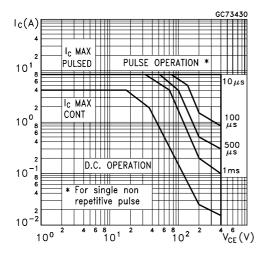
ELECTRICAL CHARACTERISTICS ($T_{case} = 25 \ ^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Co	Min.	Тур.	Max.	Unit	
ICES	Collector Cut-off Current (V _{BE} = -1.5 V)	V _{CE} = 700 V V _{CE} = 700 V	T _j = 125 °C			100 500	μΑ μΑ
V _{EBO}	Emitter-Base Voltage (I _C = 0)	I _E = 10 mA		9			V
$V_{CEO(sus)^*}$	Collector-Emitter Sustaining Voltage (I _B = 0)	I _C = 100 mA	L = 25 mH	400			V
ICEO	Collector Cut-Off Current ($I_B = 0$)	$V_{CE} = 400 V$				250	μA
$V_{CE(sat)^*}$	Collector-Emitter Saturation Voltage		$I_B = 0.1 A$ $I_B = 0.2 A$ $I_B = 0.5 A$ $I_B = 1 A$		0.5	0.7 1 1.5	V V V V
$V_{BE(sat)}*$	Base-Emitter Saturation Voltage	$I_{C} = 0.5 A$ $I_{C} = 1 A$ $I_{C} = 2.5 A$	I _B = 0.1 A I _B = 0.2 A I _B = 0.5 A			1.1 1.2 1.3	V V V
h _{FE} *	DC Current Gain	$ I_C = 10 \text{ mA} I_C = 2 \text{ A} Group A Group B $	$V_{CE} = 5 V$ $V_{CE} = 5 V$	10 14 25		28 40	
ts tf	RESISTIVE LOAD Storage Time Fall Time		I _C = 2 A I _{B2} = -0.4 A (see fig.2)	1.5	0.2	3 0.4	μs μs
ts tf	INDUCTIVE LOAD Storage Time Fall Time	$\label{eq:lc} \begin{array}{l} I_C = 2 \ A \\ V_{BE(off)} = -5 \ V \\ V_{clamp} = 200 \ V \end{array}$	$\label{eq:BB} \begin{split} I_{B1} &= 0.4 \ A \\ R_{BB} &= 0 \ \Omega \\ (see \ fig.1) \end{split}$		0.6 0.1	1 0.2	μs μs

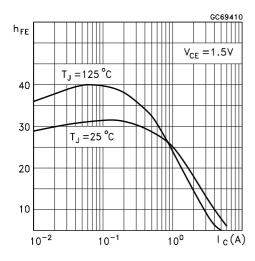
* Pulsed: Pulse duration = 300 μs, duty cycle 1.5 % Note : Product is pre-selected in DC current gain (GROUP A and GROUP B). STMicroelectronics reserves the right to ship either groups according to production availability. Please contact your nearest STMicroelectronics sales office for delivery details.

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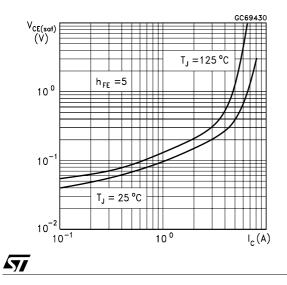
Safe Operating Areas



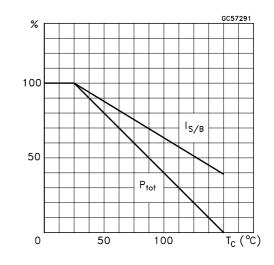
DC Current Gain



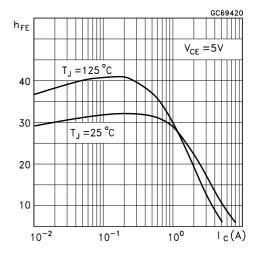
Collector Emitter Saturation Voltage

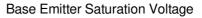


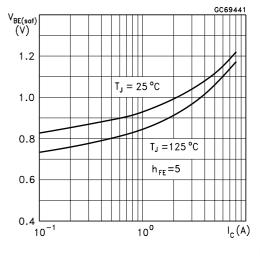
Derating Curve



DC Current Gain

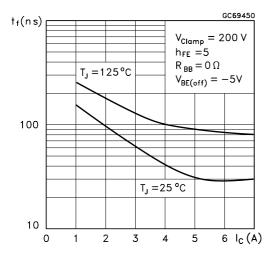




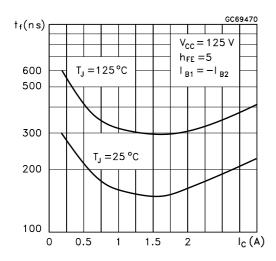


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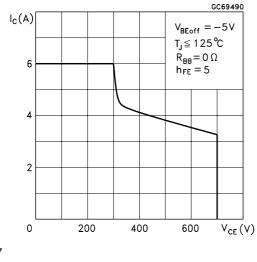
Inductive Load Fall Time



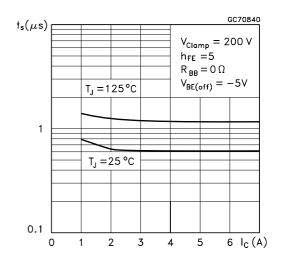
Resistive Load Fall Time



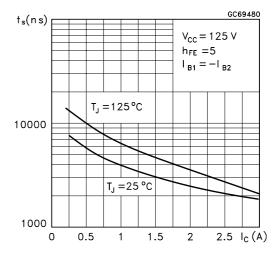
Reverse Biased SOA



Inductive Load Storage Time



Resistive Load Storage Time





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Figure 1: Inductive Load Switching Test Circuit.

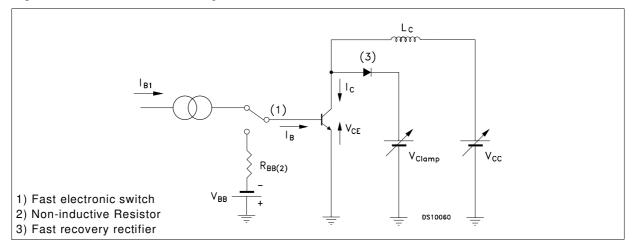
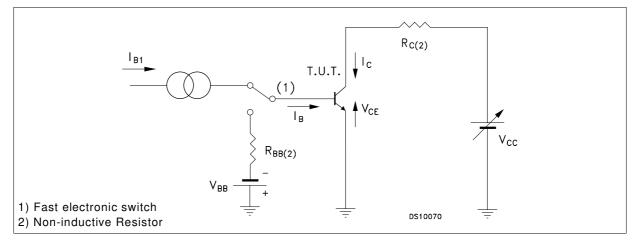
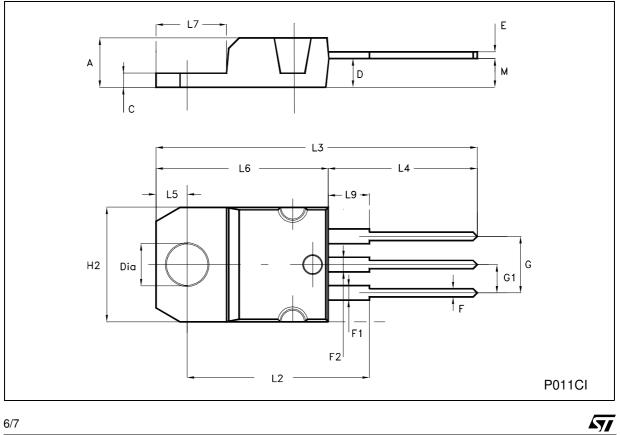


Figure 2: Resistive Load Switching Test Circuit.



DIM.		mm		inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А	4.40		4.60	0.173		0.181	
С	1.23		1.32	0.048		0.052	
D	2.40		2.72	0.094		0.107	
Е	0.49		0.70	0.019		0.027	
F	0.61		0.88	0.024		0.034	
F1	1.14		1.70	0.044		0.067	
F2	1.14		1.70	0.044		0.067	
G	4.95		5.15	0.194		0.202	
G1	2.40		2.70	0.094		0.106	
H2	10.00		10.40	0.394		0.409	
L2		16.40			0.645		
L4	13.00		14.00	0.511		0.551	
L5	2.65		2.95	0.104		0.116	
L6	15.25		15.75	0.600		0.620	
L7	6.20		6.60	0.244		0.260	
L9	3.50		3.93	0.137		0.154	
М		2.60			0.102		





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