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Overview

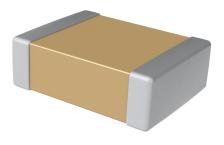
KEMET's X5R dielectric features an 85°C maximum operating temperature and is considered "semi-stable." The Electronics Components, Assemblies & Materials Association (EIA) characterizes X5R dielectric as a Class II material. Components of this classification are fixed, ceramic dielectric capacitors suited for bypass and decoupling applications or for frequency discriminating circuits where Q and stability of capacitance characteristics are not critical. X5R exhibits a predictable change in capacitance with respect to time and voltage and boasts a minimal change in capacitance with reference to ambient temperature. Capacitance change is limited to $\pm 15\%$ from -55° C to $+85^{\circ}$ C.

Benefits

- -55°C to +85°C operating temperature range
- Lead (Pb)-free, RoHS and REACH compliant
- Temperature stable dielectric
- EIA 0201, 0402, 0603, 0805, 1206, 1210, and 1812 case sizes
- DC voltage ratings of 4 V, 6.3 V, 10 V, 16 V, 25 V, 35 V, and 50 V
- Capacitance offerings ranging from 0.01 μF to 100 μF
- Available capacitance tolerances of ±10% and ±20%
- · Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination finish allowing for excellent solderability

Applications

Typical applications include decoupling, bypass, and filtering.



Ordering Information

C	1206	C	107	М	9	Р	Α	С	TU
Ceramic	Case Size (L" x W")	Specification/ Series	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage (VDC)	Dielectric	Failure Rate/ Design	Termination Finish ¹	Packaging/Grade (C-Spec)
	0201 0402 0603 0805 1206 1210	C = Standard	Two significant digits and number of zeros.	K = ±10% M = ±20%	7 = 4 9 = 6.3 8 = 10 4 = 16 3 = 25 6 = 35 5 = 50	P = X5R	A = N/A	C = 100% Matte Sn	See "Packaging C-Spec Ordering Options Table" below

¹ Additional termination finish options may be available. Contact KEMET for details.



Packaging C-Spec Ordering Options Table

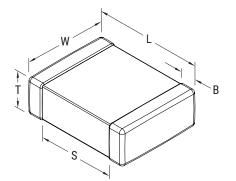
Packaging Type ¹	Packaging/Grade Ordering Code (C-Spec)
Bulk Bag/Unmarked	Not required (Blank)
7" Reel/Unmarked	TU
13" Reel/Unmarked	7411 (EIA 0603 and smaller case sizes) 7210 (EIA 0805 and larger case sizes)
7" Reel/Marked	ТМ
13" Reel/Marked	7040 (EIA 0603 and smaller case sizes) 7215 (EIA 0805 and larger case sizes)
7" Reel/Unmarked/2mm pitch ²	7081
13" Reel/Unmarked/2mm pitch ²	7082

¹ Default packaging is "Bulk Bag". An ordering code C-Spec is not required for "Bulk Bag" packaging.

¹ The terms "Marked" and "Unmarked" pertain to laser marking option of capacitors. All packaging options labeled as "Unmarked" will contain capacitors that have not been laser marked. Please contact KEMET if you require a laser marked option. For more information see "Capacitor Marking".

² The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

Dimensions – Millimeters (Inches)



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0201	0603	0.60 (0.024) ±0.03 (0.001)	0.30 (0.012) ±0.03 (0.001)		0.15 (0.006) ±0.05 (0.002)	N/A	Solder Reflow
0402 ¹	1005	1.00 (0.040) ±0.05 (0.002)	0.50 (0.020) ±0.05 (0.002)		0.30 (0.012) ±0.10 (0.004)	0.30 (0.012)	Only
0603	1608	1.60 (0.063) ±0.15 (0.006)	0.80 (0.032) ±0.15 (0.006)	See Table 2 for	0.35 (0.014) ±0.15 (0.006)	0.70 (0.028)	
0805	2012	2.00 (0.079) ±0.20 (0.008)	1.25 (0.049) ±0.20 (0.008)	Thickness	0.50 (0.02) ±0.25 (0.010)	0.75 (0.030)	Solder Wave or Solder Reflow
1206²	3216	3.20 (0.126) ±0.20 (0.008)	1.60 (0.063) ±0.20 (0.008)		0.50 (0.02) ±0.25 (0.010)	N1 / A	
1210 ³	3225	3.20 (0.126) ±0.20 (0.008)	2.50 (0.098) ±0.20 (0.008)	-	0.50 (0.02) ±0.25 (0.010)	N/A	Solder Reflow Only

¹ For capacitance values \geq 4.7 µF add 0.15 (0.006) to the width and length tolerance dimensions.

² For capacitance values $\ge 22 \ \mu\text{F}$ add 0.10 (0.004) to the positive bandwidth tolerance dimension.

³ For capacitance values ≥ 22 μF add 0.10 (0.004) to the length and width tolerance dimension and add 0.15 (0.006) to the positive bandwidth tolerance dimension.



Qualification/Certification

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance & Reliability.

Environmental Compliance

Lead (Pb)-free, RoHS, and REACH compliant without exemptions.

Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +85°C
Capacitance Change with Reference to +25°C and 0 Vdc Applied (TCC)	±15%
¹ Aging Rate (Maximum % Capacitance Loss/Decade Hour)	5.0%
² Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5±1 seconds and charge/discharge not exceeding 50mA)
³ Dissipation Factor (DF) Maximum Limit at 25°C	See Dissipation Factor Limit Table
⁴ Insulation Resistance (IR) Minimum Limit at 25°C	See Insulation Resistance Limit Table (Rated voltage applied for 120±5 seconds at 25°C)

¹ Regarding Aging Rate: Capacitance measurements (including tolerance) are indexed to a referee time of 48 or 1,000 hours. Please refer to a part number specific datasheet for referee time details.

²DWV is the voltage a capacitor can withstand (survive) for a short period of time. It exceeds the nominal and continuous working voltage of the capacitor.

³ Capacitance and dissipation factor (DF) measured under the following conditions:

1 kHz ±50 Hz and 1.0 ±0.2 Vrms if capacitance ≤ 10 μF

120 Hz ±10 Hz and 0.5 ±0.1 Vrms if capacitance > 10 μF

⁴ To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to G Ω limit. Select the lower of the two limits.

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."



Post Environmental Limits

High Temperature Life, Biased Humidity, Moisture ResistanceDielectricRated DC VoltageCapacitance ValueDissipation Factor (Maximum %)Capacitance ShiftInsulation Resistance $\lambda = 25$ $< 1.0 \ \mu$ F 7.5 > 25 $< 1.0 \ \mu$ F 7.5 > 20.0 > 25 $< 2.2 \ \mu$ F 7.5 $= 1.0 \ \mu$ F 20.0 $10\% $ of Initial Limit $\chi 5R$ 25 $< 2.2 \ \mu$ F 7.5 $\pm 20\%$ $10\% $ of Initial Limit												
Dielectric												
	× 95	< 1.0 µF	7.5									
	> 20	≥ 1.0 µF	20.0									
VED	05	< 2.2 µF	7.5	100%	10% of Initial							
A DK	20	≥ 2.2 µF	20.0	120%	Limit							
	- 25	< 0.56 µF	7.5									
	< 25	≥ 0.56 µF	20.0									

Dissipation Factor Limit Table

Rated DC Voltage	Capacitance	Dissipation Factor (Maximum %)
> 25	< 1.0 µF	5.0
> 25	≥ 1.0 µF	10.0
25	< 2.2 µF	5.0
25	≥ 2.2 µF	10.0
< 25	< 0.56 µF	5.0
< 25	≥ 0.56 µF	10.0

Insulation Resistance Limit Table

EIA Case Size	1,000 Megohm Microfarads or 100 GΩ	500 Megohm Microfarads or 10 GΩ	100 Megohm Microfarads
0201	N/A	ALL	N/A
0402	< .012 µF	≥ .012 µF < 1.0 µF	≥ 1.0 µF
0603	< .047 µF	≥ .047 µf < 1.0 µF	≥ 1.0 µF
0805	< 0.15 µF	≥ 0.15 µF < 1.0 µF	≥ 1.0 µF
1206	< 0.47 µF	≥ 0.47 µF < 1.0 µF	≥ 1.0 µF
1210	< 0.39 µF	≥ 0.39 µF < 1.0 µF	≥ 1.0 µF
1812	< 2.2 µF	≥ 2.2 µF	N/A



Table 1 – Capacitance Range/Selection Waterfall (0201 – 0805 Case Sizes)

			Size/ ies		C02	010	;			C04	020	;			(C06	030	;				C08	050	;	
Capacitance	Capacitance	Voltag	e Code	7	9	8	4	7	9	8	4	3	5	7	9	8	4	3	5	7	9	8	4	3	5
	Code	Rated Volt	age (VDC)	4	6.3	10	16	4	6.3	10	16	25	50	4	6.3	10	16	25	50	4	6.3	10	16	25	50
		Capac Tolei	itance		Į	oduct	: Ava	i ilabil	ity aı	nd Ch	nip Th	nickn	ess (Code		ee Ta	ble 2	for (Chip	ı Thick		s Dim	ensio	ons	
10,000 pF	103	K	M	AB	AB	AB	AB	BB	BB	BB	BB														
12,000 pF	123	К	М					BB	BB	BB	BB									1					
15,000 pF	153	K	М					BB	BB	BB	BB														
18,000 pF	183	К	М					BB	BB	BB	BB														
22,000 pF	223	K	М					BB	BB	BB	BB														
27,000 pF	273	K	М					BB	BB	BB	BB														
33,000 pF	333	K	М					BB	BB	BB	BB														
39,000 pF	393	К	М					BB	BB	BB	BB														
47,000 pF	473	K	М					BB	BB	BB	BB														
56,000 pF	563	K	M					BB	BB	BB	BB														
68,000 pF	683	K	М					BB	BB	BB	BB														
82,000 pF	823	K	М					BB	BB	BB	BB					00									
0.10 µF	104	K	М	AB	AB			BB	BB	BB	BB	BB		CG	CG	CG	CG	CG							
0.12 µF	124	K	M																						
0.15 µF	154	K	M																						
0.18 µF	184	K	М											0.0	00	00	00								
0.22 µF	224	K	M					BB	BB					CG	CG	CG	CG								
0.27 µF	274 334	K K	M M											CG CG	CG CG	CG CG	CG CG								
0.33 μF 0.39 μF	334 394	ĸ	M											CG	CG	CG	CG								
0.39 μF 0.47 μF	474	K	M					BB	BB					CG	CG	CG	CG	CG		DG	DG	DG	DG	DG	
0.47 μF 0.56 μF	564	K	M											CG	CG	CG	CG	00		DP	DP	DP	DP	DO	
0.68 µF	684	K	M											CG	CG	CG	CG			DP	DP	DP	DP	DE	
0.82 µF	824	K	M											CG	CG	CG	CG			DF	DF	DF	DF	DF	
1.0 μF	105	K	M					BB	BB	BB	BB			CG	CG	CG	CG	СJ		DP	DP	DP	DP	DG	DG
1.2 μF	125	K	M					00	00	00	00				00	00	00	00		DN	DN	DN	DN	00	00
1.5 µF	155	K	M																	DN	DN	DN	DN		
1.8 µF	185	K	M																	DP	DP	DP	DP		
2.2 µF	225	K	М					вв	BB	BB1				CG	CG	CG	CG			DG	DG	DG	DG	DG	
2.7 µF	275	K	М																	DL	DL	DL	DL		
3.3 µF	335	K	M					BB1						CG	CG					DL	DL	DL	DG		
3.9 µF	395	K	M																	DG	DG	DG	DG		
4.7 μF	475	К	М					BE1	BE1					CG	CG	CG				DG	DG	DG	DG	DG	
5.6 µF	565	K	М																	DG	DG	DG			
6.8 µF	685	К	М																	DG	DG	DG			
8.2 µF	825	K	М																						
10 µF	106	К	М					BF1	BF1					CG1	CG1	CK1				DG	DG	DG	DG	DH	
12 µF	126	K	М																						
15 µF	156	К	М																						
18 µF	186	К	М																						
22 µF	226	K	М																	DG	DG	DH1			
47 μF	476	K	M		e	~	~		e	-	5	5	~			_	50	10	_	DH1	DH1 m	-	~	ĥ	_
	Consoltance		age (VDC)	4	ف	9	16	4	ق	9	16	25	50	4	6.3	9	16	25	50	4	ف	9	16 25		50
Capacitance	Capacitance Code	Voltag	e Code	7	9	8	4	7	9	8	3	5	4	7	9	8	4	3	5	7	9	8	4	3	5
		Case Siz	e/Series		C02	01C				C04	02C					C06	03C				7 9 8 4 3 C0805C				

xx¹ Available only in M tolerance.



Table 1 – Capacitance Range/Selection Waterfall (1206 – 1812 Case Sizes)

		Case Ser			C	:1206	С				C12	10C			C	C1812	С
Capacitance	Capacitance	Voltag	e Code	9	8	4	3	1	9	8	4	3	6	5	3	6	5
oupuentance	Code	Rated Volt	age (VDC)	6.3	6	16	25	50	6.3	10	16	25	35	50	25	35	50
		Cap To	lerance	Pi	roduct /	Availabi	lity and	Chip T	hicknes	s Code	s – See	Table 2	for Chi	p Thick	ness Di	mensio	ns
0.27 µF	274	K	М	EB	EB	EB	EB										
0.33 µF	334	K	М	EB	EB	EB	EB										
0.39 µF	394	К	М	EB	EB	EB	EB		FD	FD	FD	FD	FD	FD			
0.47 μF	474	K	М	EC	EC	EC	EC		FD	FD	FD	FD	FD	FD			
0.56 µF	564	K	М	ED	ED	ED	ED		FD	FD	FD	FD	FD	FD			
0.68 µF	684	K	М	EE	EE	EE	EE		FD	FD	FD	FD	FD	FD			
0.82 µF	824	К	М	EF	EF	EF	EF		FF	FF	FF	FF	FF	FF			
1.0 µF	105	К	М	EP	EP	EP	EP		FH	FH	FH	FH	FH	FH			
1.2 µF	125	К	М	EC	EC	EC	EC		FD	FD	FD	FD					
1.5 µF	155	К	М	EC	EC	EC	EC		FD	FD	FD	FD					
1.8 µF	185	К	М	EC	EC	EC	EC		FD	FD	FD	FD					
2.2 µF	225	К	М	EC	EC	EC	EC		FJ	FJ	FJ	FJ					
2.7 µF	275	К	М	EF	EF	EF	EF		FG	FG	FG	FG					
3.3 µF	335	К	м	EH	EH	EH	EH		FH	FH	FH	FH					
3.9 µF	395	к	м	ED	ED	ED	ED		FJ	FJ	FJ	FJ					
4.7 μF	475	К	М	EH	EH	EH	EH	EH	FT	FT	FT	FT					
5.6 µF	565	к	м	EK	EK	EH			FG	FG	FG	FE					
6.8 µF	685	к	м	EK	EK	EH			FJ	FJ	FJ	FJ					
8.2 µF	825	К	м	ED	ED	EH			FK	FK	FK	FG					
10 µF	106	к	м	EH	EH	EH	EH		FT	FT	FT	FT	FT	FS	GK		
12 µF	126	K	M						FD	FD	FG			-			
15 µF	156	K	M						FF	FF	FG						
18 µF	186	K	M						FG	FG	FH						
22 µF	226	K	M	EH	EH1	EH1			FS	FS	FS	FS					
47 μF	476	K	M	EH1	EH1				FS ¹	FS ¹	FS ¹						
100 µF	107	К	М	EH1					FS ¹	FS ¹	FS ¹						
		Rated Volt	age (VDC)	6.3	10	16	25	50	6.3	1	16	25	35	50	25	35	50
Capacitance	Capacitance Code	Voltag	e Code	9	8	4	3	1	9	8	4	3	6	5	3	6	5
		Case Siz	e/Series			C1206C	;				C12	10C				C18120	;

xx¹ Available only in M tolerance.



Table 2A – Chip Thickness/Tape & Reel Packaging Quantities

Thickness	Case	Thickness ±	Paper Q	uantity ¹	Plastic (Quantity	
Code	Size1	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel	
AB	0201	0.30 ± 0.03	15,000	0	0	0	
BB	0402	0.50 ± 0.05	10,000	50,000	0	0	
BE	0402	0.50 ± 0.15	10,000	50,000	0	0	
BF	0402	0.50 ± 0.20	10,000	50,000	0	0	
CG	0603	0.80 ± 0.10*	4,000	15,000	0	0	
CJ	0603	0.80 ± 0.15*	4,000	15,000	0	0	
CK	0603	0.80 ± 0.20	4,000	15,000	0	0	
DN	0805	0.78 ± 0.10*	4,000	15,000	0	0	
DP	0805	0.90 ± 0.10*	4,000	15,000	0	0	
DL	0805	0.95 ± 0.10	0	0	4,000	10,000	
DE	0805	1.00 ± 0.10	0	0	2,500	10,000	
DF	0805	1.10 ± 0.10	0	0	2,500	10,000	
DG	0805	1.25 ± 0.15	0	0	2,500	10,000	
DH	0805	1.25 ± 0.20	0	0	2,500	10,000	
EB	1206	0.78 ± 0.10	4,000	10,000	4,000	10,000	
EK	1206	0.80 ± 0.10	0	0	2,000	8,000	
EC	1206	0.90 ± 0.10	0	0	4,000	10,000	
ED	1206	1.00 ± 0.10	0	0	2,500	10,000	
EE	1206	1.10 ± 0.10	0	0	2,500	10,000	
EF	1206	1.20 ± 0.15	0	0	2,500	10,000	
EP	1206	1.20 ± 0.20	0	0	2,500	10,000	
EH	1206	1.60 ± 0.20	0	0	2,000	8,000	
FD	1210	0.95 ± 0.10	0	0	4,000	10,000	
FE	1210	1.00 ± 0.10	0	0	2,500	10,000	
FF	1210	1.10 ± 0.10	0	0	2,500	10,000	
FG	1210	1.25 ± 0.15	0	0	2,500	10,000	
FH	1210	1.55 ± 0.15	0	0	2,000	8,000	
FJ	1210	1.85 ± 0.20	0	0	2,000	8,000	
FT	1210	1.90 ± 0.20	0	0	2,000	8,000	
FK	1210	2.10 ± 0.20	0	0	2,000	8,000	
FS	1210	2.50 ± 0.30	0	0	1,000	4,000	
GK	1812	1.60 ± 0.20	0	0	1,000	4,000	
Thickness	Case	Thickness ±	7" Reel	13" Reel	7" Reel	13" Reel	
Code	Size ¹	Range (mm)	Paper Q	uantity ¹	Plastic	ic Quantity	

Package quantity based on finished chip thickness specifications.

¹ If ordering using the 2 mm Tape and Reel pitch option, the packaging quantity outlined in the table above will be doubled. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".



Table 2B – Bulk Packaging Quantities

Deekeri		Loose Pa	ackaging					
Packagi	ing Type	Bulk Bag	(default)					
Packagin	g C-Spec¹	N/	N/A ²					
Case	Size	Packaging Quantities (pieces/unit packa						
EIA (in)	Metric (mm)	Minimum	Maximum					
0402	1005							
0603	1608							
0805	2012		50,000					
1206	3216							
1210	3225	1						
1808	4520							
1812	4532							
1825	4564		20,000					
2220	5650							
2225	5664							

¹ The "Packaging C-Spec" is a 4 to 8 digit code which identifies the packaging type and/or product grade. When ordering, the proper code must be included in the 15th through 22nd character positions of the ordering code. See "Ordering Information" section of this document for further details. Commercial Grade product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging. Contact KEMET if you require a bulk bag packaging option for Automotive Grade products.

² A packaging C-Spec (see note 1 above) is not required for "Bulk Bag" packaging (excluding Anti-Static Bulk Bag and Automotive Grade products). The 15th through 22nd character positions of the ordering code should be left blank. All product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging.



Table 3 – Chip Capacitor Land Pattern Design Recommendations per IPC-7351

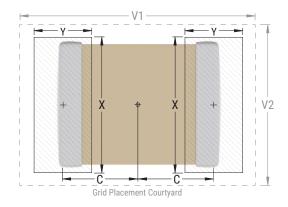
EIA Size Code	Metric Size Code	I		sity Lev mum (I rotrusic	Most))	Density Level B: Median (Nominal) Land Protrusion (mm)					Density Level C: Minimum (Least) Land Protrusion (mm)				
Coue	Coue	C	Y	X	V1	V2	C	Y	X	V1	V2	C	Y	X	V1	V2
0201	0603	0.38	0.56	0.52	1.80	1.00	0.33	0.46	0.42	1.50	0.80	0.28	0.36	0.32	1.20	0.60
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90
1210 ¹	3225	1.50	1.60	2.90	5.60	3.90	1.40	1.40	2.80	4.70	3.30	1.30	1.20	2.70	4.00	3.00

¹ Only for capacitance values \geq 22 μ F

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC-7351).

Image below based on Density Level B for an EIA 1210 case size.





Soldering Process

Recommended Soldering Technique:

- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder reflow only

Recommended Reflow Soldering Profile:

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

Profile Feature	Terminat	ion Finish
Tomereature	SnPb	100% Matte Sn
Preheat/Soak		
Temperature Minimum (T _{smin})	100°C	150°C
Temperature Maximum (T _{Smax})	150°C	200°C
Time (t_s) from T_{smin} to T_{smax}	60 – 120 seconds	60 – 120 seconds
Ramp-Up Rate $(T_L to T_p)$	3°C/second maximum	3°C/second maximum
Liquidous Temperature (T_L)	183°C	217°C
Time Above Liquidous (t_L)	60 – 150 seconds	60 – 150 seconds
Peak Temperature (T _P)	235°C	260°C
Time Within 5°C of Maximum Peak Temperature (t _P)	20 seconds maximum	30 seconds maximum
Ramp-Down Rate $(T_p to T_l)$	6°C/second maximum	6°C/second maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.

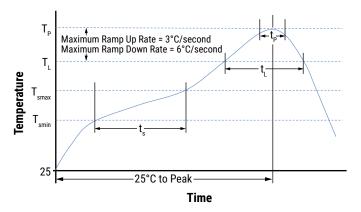




Table 4 – Performance & Reliability: Test Methods and Conditions

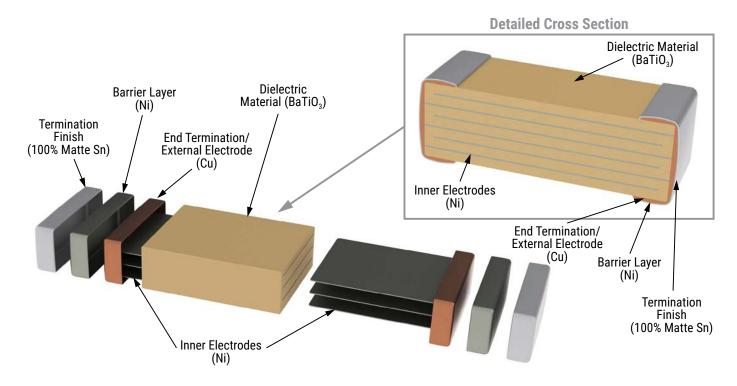
Stress	Reference	Test or Inspection Method						
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8 kg for 60 seconds.						
Board Flex	JIS-C-6429	Appendix 2, Note: Standard COG. Flexible termination sy	Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for COG. Flexible termination system – 3.0 mm (minimum).					
		Magnification 50 X. Condition	ons:					
		a) Method B, 4 hours a	t 155°C, dry heat at 235°C					
Solderability	J-STD-002	b) Method B at 215°C o	category 3					
		c) Method D, category	3 at 260°C					
Temperature Cycling	JESD22 Method JA-104	1,000 Cycles (-55°C to +125	°C). Measurement at 24 ho	urs +/- 4 hours after test conclusion.				
Biased Humidity	MIL-STD-202 Method 103	Measurement at 24 hours +/ Low Volt Humidity: 1,000 ho	Load Humidity: 1,000 hours 85°C/85% RH and rated voltage. Add 100 K ohm resistor. Measurement at 24 hours +/- 4 hours after test conclusion. Low Volt Humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor.					
Moisture Resistance	MIL-STD-202 Method 106	Measurement at 24 hours +/- 4 hours after test conclusion. t = 24 hours/cycle. Steps 7a and 7b not required. Measurement at 24 hours +/- 4 hours after test conclusion.						
Thermal Shock	MIL-STD-202 Method 107	-55°C/+125°C. Note: Numbe seconds. Dwell time – 15 mi	er of cycles required – 300. nutes. Air – Air.	Maximum transfer time - 20				
		1,000 hours at 85°C with 2 X rated voltage applied excluding the following:						
		Case Size	Capacitance	Applied Voltage				
	MIL-STD-202 Method	0402	≥ 0.22 µF					
High Temperature Life	108	0603	≥ 1.0 µF					
	/EIA-198	0805	≥ 4.7 µF	1.5 X				
		1206	≥ 2.2 µF					
		1210	≥ 10 µF					
Storage Life	MIL-STD-202 Method 108	150°C, 0 VDC for 1,000 hours.						
Vibration	MIL-STD-202 Method 204	5 g's for 20 minutes, 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz						
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.						
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemica	l, OKEM Clean or equivalen	t.				

Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature–reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction (Typical)





Capacitor Marking (Optional):

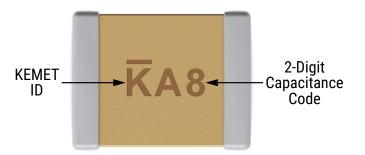
These surface mount multilayer ceramic capacitors are normally supplied unmarked. If required, they can be marked as an extra cost option. Marking is available on most KEMET devices but must be requested using the correct ordering code identifier(s). If this option is requested, two sides of the ceramic body will be laser marked with a "K" to identify KEMET, followed by two characters (per EIA–198 - see table below) to identify the capacitance value. EIA 0603 case size devices are limited to the "K" character only.

Laser marking option is not available on:

- COG, Ultra Stable X8R and Y5V dielectric devices.
- EIA 0402 case size devices.
- EIA 0603 case size devices with Flexible Termination option.
- KPS Commercial and Automotive Grade stacked devices.
- X7R dielectric products in capacitance values outlined below.

EIA Case Size	Metric Size Code	Capacitance
0603	1608	≤ 170 pF
0805	2012	≤ 150 pF
1206	3216	≤ 910 pF
1210	3225	≤ 2,000 pF
1808	4520	≤ 3,900 pF
1812	4532	≤ 6,700 pF
1825	4564	≤ 0.018 µF
2220	5650	≤ 0.027 µF
2225	5664	≤ 0.033 µF

Marking appears in legible contrast. Illustrated below is an example of an MLCC with laser marking of "KA8", which designates a KEMET device with rated capacitance of 100 μ F. Orientation of marking is vendor optional.





Capacitor Marking (Optional) cont'd

Capacitance (pF) For Various Alpha/Numeral Identifiers												
Alpha						Numera	al					
Alpha	9	0	1	2	3	4	5	6	7	8		
Character	Capacitance (pF)											
А	0.10	1.0	10	100	1,000	10,000	100,000	1,000,000	10,000,000	100,000,000		
В	0.11	1.1	11	110	1,100	11,000	110,000	1,100,000	11,000,000	110,000,000		
С	0.12	1.2	12	120	1,200	12,000	120,000	1,200,000	12,000,000	120,000,000		
D	0.13	1.3	13	130	1,300	13,000	130,000	1,300,000	13,000,000	130,000,000		
E	0.15	1.5	15	150	1,500	15,000	150,000	1,500,000	15,000,000	150,000,000		
F	0.16	1.6	16	160	1,600	16,000	160,000	1,600,000	16,000,000	160,000,000		
G	0.18	1.8	18	180	1,800	18,000	180,000	1,800,000	18,000,000	180,000,000		
Н	0.20	2.0	20	200	2,000	20,000	200,000	2,000,000	20,000,000	200,000,000		
J	0.22	2.2	22	220	2,200	22,000	220,000	2,200,000	22,000,000	220,000,000		
К	0.24	2.4	24	240	2,400	24,000	240,000	2,400,000	24,000,000	240,000,000		
L	0.27	2.7	27	270	2,700	27,000	270,000	2,700,000	27,000,000	270,000,000		
М	0.30	3.0	30	300	3,000	30,000	300,000	3,000,000	30,000,000	300,000,000		
Ν	0.33	3.3	33	330	3,300	33,000	330,000	3,300,000	33,000,000	330,000,000		
Р	0.36	3.6	36	360	3,600	36,000	360,000	3,600,000	36,000,000	360,000,000		
Q	0.39	3.9	39	390	3,900	39,000	390,000	3,900,000	39,000,000	390,000,000		
R	0.43	4.3	43	430	4,300	43,000	430,000	4,300,000	43,000,000	430,000,000		
S	0.47	4.7	47	470	4,700	47,000	470,000	4,700,000	47,000,000	470,000,000		
Т	0.51	5.1	51	510	5,100	51,000	510,000	5,100,000	51,000,000	510,000,000		
U	0.56	5.6	56	560	5,600	56,000	560,000	5,600,000	56,000,000	560,000,000		
V	0.62	6.2	62	620	6,200	62,000	620,000	6,200,000	62,000,000	620,000,000		
W	0.68	6.8	68	680	6,800	68,000	680,000	6,800,000	68,000,000	680,000,000		
Х	0.75	7.5	75	750	7,500	75,000	750,000	7,500,000	75,000,000	750,000,000		
Y	0.82	8.2	82	820	8,200	82,000	820,000	8,200,000	82,000,000	820,000,000		
Z	0.91	9.1	91	910	9,100	91,000	910,000	9,100,000	91,000,000	910,000,000		
а	0.25	2.5	25	250	2,500	25,000	250,000	2,500,000	25,000,000	250,000,000		
b	0.35	3.5	35	350	3,500	35,000	350,000	3,500,000	35,000,000	350,000,000		
d	0.40	4.0	40	400	4,000	40,000	400,000	4,000,000	40,000,000	400,000,000		
е	0.45	4.5	45	450	4,500	45,000	450,000	4,500,000	45,000,000	450,000,000		
f	0.50	5.0	50	500	5,000	50,000	500,000	5,000,000	50,000,000	500,000,000		
m	0.60	6.0	60	600	6,000	60,000	600,000	6,000,000	60,000,000	600,000,000		
n	0.70	7.0	70	700	7,000	70,000	700,000	7,000,000	70,000,000	700,000,000		
t	0.80	8.0	80	800	8,000	80,000	800,000	8,000,000	80,000,000	800,000,000		
у	0.90	9.0	90	900	9,000	90,000	900,000	9,000,000	90,000,000	900,000,000		



Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

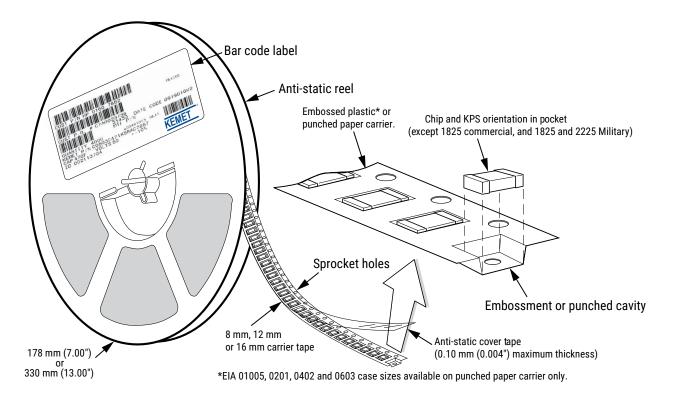


Table 5 – Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

	Таре	Embosse	d Plastic	Punche	d Paper
EIA Case Size	Size	7" Reel	13" Reel	7" Reel	13" Reel
	(W)*	Pitch	(P ₁)*	Pitch	(P ₁)*
01005 - 0402	8			2	2
0603	8			2/4	2/4 -
0805	8	4	4	4	4
1206 - 1210	8	4	4	4	4
1805 - 1808	12	4	4		
≥ 1812	12	8	8		
KPS 1210	12	8	8		
KPS 1812 and 2220	16	12	12		
Array 0612	8	4	4		

*Refer to Figures 1 and 2 for W and P_1 carrier tape reference locations. *Refer to Tables 6 and 7 for tolerance specifications.

New 2 mm Pitch Reel Options*

Packaging Ordering Code (C-Spec)	Packaging Type/Options
C-3190	Automotive grade 7" reel unmarked
C-3191	Automotive grade 13" reel unmarked
C-7081	Commercial grade 7" reel unmarked
C-7082	Commercial grade 13" reel unmarked

* 2 mm pitch reel only available for 0603 EIA case size. 2 mm pitch reel for 0805 EIA case size under development.

Benefits of Changing from 4 mm to 2 mm Pitching Spacing

- Lower placement costs.
- Double the parts on each reel results in fewer reel changes and increased efficiency.
- Fewer reels result in lower packaging, shipping and storage costs, reducing waste.



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

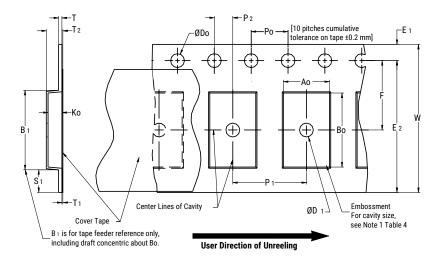


Table 6 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions – Millimeters (Inches)								
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm		1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm		(0.059)				(1.181)			
		,	Variable Dime	ensions — Mil	limeters (Inch	ies)			
Tape Size	Pitch	B ₁ Maximum Note 4	E ₂ Minimum	F	P ₁	T ₂ Maximum	W Maximum	A ₀ ,B ₀	& K ₀
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) and Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Not	te 5
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.

2. The tape with or without components shall pass around R without damage (see Figure 6).

3. If S₁ < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).

4. B, dimension is a reference dimension for tape feeder clearance only.

5. The cavity defined by A_{α} , B_{α} and K_{α} shall surround the component with sufficient clearance that:

(a) the component does not protrude above the top surface of the carrier tape.

(b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

(c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).

(d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4).

(e) for KPS product, A_a and B_a are measured on a plane 0.3 mm above the bottom of the pocket.

(f) see addendum in EIA Standard 481 for standards relating to more precise taping requirements.



Figure 2 – Punched (Paper) Carrier Tape Dimensions

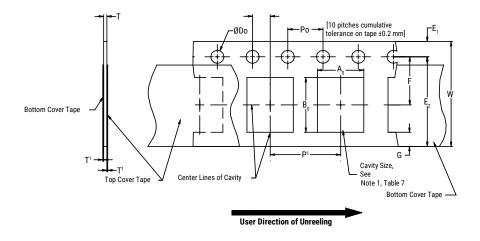


Table 7 – Punched (Paper) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions – Millimeters (Inches)								
Tape Size	D ₀	E ₁	P ₀	P ₂	T ₁ Maximum	G Minimum	R Reference Note 2		
8 mm	1.5 +0.10 -0.0 (0.059 +0.004 -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) Maximum	0.75 (0.030)	25 (0.984)		
		Variable D	imensions — M	illimeters (Inche	es)				
Tape Size	Tape Size Pitch E2 Minimum F P1 T Maximum W Maximum A0B0								
8 mm	Half (2 mm)	6.25	3.5 ±0.05	2.0 ±0.05 (0.079 ±0.002)	1.1	8.3 (0.327)	Note 1		
8 mm	Single (4 mm)	(0.246)	(0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	(0.098)	8.3 (0.327)	NOLE I		

1. The cavity defined by A_{α} , B_{α} and T shall surround the component with sufficient clearance that:

a) the component does not protrude beyond either surface of the carrier tape.

b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

c) rotation of the component is limited to 20° maximum (see Figure 3).

d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).

e) see addendum in EIA Standard 481 for standards relating to more precise taping requirements.

2. The tape with or without components shall pass around R without damage (see Figure 6).



Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 Newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ± 10 mm/minute.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624*.

Figure 3 – Maximum Component Rotation

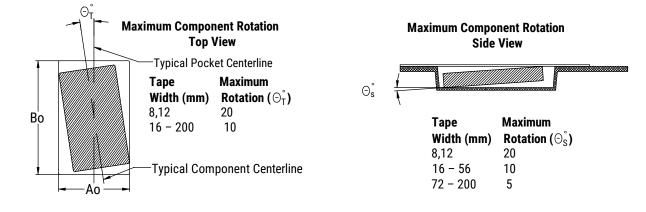


Figure 4 – Maximum Lateral Movement

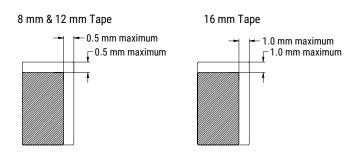


Figure 5 – Bending Radius

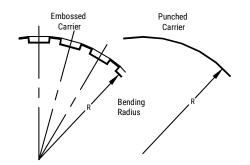
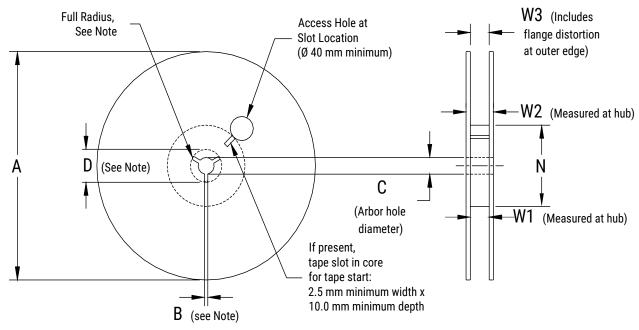




Figure 6 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 8 – Reel Dimensions

Metric will govern

	Constant Dimensions — Millimeters (Inches)								
Tape Size	А	D Minimum							
8 mm	178 ±0.20								
12 mm	(7.008 ±0.008) or	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)					
16 mm	330 ±0.20 (13.000 ±0.008)		()						
	Variable	Dimensions — Millimeter	rs (Inches)						
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃					
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)						
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference					
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)						



Figure 7 – Tape Leader & Trailer Dimensions

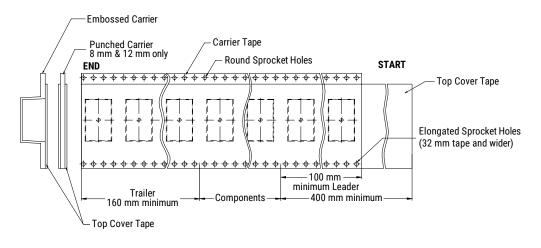
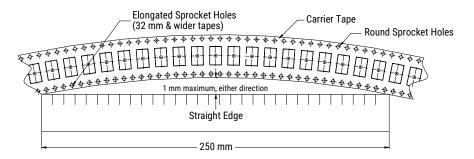


Figure 8 – Maximum Camber





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Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicted or that other measures may not be required.

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