

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

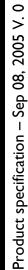
Mid-voltage: NPO/X7R (Pb Free & RoHS compliant)

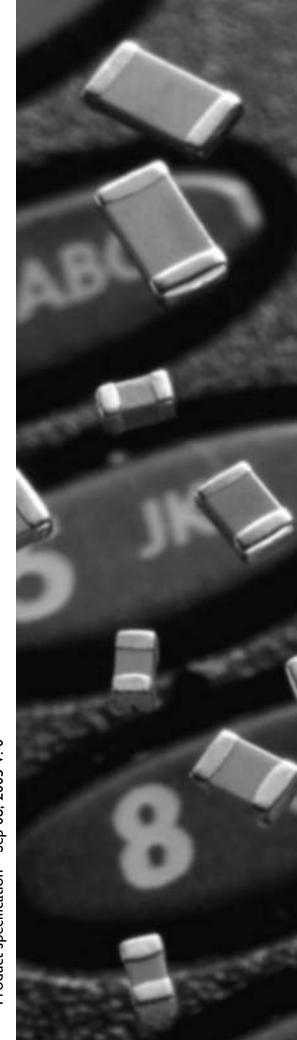
100 V TO 500 V

10 pF to 470 nF



YAGEO





SCOPE

This specification describes Midvoltage NP0/X7R series chip capacitors with lead-free terminations.

APPLICATIONS

- PCs, hard disk, game PCs
- Power supplies
- LCD panel
- ADSL, modem

FEATURES

- Supplied in tape on reel
- Nickel-barrier end termination

ORDERING INFORMATION

Part number is identified by the series, size, tolerance, packing style, TC material, rated voltage and capacitance value.

YAGEO ORDERING CODE

CC <u>xxxx x x x xxx x B x xxx</u> (1) (2) (3) (4) (5)

(I) SIZE - INCH BASED (METRIC)

0603 (1608)

0805 (2012)

1206 (3216)

1210 (3225)

1808 (4520)

1812 (4532)

(2) TOLERANCE

 $| = \pm 5\%$

 $K = \pm 10\%$

(3) PACKING STYLE

R = 7" paper tape

K = 7" blister tape

P = 13" paper tape

F = 13" blister tape

C = Bulk case

(4) TC MATERIAL

NP0

X7R

(5) RATED VOLTAGE

0 = 100 V

A = 200 V

Y = 250 V

B = 500 V

(6) PROCESS

B = BME

N = NME

(7) CAPACITANCE VALUE:

First two for significant figures and 3rd for number of zero

Letter "R" for decimal point

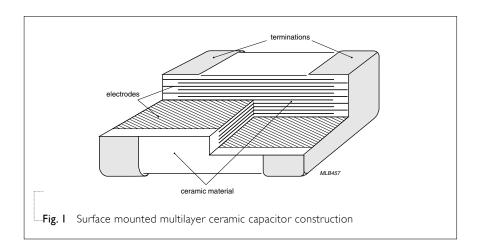


CONSTRUCTION

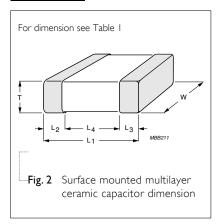
YAGEO

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.



DIMENSION

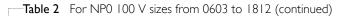


-Table I **TYPE** CC0603 CC0805 CC1206 CC1210 CC1808 CC1812 L_I (mm) 1.6 ±0.10 2.0 ± 0.20 3.2±0.20 3.2 ±0.20 4.5 ±0.30 4.5 ± 0.30 W (mm) 0.8 ±0.07 1.25 ±0.20 1.6±0.20 2.5 ± 0.20 2.0 ± 0.30 3.2 ± 0.30 Т Refer to table 2 to 7 (mm) min. 0.20 0.25 0.25 0.25 0.25 0.25 L_2/L_3 (mm) max. 0.50 0.75 0.75 0.75 0.75 0.75 L_4 min. 0.60 0.55 1.40 1.40 2.20 2.20 (mm)

CAPACITANCE RANGE & THICKNESS FOR NPO 100V

Table 2 For NP0 100 V sizes from 0603 to 1812

CAPACITANCE	100 V					
(pF)	0603	0805	1206	1210	1808	1812
10						
12						
15						
18						
22						
27						
33						
39						
47						
56						
68						
82						
100	0.8 ±0.10	0.6 ±0.10				
120						
150			0.6 ±0.10			
180						
220						
270						
330						
390						
470						
560						
680						
820						
1,000						
1,200 1,500						
1,800		0.85 ±0.10				
2,200				0 (, 0 , 0	125 1020	125 122
2,700				0.6 ±0.10	1.25 ±0.20	1.25 ±0.20
3,300		125 1020				
3,900		1.25 ±0.20	0.05 +0.10			
4,700			0.85 ±0.10			
5,600			1151015	0.05 1.0.10		
5,000			1.15±0.15	0.85 ±0.10		



CAPACITANCE	100 V					
(pF)	0603	0805	1206	1210	1808	1812
6,800			1.15±0.15			
8,200			125 .020	0.85 ±0.10		1.25 ±0.20
10,000			1.25 ±0.20			
12,000				125 1020		0.05 + 0.10
15,000				1.25 ±0.20		0.85 ±0.10
18,000						1.15.0.15
22,000						1.15±0.15
27,000						
33,000						
39,000						
47,000						

NOTE

- 1. Values in shaded cells indicate thickness class in mm.
- 2. Capacitance range < 10 pF is on request.

CAPACITANCE RANGE & THICKNESS FOR NPO 200/250 V

Table 3 For NP0 200/250 V sizes from 0603 to 1812

CAPACITANCE	200/250 V					
(pF)	0603	0805	1206	1210	1808	1812
10						
12						
15						
18						
22						
27						
33						
39						
47	0.8 ±0.10	0.6 ±0.10	0.6 ±0.10			
56						
68						
82						
100						
120						
150						
180						



Table 3 For NP0 200/250 V sizes **from** 0603 to 1812 (continued)

CAPACITANCE	200/250 V	om 0003 to 1012 (c	,			
(pF)	0603	0805	1206	1210	1808	1812
220						
270						
330	0.8 ±0.10	0.85 ±0.10				
390			0.6 ±0.10			
470						
560		1.25 ±0.20				
680						
820		0.8 ±0.10				
1,000			0.85 ±0.10			
1,200				1.25 ±0.20		
1,500		1.25 ±0.20	1.15±0.15			
1,800			0.8 ±0.10	0.05 + 0.10		1.25 ±0.20
2,200				0.85 ±0.10	105.000	
2,700			1.05		1.25 ±0.20	
3,300			1.25 ±0.20	1.15±0.15		
3,900						0.85 ±0.10
4,700				1.25 ±0.20		
5,600						1.15±0.15
6,800						
8,200						
10,000						

NOTE

- 1. Values in shaded cells indicate thickness class in mm.
- 2. Capacitance range < 10 pF is on request.

CAPACITANCE RANGE & THICKNESS FOR NPO 500 V

Table 4 For NP0 500 V sizes from 0805 to 1812

CAPACITANCE (pF)	500 V 0805	1206	1210	1808	1812
10					
12					
15	0.6 ±0.10	0.6 ±0.10			
18					
22					
27					





Table 4 For NP0 500 V sizes from 0805 to 1812 (continued)

CAPACITANCE	500 V	os to 1012 (continued)			
(pF)	0805	1206	1210	1808	1812
33					
39					
47					
56					
68	0.6 ±0.10	0.6 ±0.10			
82					
100					
120					
150					
180			0.85 ±0.10		
220					
270					
330	0.85 ±0.10				
390					
470		0.85 ±0.10			
560					
680		1.15±0.15			
820	1.25 ±0.20	1.13±0.13			
1,000					
1,200		0.8 ±0.10	1.15.0.15		125 1020
1,500			1.15±0.15	1.25 1.020	1.25 ±0.20
1,800		1.25 ±0.20		1.25 ±0.20	
2,200			1.25 ±0.20		
2,700					1.15±0.15
3,300					
3,900					105 1000
4,700					1.25 ±0.20
5,600					
6,800					
8,200					
10,000					

NOTE

- 1. Values in shaded cells indicate thickness class in mm.
- 2. Capacitance range $< 10 \ pF$ is on request.



CAPACITANCE RANGE & THICKNESS FOR X7R 100V

Table 5 For X7R 100 V sizes from 0805 to 1812

CAPACITANCE	100 V					
(pF)	0603	0805	1206	1210	1808	1812
100						
150						
220						
330						
470						
680						
1,000	0.8 ±0.10					
1,500						
2,200		0.6 ±0.10	0.85 ±0.10			
3,300						
4,700						
6,800						
10,000						
15,000		0.85 ±0.10				
22,000		0.03 ±0.10				
33,000		1.25 ±0.20				
47,000		1,23 ±0,20				0.85 ±0.10
68,000			1.15±0.15	0.85 ±0.10	1.25 ±0.20	
100,000			1.13±0.13			
150,000				1.15±0.15		1.15±0.15
220,000				1.6 ±0.20		1.13±0.13
330,000						
470,000						1.6 ±0.20
680,000						
1,000,000						

NOTE

1. Values in shaded cells indicate thickness class in mm.

CAPACITANCE RANGE & THICKNESS FOR X7R 200/250 V

Table 6 For X7R 200/250 V sizes from 0805 to 1812

CAPACITANCE	200/250 V				
(pF)	0805	1206	1210	1808	1812
100					
150					
220					
330					
470					
680					
1,000	0.85 ±0.10				
1,500					
2,200					
3,300		0.85 ±0.10			
4,700					
6,800	1.25 ±0.20				
10,000	1.23 ±0.20		0.85 ±0.10		
15,000	0.8 ±0.10		0.85 ±0.10		
22,000	1.25 ±0.20	1.15.0.15			
33,000		1.15±0.15	1.15±0.15	1.25 ±0.20	
47,000		125 1020			
68,000		1.25 ±0.20	125 122		
100,000			1.25 ±0.20		1.15±0.15
150,000					
220,000					1.6 ±0.20
330,000					2.0 ±0.20
470,000					
680,000					
1,000,000					

NOTE

1. Values in shaded cells indicate thickness class in mm.

CAPACITANCE RANGE & THICKNESS FOR X7R 500 V

Table 7 For X7R 500 V sizes from 0805 to 1812

CAPACITANCE	500 V				
(pF)	0805	1206	1210	1808	1812
100					
150					
220					
330					
470					
680					
1,000	0.8 ±0.10				
1,500		1 15 10 15			
2,200		1.15±0.15			
3,300					
4,700			1.15±0.15		0.85 ±0.10
6,800					
10,000	1.25 ±0.20	1.25 ±0.20			1.15.0.15
15,000				1.25 ±0.20	1.15±0.15
22,000			1.25 ±0.20		
33,000		1.6 ±0.20			
47,000					1.25 ±0.20
68,000					
100,000					1.6 ±0.20
150,000					
220,000					
330,000					
470,000					
680,000					
1,000,000					

NOTE

1. Values in shaded cells indicate thickness class in mm.

THICKNESS CLASSES AND PACKING QUANTITY

YAGEO

Table 8							
DESCRIPTION	SIZE	THICKNESS			TH/AMOUNT		12 mm TAPE WIDTH
	CODE	CLASSIFICATION (mm)		80 mm, 7"		0 mm, 13"	/AMOUNT PER REEL
		()	Paper	Blister	Paper	Blister	Ø180 mm, 7" Blister
	0603	0.8 ±0.10	4,000				
	0805	0.6 ±0.10	4,000				
		0.8 ±0.10	4,000				
	_	0.85 ±0.1	4,000				
		1.25 ±0.20		3,000			
	1206	0.6 ±0.10	4,000		20,000		
	_	0.8 ±0.10	4,000				
	_	0.85 ±0.10	4,000		15,000		
	_	1.00 ±0.10		3,000		10,000	
	_	1.15 ±0.15		3,000		10,000	
	_	1.25 ±0.20		3,000			
	1210	0.6 ±0.10		4,000		15,000	
	_	0.85 ±0.10		4,000		10,000	
Mid / High	_	1.15 ±0.15		3,000		10,000	
voltage	_	1.25 ±0.20		3,000			
J		1.6 ±0.20		2,000			
	1808	1.15 ±0.15					1,500
	_	1.25 ±0.20					3,000
		1.35 ±0.15					1,000
	_	1.5 ±0.10					1,000
		1.6 ±0.20					2,000
		2.0 ±0.20					2,000
	1812	0.85 ±0.10					2,000
		1.15 ±0.15					1,500
		1.25 ±0.20					1,000
		1.35 ±0.15					1,000
		1.5 ±0.1					1,000
		1.6 ±0.2					1,000
		2.0 ±0.20					2,000



ELECTRICAL CHARACTERISTICS

NP0/X7R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise stated all electrical values apply at an ambient temperature of 20±1 °C, an atmospheric pressure of 86 to 106 kPa, and a relative humidity of 63 to 67%.

_	_			_
	a	b	le	9

DESCRIPTION	VALUE
Capacitance range (1)	10 pF to 470 nF
Capacitance tolerance (I):	
NP0	±5%
X7R	±10%
Dissipation factor (D.F.) (1):	
NP0	≤ 0.1%
X7R	≤ 2.5%
Insulation resistance after 1 minute at U _r (DC)	$R_{ins} \ge 10 \text{ G}\Omega$ or $R_{ins} \times C \ge 500$ seconds whichever is less
Maximum capacitance change as a function of temperature	
(temperature characteristic/coefficient):	
NP0	±30 ppm/°C
X7R	±15%
Operating temperature range:	
NP0/X7R	–55 °C to +125 °C

NOTE

^{1.} NP0: frequency = 1 MHz for C \leq 1 nF, measuring at voltage 1 V_{rms}; frequency = 1 KHz for C > 1 nF, measuring at voltage 1 V_{rms} X7R: frequency = 1 KHz for C \leq 10 μ F, measuring at voltage 1 V_{rms} .



TESTS AND REQUIREMENTS

Table 10 Test condition, procedure and requirements

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual inspection and dimension check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance		4.5.1	NP0: $f = 1 \text{ MHz for } C \le 1 \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C;} \\ f = 1 \text{ KHz for } C > 1 \text{ nF, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C} \\ \times 7R: \\ f = 1 \text{ KHz for } C \le 10 \mu\text{F, measuring at voltage } I \text{ V}_{rms} \text{ at } 20 \text{ °C}$	Within specified tolerance
Dissipation factor (D.F.)		4.5.2	NP0: f = I MHz for C \leq I nF, measuring at voltage I V _{rms} at 20 °C; f = I KHz for C $>$ I nF, measuring at voltage I V _{rms} at 20 °C \times 7R: f = I KHz for C \leq 10 μ F, measuring at voltage I V _{rms} at 20 °C	In accordance with specification
Insulation resistance		4.5.3	At U _r (DC) for I minute	In accordance with specification
Voltage proof		4.5.4.2	Test voltage (DC) applied for 1 minute $U_r \leq 100 \text{ V: } 2.5 \times U_r \text{ applied to NP0/X7R series}$ $100 \text{ V} < U_r \leq 200 \text{ V: } 1.5 \times U_r + 100 \text{ V applied to NP0/X7R series}$ $200 \text{ V} < U_r \leq 500 \text{ V: } 1.3 \times U_r + 100 \text{ V applied to NP0/X7R series}$ $U_r > 500 \text{ V: } 1.3 \times U_r \text{ applied to NP0/X7R series}$ $\text{I: } 7.5 \text{ mA}$	No breakdown or flashover
Temperature characteristic		4.6	Between minimum and maximum temperature	NP0: IΔC/Cl: 30 ppm/°C X7R: IΔC/Cl: 15%
Adhesion		4.15	A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate for size \geq 0603: a force of 5 N applied for size 0402: a force of 2.5 N applied	No visible damage

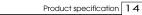


Table 10 Test condition, procedure and requirements (continued)

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS
Bond strength of plating on	IEC 60384- 21/22	4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage
end face			Conditions: bending I mm at a rate of I mm/s, radius jig 340 mm	NP0: $ \Delta C/C $: $\leq 1\%$ or 0.5 pF whichever is greater
				X7R: IΔC/Cl: ≤ 10%
Resistance to soldering heat	4.9		Precondition: $150 + 0/-10$ °C for I hour, then keep for 24 ±1 hours at room temperature Preheating: for size ≤ 1206 : 120 to 150 °C for I minute	The termination shall be well tinne NP0: I∆C/Cl: ≤ 0.5% or 0.5 pF whichever is greater
			Preheating: for size >1206: 100 to 120 °C for 1 minute	X7R: IΔC/Cl: ≤ 10%
			and 170 to 200 °C for 1 minute Solder bath temperature: 260 ±5 °C	D.F.: within initial specified value R _{ins} : within initial specified value
			Dipping time: 10 \pm 0.5 seconds Recovery time: 24 \pm 2 hours.	
Solderability		4.10	Unmounted chips completely immersed in a solder bath at 235 \pm 5 $^{\circ}\text{C}$	The termination shall be well tinned.
			Dipping time: 2 ± 0.5 seconds Depth of immersion: 10 mm	
Rapid change of temperature		4.11	Preconditioning; 150 +0/ -10 °C for 1 hour, then keep for 24 ± 1 hours at room temperature	No visual damage NP0: IΔC/CI: ≤ 1% or 1 pF whichever is greater X7R: IΔC/CI: ≤ 15% D.F.: within initial specified value R _{ins} : within initial specified value
			5 cycles with following detail: 30 minutes at lower category temperature; 30 minutes at upper category temperature	
			Recovery time 24 ±2 hours.	
Damp heat, with U _r load		4.13	Initial measurements; after 150 +0/-10 °C for 1 hour, then keep for 24 ± 1 hours at room temperature Duration and conditions: 500 ± 12 hours at 40 ± 2 °C;	NP0: IΔC/CI: ≤ 2% or 1 pF whichever is greater X7R: IΔC/CI: ≤ 15%
			90 to 95% RH; U_r applied Final measurement: perform a heat treatment at 150 +0/-10 °C for 1 hour, final measurements shall be carried out 24 ± 1 hours after recovery at room temperature without load.	NP0: D.F.: 2 × initial value max. X7R ≥ 100 V: D.F. ≤ 5%
				NP0: $R_{ins} \ge 2,500 \text{ M}\Omega \text{ or } R_{ins} \times C_r$ $\ge 25 \text{ seconds, whichever is less}$ X7R: $R_{ins} \ge 500 \text{ M}\Omega \text{ or } R_{ins} \times C_r \ge 25 \text{ seconds, whichever is less}$



Table 10 Test condition, procedure and requirements (continued)

	REQUIREMENTS	
Endurance IEC 60384- 4.14 Preconditioning; Initial measurements; after 150 +0/-10 °C for 1 hour, then keep for 24 ±1 hours at room temperature Duration and conditions: 1,000 ±12 hours at upper category temperature with 1.5 × U _r voltage applied Final measurement: perform a heat treatment at 150 +0/-10 °C for 1 hour, final measurements shall be carried out 24 ±1 hours after recovery at room temperature without load. NP0: R _{ins} ≥ 4,000 MΩ or 50 seconds, whichever is X 7R: R _{ins} ≥ 1,000 MΩ or 50 seconds, whichever is	e max. $^{\circ} R_{ins} \times C_r \ge $ less $^{\circ} R_{ins} \times C_r \ge $	



Product specification 16

Surface-Mount Ceramic Multilayer Capacitors | Mid-voltage | NP0/X7R | 100 V to 500 V

REVISION HISTORY

REVISION DATE CHANGE NOTIFICATION DESCRIPTION

Version 0 Sep 08, 2005 - - New

