# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

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CCPD-034 Model 5×7 mm SMD, 3.3V, LVPECL



## CCPD-034 5×7mm SMD LVPECL Clock Oscillator



Model CCPD-034 is a 162.000 MHz to 250.000 MHz LVPECL Clock Oscillator operating at 3.3 Volts. The oscillator utilizes a High Q Third Overtone crystal design providing very low Jitter and Phase Noise. No Sub-Harmonics are present in the Output Signal.



5×7mm SMD

## **Applications:**

Digital Video SONET/SDH/DWDM Storage Area Networks Broadband Access Ethernet, Gigabit Ethernet

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12730 Commonwealth Drive • Fort Myers, Florida 33913 Phone: 239-561-3311 • 800-237-3061 Fax: 239-561-1025 • www.crystek.com



### CCPD-034 5×7mm SMD LVPECL Clock Oscillator

#### CCPD-034 Model 5×7 mm SMD, 3.3V, LVPECL

Frequency Range: Frequency Stability Options (ppm): Temperature Range: (Option M) (Option X) Storage: Input Voltage: Input Current: Output: Symmetry: Piso/Fall Time:

162.000 MHz to 250.000 MHz ±20, ±25, ±50, ±100 (standard) 0°C to +70°C -20°C to +70°C -40°C to +85°C -45°C to 90°C 3.3V ±0.3V 55mA Typical, 88mA Max Differential LVPECL 45/55% Max @ 50% Vdd 1nsec Max @ 20% to 80% Vdd



 Symmetry:
 45/35 / 6 Ma

 Rise/Fall Time:
 1nsec Max

 Logic:
 Terminated to Vdd-2V into 50 Ω

Temp. 0°C to 85°C

Temp. -40°C to 0°C

РП

R

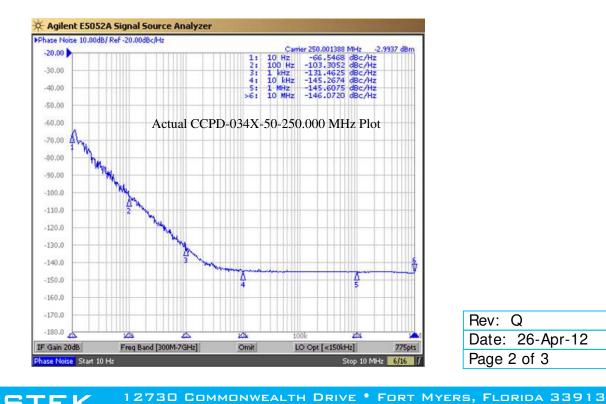
Disable Time: Enable Time: Phase Jitter: 12kHz~80MHz Phase Noise: (See Plot Below) Sub-harmonics: Aging: "0"=1.490 Min, 1.680 Max "1"=2.275 Min, 2.420 Max "0"=1.470 Min, 1.745 Max "1"=2.215 Min, 2.420 Max 200nSec Max 1mSec Typical, 2mSec Max 0.5psec Typical, 1psec RMS Max

None

<3ppm 1<sup>st</sup> year, <1ppm every year thereafter

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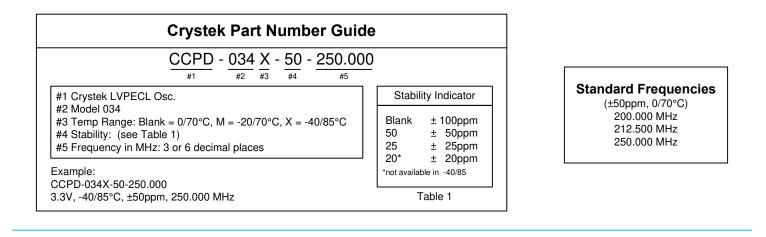






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**CCPD-034 Model** 5×7 mm SMD, 3.3V, LVPECL

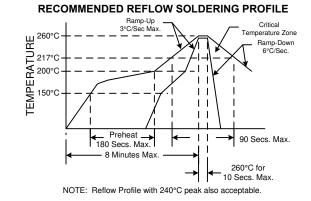


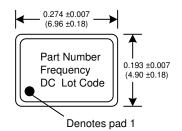
#### Mechanical:

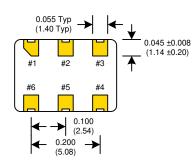
Shock:MIL-STD-883, Method 2002, Condition BSolderability:MIL-STD-883, Method 2003Vibration:MIL-STD-883, Method 2007, Condition ASolvent Resistance:MIL-STD-202, Method 215Resistance to Soldering Heat:MIL-STD-202, Method 210, Condition I or J

#### Environmental:

Thermal Shock: MIL-STD-883, Method 1011, Condition A Moisture Resistance: MIL-STD-883, Method 1004



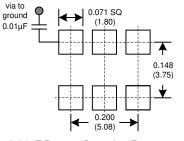




Dimensions inches (mm) All dimensions are Max unless otherwise specified.



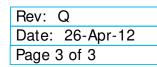
#### SUGGESTED PAD LAYOUT



0.01uF Bypass Capacitor Recommended

Tristate Function	
Function pin 1	Output pin
Open or N/C "1" level 0.7×Vdd Min "0" level 0.3×Vdd Max	Active Active High Z

PIN	Connection
1	Enable/Disable
2	N/C
3	GND
4	Output
5	Comp Output
6	Vcc





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