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RF SWITCH CG2214M6

L, S-band Medium Power SPDT Switch

DESCRIPTION

• The CG2214M6 is a pHEMT GaAs SPDT (<u>Single</u> <u>Pole</u> <u>Double</u> <u>Throw</u>) switch. This device can operate from 0.05 to 3.0 GHz, having low insertion loss and high isolation.

FEATURES

- Control voltage : VC(H) = 1.8 to 5.0 V (3.0 V TYP.) VC(L) = -0.2 to 0.2 V (0 V TYP.)
- High isolation : ISL1 = 38 dB TYP. @ f = 0.05 to 0.5 GHz ISL2 = 32 dB TYP. @ f = 0.5 to 1.0 GHz ISL3 = 27 dB TYP. @ f = 1.0 to 2.0 GHz ISL4 = 25 dB TYP. @ f = 2.0 to 2.5 GHz ISL5 = 23 dB TYP. @ f = 2.5 to 3.0 GHz
- Power handling : P_{in}(0.5dB) = +32 dBm TYP. @ f = 3.0 GHz VC(H) = 3.0 V, VC(L) = 0 V

ORDERING INFORMATION

PACKAGE

 6-pin lead-less mini mold package (1.5mm x 1.1mm x 0.55mm)

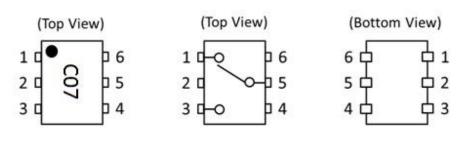


APPLICATIONS

- Wireless LAN (IEEE 802.11 b/g/n/ac)
- Bluetooth

Part Number	Order Number	Package	Marking	Description
CG2214M6	CG2214M6-C2	6-pin lead-less mini mold package (Pb-Free)	C07	 Embossed tape 8 mm wide Pin 1, 6 face the perforation side of the tape MOQ 9 kpcs/reel
CG2214M6-EVAL	CG2214M6-EVAL			 Evaluation Board with DC block capacitors, power supply bypass capacitors, and RF and DC connectors MOQ 1

PIN CONFIGURATION AND INTERNAL BLOCK DIAGRAM



Pin No.	Pin Name
1	RF1
2	GND
3	RF2
4	VC2
5	RFC
6	VC1

TRUTH TABLE

VC1	VC2	RFC-RF1	RFC-RF2
Low	High	ON	OFF
High	Low	OFF	ON

ABSOLUTE MAXIMUM RATINGS

(TA = +25°C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Control Voltage	VC	6.0 ^{Note 1}	V
Input Power	P _{in}	+33 ^{Note 2}	dBm
Operating Ambient Temperature	T _A	-45 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C

Note 1. $|VC1 - VC2| \leq 6.0 V$

2. 3.0V \leq |VC1 – VC2| \leq 5.0 V, f \geq 0.5 GHz

RECOMMENDED OPERATING RANGE

1	ΤΔ -	- +25°C	unlass	otherwise	specified	١
	1 ~ =	= +20 C,	uniess	otherwise	specified)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Operating Frequency	f	0.05	-	3.0	GHz
Switch Control Voltage (H)	VC(H)	+1.8	+3.0	+5.0	V
Switch Control Voltage (L)	VC(L)	-0.2	0	+0.2	V

This document is subject to change without notice.

ELECTRICAL CHARACTERISTICS

 $(TA = +25^{\circ}C, VC(H) = 3.0 V, VC(L) = 0 V, Zo = 50 \Omega, DC Block Capacitance = 56 pF, unless otherwise specified)$

Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Insertion Loss	L _{INS} 1	f=0.05 to 0.5GHz ^{Note 1}	-	0.30	0.50	dB
	L _{INS} 2	f=0.5 to 1.0GHz	-	0.30	0.50	dB
	L _{INS} 3	f=1.0 to 2.0GHz	-	0.30	0.50	dB
	L _{INS} 4	f=2.0 to 2.5GHz	-	0.35	0.55	dB
	L _{INS} 5	f=2.5 to 3.0GHz	-	0.35	0.55	dB
Isolation	ISL1	f=0.05 to 0.5GHz ^{Note 1}	35	38	-	dB
	ISL2	f=0.5 to 1.0GHz	29	32	-	dB
	ISL3	f=1.0 to 2.0GHz	24	27	-	dB
	ISL4	f=2.0 to 2.5GHz	22	25	-	dB
	ISL5	f=2.5 to 3.0GHz	20	23	-	dB
Return Loss	RL1	f=0.05 to 0.5GHz ^{Note 1}	15	20	-	dB
	RL2	f=0.5 to 3.0GHz	15	20	-	dB
0.1dB Loss Compression Input Power	P _{in(0.1dB)}	f=3.0GHz, VC(H)=1.8V, VC(L)=0V	-	+23	-	dBm
Note 2		f=3.0GHz, VC(H)=3.0V, VC(L)=0V	-	+30	-	dBm
0.5dB Loss Compression Input Power	P _{in(0.5dB)}	f=3.0GHz, VC(H)=1.8V, VC(L)=0V	-	+26	-	dBm
Note 3		f=3.0GHz, VC(H)=3.0V, VC(L)=0V	-	+32	-	dBm
2nd Harmonics	2f0	f=3.0GHz, P _{in} =+20dBm	-	-85	-	dBc
3rd Harmonics	3f0	f=3.0GHz, P _{in} =+20dBm	-	-85	-	dBc
3rd Order Input Intercept Point	IIP ₃	f=2.5GHz, 2-tone 1MHz Spacing	-	+58	-	dBm
Switch Control Current	I _{CONT}	RF none	-	1	10	uA
Switching Speed	t _{sw}	50% CTL to 90/10% RF	-	50	-	ns

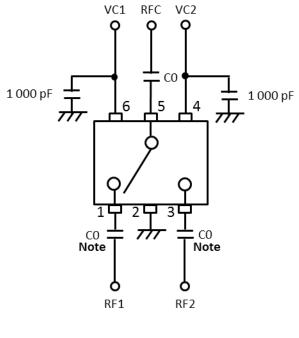
Note 1. DC block capacitance = 1000 pF at f = 0.05 to 0.5 GHz

2. P_{in(0.1dB)} is the measured input power level when the insertion loss increases 0.1dB more than that of the linear range.

3. P_{in(0.5dB)} is the measured input power level when the insertion loss increases 0.5dB more than that of the linear range

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EVALUATION CIRCUIT

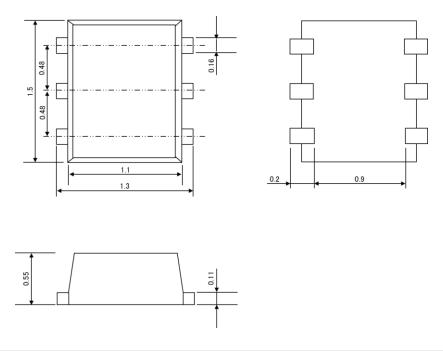


Note C0 : 0.05 to 0.5 GHz 1000pF : 0.5 to 3.0 GHz 56pF

The application circuits and their parameters are for reference only and are not intended for use in actual designs. DC Blocking Capacitors are required at all RF ports.

PACKAGE DIMENSIONS

6-pin lead-less mini mold package (Unit: mm)





RECOMMENDED SOLDERING CONDITIONS

Recommended Soldering Conditions are available on CEL's Part Summary page under Associated Documents



REVISION HISTORY

Version	Change to current version	Page(s)
CDS-0021-01 (Issue A) February 17, 2016	Initial datasheet	N/A
CDS-0021-02 (Issue B) March 29, 2016	Added Eval Board ordering information Updated marking information	1, 2
CDS-0021-03 (Issue C) April 20, 2016	Updated Features section	1
CDS-0021-03 (Issue D) August 11, 2016	Removed "preliminary"	All
CDS-0025-01 (Issue A) September 14, 2016	Revise CDS No. CDS-0021-03 to CDS-0025-01	N/A
CDS-0025-01 (Issue B) January 11, 2017	Revised Electrical Characteristics table Added "Recommended Soldering Conditions" section	3, 5



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